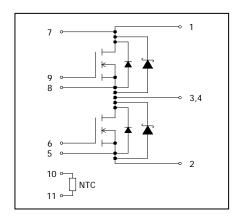
Application

- · Motor drive
- · Inverter, Converter
- · Photovoltaics, wind power generation.
- · Induction heating equipment.

Features

- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

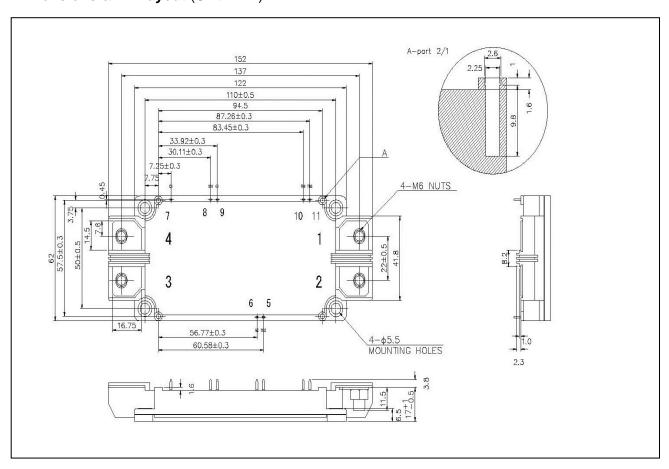
●Circuit diagram



Construction

This product is a half bridge module consisting of SiC-UMOSFET and SiC-SBD from ROHM.

● Dimensions & Pin layout (Unit : mm)



• Absolute maximum ratings $(T_j = 25^{\circ}C)$

Parameter	Symbol	Conditions	Limit	Unit	
Drain-source voltage	V_{DSS}	G-S short	1200		
Gate-source voltage(+)	W	D-S short	22	V	
Gate-source voltage(-)	V_{GSS}	D-2 2UOIT	-4		
Drain current *1	I _D	DC (T _c =60°C) V _{GS} =18V	576		
	I _D	DC (T _c =50°C) V _{GS} =18V	600		
	I _{DRM}	Pulse (T_c =60°C) 1ms V_{GS} =18V * ²	1200	- A	
Source current *1	I _S	DC (T _c =60°C) V _{GS} =18V	576		
	Is	DC (T _c =50°C) V _{GS} =18V	600		
	Is	DC (T _c =60°C) V _{GS} =0V	418		
	I _{SRM}	Pulse (Tc=60°C) 1ms V _{GS} =18V * ²	1200		
	I _{SRM}	Pulse (Tc=60°C) 10μs V _{GS} =0V * ²	1200		
Total power disspation *3	Ptot	T _c =25°C	2450	W	
Max Junction Temperature	T _{jmax}		175		
Junction temperature	T _{jop}		-40 to150	°C	
Storage temperature	T _{stg}		-40 to125		
Isolation voltage	Visol	Terminals to baseplate, f=60Hz AC 1min. 2500		Vrms	
Mounting torque		Main Terminals : M6 screw	4.5	NI m=	
	_	Mounting to heat shink: M5 screw	3.5	N·m	

^(*1) Case temperature ($T_{\rm c}$) is defined on the surface of base plate just under the chips.

^(*2) Repetition rate should be kept within the range where temperature rise if die should not exceed T_{j max}.

^(*3) T_j is less than 175°C

●Electrical characteristics (T_i=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Static drain-source on-state voltage	$V_{DS(on)}$	I _C =600A, V _{GS} =18V	T _j =25°C	-	1.8	2.4	V
			T _j =125°C	-	2.6	-	
			T _j =150°C	-	2.9	4.1	
Drain cutoff current	I _{DSS}	V _{DS} =1200V, V _{GS} =0V		-	-	4	mA
Souce-Drain Voltage	V_{SD}	V _{GS} =0V, I _S =600A	T _j =25°C	ı	2	2.9	V
			T _j =125°C	-	2.6	-	
			T _j =150°C	-	2.7	4.6	
		V _{GS} =18V, I _S =600A	T _j =25°C	-	1.4	-	V
			T _j =125°C	-	1.7	-	
			T _j =150°C	ı	1.9	-	
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} =10V, I_{D} =182mA		2.7	-	5.6	V
Gate-source leakage current	I _{GSS}	V _{GS} =22V, V _{DS} =0V		ı	-	0.5	μΑ
		$V_{GS} = -4V, V_{DS} = 0V$		-0.5	-	-	
Switching characteristics	t _{d(on)}	$V_{GS(on)}$ =18V, $V_{GS(off)}$ = -2V * ⁴		-	60	-	ns
	t _r	V _{DS} =600V	-	70	-		
	t _{rr}	I _D =600A	-	45	-		
	t _{d(off)}	$R_{G(on)}$ =1.8 Ω , $R_{G(off)}$ =1.8 Ω		-	320	-	
	t _f	inductive load	-	65	-		
Input capacitance	Ciss	V _{DS} =10V, V _{GS} =0V,200kHz		-	31	-	nF
Gate Registance	R _{Gint}	T _j =25°C		-	1.4	-	Ω
NTC Rated Resistance	R25	-		-	5.0	-	kΩ
NTC B Value	B _{50/25}			-	3370	-	K
Stray Inductance	Ls			-	10.0	-	nΗ
Creepage Distance	-	Terminal to heat sink		-	16.7	-	mm
		Terminal to terminal		-	16.7	-	mm
Clearance Distance	-	Terminal to heat sink		-	12.0	-	mm
		Terminal to terminal		-	11.0	-	mm
Junction-to-case thermal resistance	R _{th} (j-c)	UMOS (1/2 module) *5		-	-	61	°C/kW
		SBD (1/2 module) *5		-	-	80	
Case-to-heat sink Thermal resistance	R _{th} (c-f)	Case to heat sink, per 1 module, Thermal grease appied *6		-	15	-	
(*4) In order to provent colf turn		recommended to apple					

- (*4) In order to prevent self turn-on, it is recommended to apply negative gate bias.
- (*5) Measurement of Tc is to be done at the point just under the chip.
- (*6) Typical value is measured by using thermally conductive grease of $\lambda=0.9W/(m\cdot K)$.
- (*7) SiC devices have lower short cuicuit withstand capability due to high current density. Please be advised to pay careful attention to short cuicuit accident and try to adjust protection time to shutdown them as short as possible.
- (*8) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.

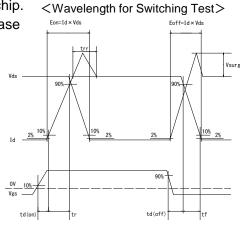
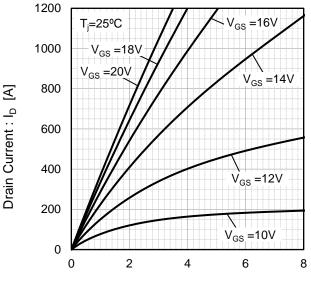
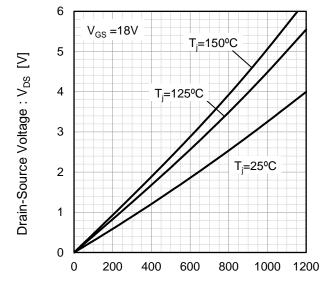


Fig.1 Typical Output Characteristics [T_j =25°C] Fig.2 Drain-Source Voltage vs. Drain Current



Drain-Source Voltage : V_{DS} [V]



Drain Current : I_D [A]

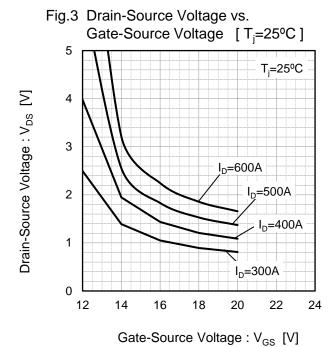
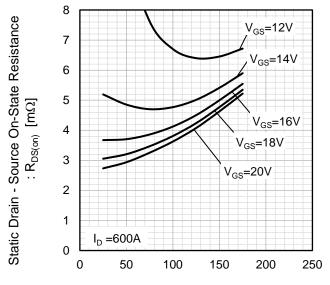
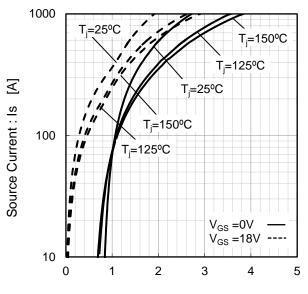


Fig.4 Static Drain - Source On-State Resistance vs. Junction Temperature



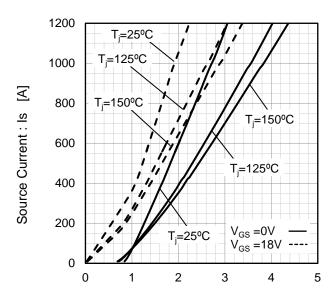
Junction Temperature : T_i [°C]

Fig.5 Forward characteristic of Diode



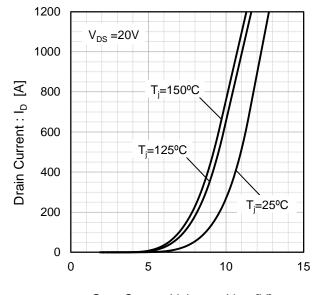
Source-Drain Voltage: V_{SD} [V]

Fig.6 Forward characteristic of Diode



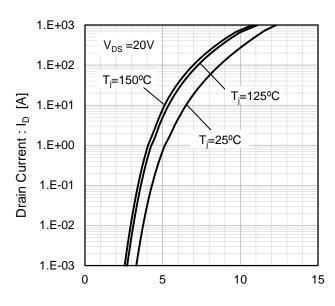
Source-Drain Voltage: V_{SD} [V]

Fig.7 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

Fig.8 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

Fig.9 Switching Characteristics [T_i=25°C]

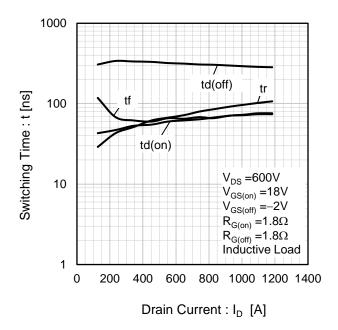
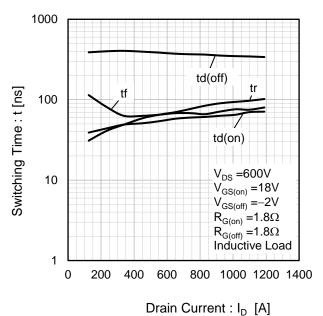


Fig.10 Switching Characteristics [T_i=125°C]



Diam Current in [A

Fig.11 Switching Characteristics [T_i=150°C]

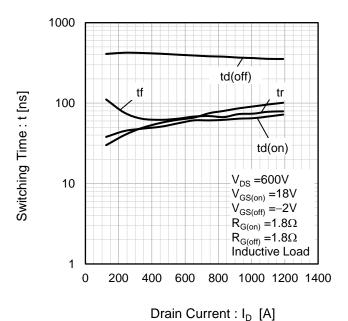
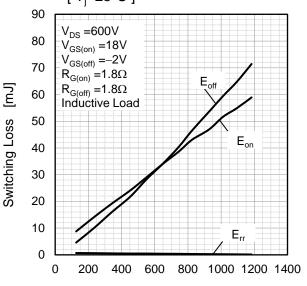
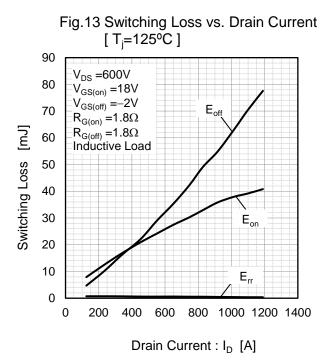
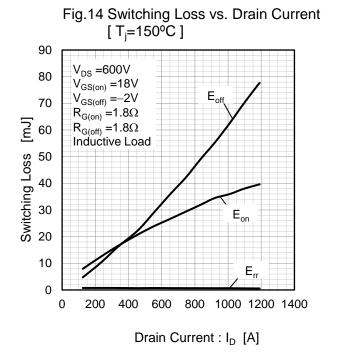


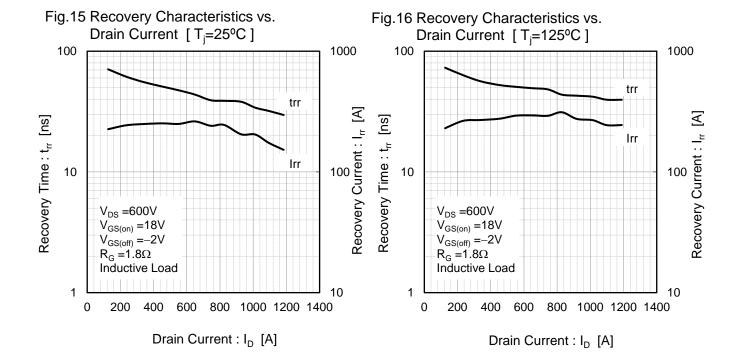
Fig.12 Switching Loss vs. Drain Current [$T_i=25^{\circ}C$]



Drain Current : I_D [A]







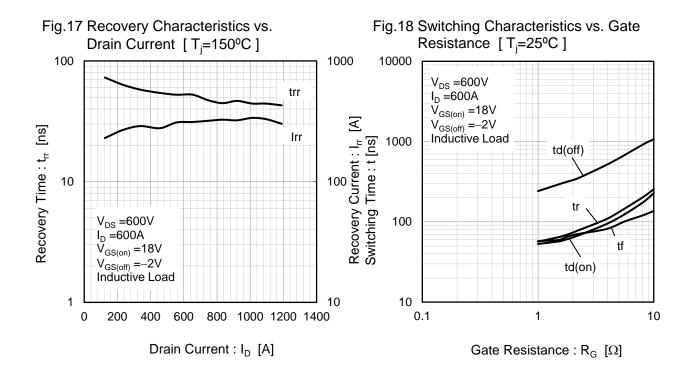


Fig.19 Switching Characteristics vs. Gate Resistance [T_i=125°C]

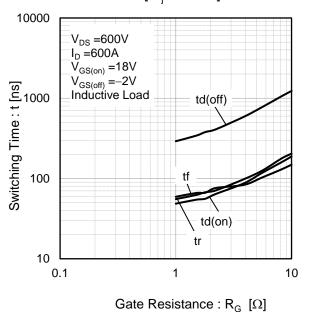
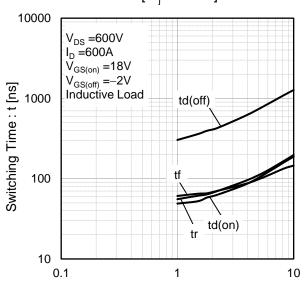


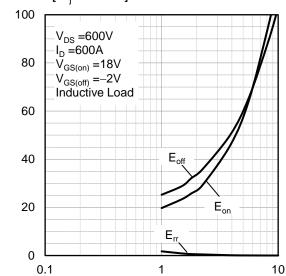
Fig.20 Switching Characteristics vs. Gate Resistance [T_i=150°C]



Gate Resistance : R_G [Ω]

Fig.21 Switching Loss vs. Gate Resistance $[T_i=25^{\circ}C]$ 100 V_{DS} =600V I_D =600A $V_{GS(on)} = 18V$ $V_{GS(off)} = -2V$ Inductive Load 80 Switching Loss [mJ] 60 40 $\mathsf{E}_{\mathsf{off}}$ 20 0 0.1 1 10

Fig.22 Switching Loss vs. Gate Resistance [T_i=125°C]



Switching Loss [mJ]

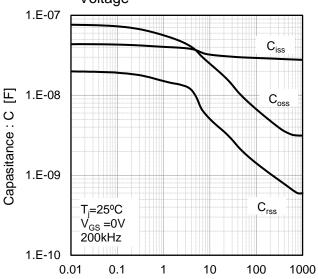
Gate Resistance : R_G [Ω]

Gate Resistance : R_G [Ω]

Fig.23 Switching Loss vs. Gate Resistance $[T_i=150^{\circ}C]$ 100 V_{DS} =600V $I_{D} = 600A$ 80 $\bar{V}_{GS(on)} = 18V$ V_{GS(off)} =-2V Inductive Load Switching Loss [mJ] 60 40 E_{on} 20 0 0.1 1 10

Gate Resistance : R_G [Ω]

Fig.24 Typical Capacitance vs. Drain-Source Voltage



Drain-Source Voltage: V_{DS} [V]

Fig.25 Gate Charge Characteristics

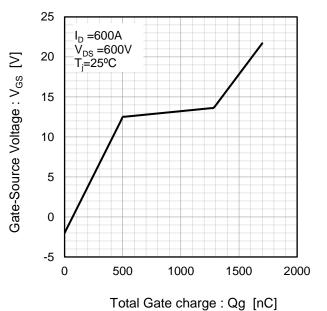


Fig.26 Normalized Transient Thermal Impedance Normalized Transient Thermal Impedance: Zth 0.1 Single Pulse $T_c=25$ °C Per unit base DMOS part: 61°C/kW SBD part : 80°C/kW 0.01 0.0001 0.001 0.01 0.1 1 10

Time [s]

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Minimum Package Quantity	4
Packing Type	
Constitution Materials List	inquiry
RoHS	Yes