

Hosted by Prof. Karl Zeilhofer
4ABHEL, 2020/2021

HTL-Steyr

Sheet: /Buttons/
File: buttons.sch

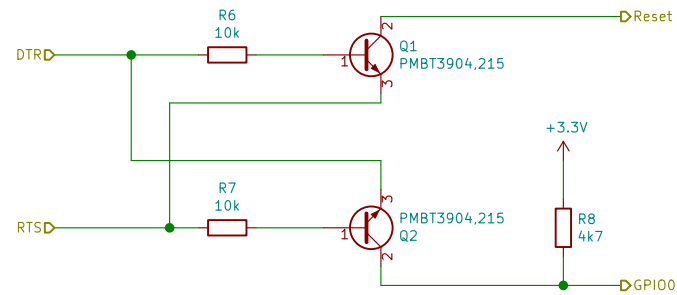
Title: Gamearino

Size: A4 Date: 2021-04-29
KiCad E.D.A. kicad_ppa 5.1.9-73d0e3b20d88ubuntu20.04.1

Rev: V0.0.2
Id: 2/5

Wahrheitstabelle:

DTR	RTS	RST	GPIO0
LO	LO	HIZ	HI
LO	HI	HIZ	LO
HI	LO	LO	HI
HI	HI	HI	HI



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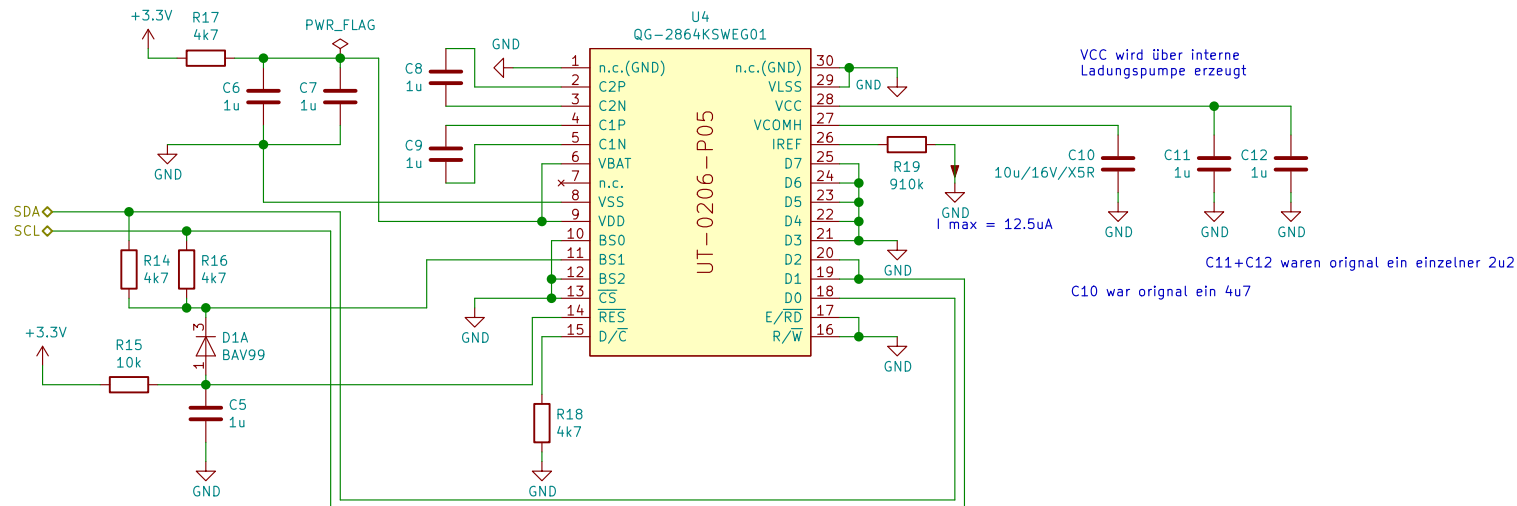
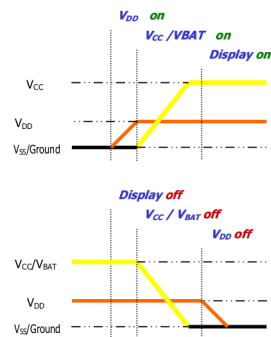
Sheet: /Auto–Reset/
File: autoreset.sch

Title: Gamearino

Size: A4	Date: 2021–04–29	Rev: V0.0.2
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10

- 1) Since an ESD protection circuit is connected between V_{DD} and V_{CC} inside the driver IC, V_{CC} becomes lower than V_{DD} whenever V_{DD} is ON and V_{CC} is OFF.
- 2) V_{CC} / V_{BAT} should be kept float (disable) when it is OFF.
- 3) Power Pins (V_{DD} , V_{CC} , V_{BAT}) can never be pulled to ground under any circumstance.
- 4) V_{DD} should not be power down before V_{CC} / V_{BAT} power down.



TODO: Poti für Helligkeit

VCC wird über interne
Ladungspumpe erzeugt

C11+C12 waren original ein einzelner 2u2
C10 war original ein 4u7

Rev: V0.0.2
Id: 5/5