

#### 4.2 Power down and Power up Sequence

To protect OEL panel and extend the panel life time, the driver IC power up/down routine should include a delay period between high voltage and low voltage power sources during turn on/off. It gives the OEL panel enough time to complete the action of charge and discharge before/after the operation.

#### 4.2.1 Power up Sequence:

- Power up V<sub>DD</sub> / V<sub>BAT</sub>
  Send Display off command
- 3. Initialization
- 4. Clear Screen
- 5. Power up V<sub>CC</sub>
- 6. Delay 100ms
- (When V<sub>CC</sub> is stable)
- 7. Send Display on command

### 4.2.2 Power down Sequence:

- 1. Send Display off command
- 2. Power down V<sub>CC</sub> / V<sub>BAT</sub>
- 3. Delay 100ms (When  $V_{CC}$  /  $V_{BAT}$  is reach 0 and panel is completely discharges)
- 4. Power down V<sub>DD</sub>



Vcc /VBAT on

Display on

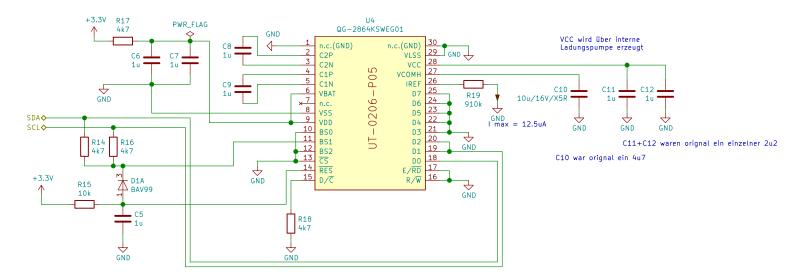
#### Note 13:

1) Since an ESD protection circuit is connected between  $V_{DD}$  and  $V_{CC}$  inside the driver IC,  $V_{CC}$ becomes lower than V<sub>DD</sub> whenever V<sub>DD</sub> is ON and V<sub>CC</sub> is OFF.

Vss/Ground

- 2) V<sub>CC</sub> / V<sub>BAT</sub> should be kept float (disable) when it is OFF.
- 3) Power Pins (V<sub>DD</sub>, V<sub>CC</sub>, V<sub>BAT</sub>) can never be pulled to ground under any circumstance.
- 4) V<sub>DD</sub> should not be power down before V<sub>CC</sub> / V<sub>BAT</sub> power down.

TODO: Poti für Helligkeit



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## HTL-Steyr

Sheet: /OLED 128x64/ File: oled.sch

# Title: Gamearino

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