# VLADISLAV VASILEVSKIY

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### GitHub

### **EDUCATION**

MIPT(Moscow Institute of Physics and Technology) 2022 - 2026 Current GPA: 7/10

## Coursework

Main Courses: Discrete Math, Linear Algebra, Higher mathematics, Physics, Computer technologies, Basics of radio engineering, Fundamentals of digital electronics

### TECHNICAL STRENGTHS

HDL Verilog / SystemVerilog

Computer Languages C/C++, Python, ARM Assembly

Languages English(B2), Belarussia(Native), Russian(Native)

Software & Tools VS Code, Linux, Quartus, IcarusVerilog, GTKWave, Latex, Git/GitHub

## WORKING EXPERIENCE

internship in the company "MCST" "Microarchitecture of computing devices" program

(01.07.2024 - 30.08.2024) project link

## ADDITIONAL ACHIEVEMENTS

Third place on the "SoC design challenge 2024" RTL design track passed The school of synthesis of digital systems 2023-2024