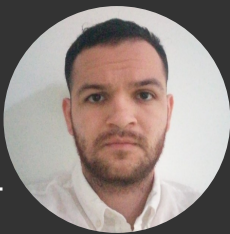


ENG. RODOLFO ROJAS OCAMPO



I am a proactive person, I like to learn new skills and I learn very quickly.
I seek to put my knowledge and skills to the test and contribute as much as possible.
My interests are fields related to technology. I am looking for a new experience with challenges, which is set in technology.

CONTACT

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 7 0239 0149

SKILLS

Programming

Python

SQL

VBA

JavaScript

C++

LaTeX

Verilog

Operating Systems

Linux

Windows

Software & Tools

OpenDSS

Office

AutoCAD

MS Project

Languages

Spanish

English

Italian

Portuguese

WORK HISTORY

11/2017 - 12/2021 EATON Electrical SRL, Moravia, Costa Rica	Project Engineering
01/2022 - Currently Intel, Heredia, Costa Rica	Project Developer Engineering

EDUCATION

01/2019 - 12/2018 University of Costa Rica, San Pedro	Bachelor of Electrical Engineering
01/2019 - 08/2021 University of Costa Rica, San Pedro	Licenciate of Electrical Engineering
01/2021 - 06/2022 University for International Collaboration, Costa Rica	Master of Project Management
08/2021 - Currently Distance State University, Costa Rica	Bachelor of Computer Engineering

ACHIEVEMENTS

- Participation in EPER-Lab, Electric Power & Energy Research Laboratory
- Project: Algorithm for detecting faults in electrical distribution grids with smart meters. Use of python and a simulation in OpenDSS, the solution use K-Means and Pearson's Correlation
- Project: Algorithm and Study of types of failures in Solar Panels. Use of subjects like solar panels, artificial intelligence and Convolutional Neural Network
- Automatization of Excel's price sheet for Eaton's Quotations.
- Design and implementation of a security equipment for bicycles with electronic.
- Design and implementation of a music speaker with electronic.
- Macros on Outlook for improve the process.

GENERAL SKILLS

Teamwork

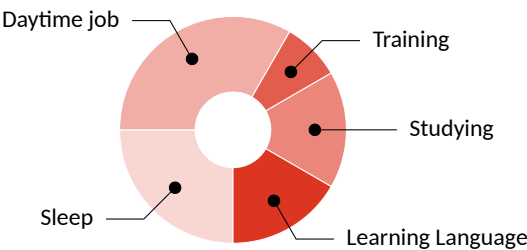
Flexibility

Analysis and problem solving

Adaptation

Autodidact

WHEEL CHART



EXPERIENCE

Python Development

In my project to obtain the bachelor's degree, the project was carried out using **Python**, OpenDss was connected and it was administered with python code. The **Numpy** and **Pandas** packages were used for the statistical part of the project.

For Eaton I created an interface in **Tkinter** with a database in SQL for the search and generation of prices for the different catalogs that are sold.

Besides this I did a small **Django** course and I practice problem solving on HackerRank. For the future I wish to improve skills in **TensorFlow**, the Licenciante's degree project used **CNN**.

Circuit Desing

For the design of electronic circuits I took four courses at the university, where I learned about semiconductor diodes and applications, transistors, operation, polarization, amplification BJT, JFET, MOSFET, IGBT. Analysis and design of circuits with operational amplifiers. Here I took two laboratory courses, as well as two design and implementation projects. Using the tools of TINA, Proteus for the circuit design and KiCAD for the PCB. Now I am looking to start a personal project in the design of different speakers.

Computer Architecture

In this field I took two courses, where topics such as Numbering and Digital Codes, Boolean Algebra, CMOS logic, Karnaugh Maps, Iterative Networks, Flip Flops, State Machines, MIPS Assembly Language, Datapath in a RISC processor and Storage systems were seen. . Now I am reading the book CMOS VLSI DESIGN A CIRCUITS AND SYSTEMS PERSPECTIVE, to enter the world of Verilog. I hope to delve more into these subjects by taking the computer science career.

Project Engineering

Interpret electrical drawings of commercial, residential and industrial buildings. Technical knowledge of the brand's equipment and provide valuable solutions and engineering.

Project Developer Engineering

Responsible for ensuring the testability and manufacturability of integrated circuits from the component feasibility stage through production ramp. Make significant contributions to design, development and validation of testability circuits. Evaluation, development and debug of complex test methods. Develops and debugs complex software programs to convert design validation vectors and drive complex test equipment. Creates and tests validation and production test hardware solutions. Tests, validates, modifies and redesigns circuits to guarantee component margin to specification. Analyzes and evaluates component specification versus performance to ensure optimal match of component requirements with production equipment capability with specific emphasis on yield analysis and bin split capability. Analyzes early customer returns with emphasis on driving test hole closure activities. Creates and applies concepts for optimizing component production relative to both quality and cost constraints. Autonomously plans and schedules own daily tasks, develops solutions to problems utilizing formal education and judgment.