**Part A**

**Objective**

Understand various logic gates

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| 1. Complete questions below |
| |  |  | | --- | --- | | A  B |  | | C |  | | D |  | | E | Draw a logic Gate diagram for De Morgan’s Law. | | F | Draw a Transistor Gate diagram for De Morgan’s Law. Why is this an important law. | |

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| 1. Complete questions below |
| Using logic.ly construct the following Circuit using NAND and NOT Gates (With a Light at Output) and complete Truth Tables  https://adamwsonu.files.wordpress.com/2010/03/quizcircuit.png |

**Hand up this practical report at the end of session and ensure it has been checked**

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| **Student Name** |  | **Student Number** |  |
| **Date** |  | **Checked** |  |
| **Group** | **A / B** |  |  |