Top Row

RJ45 Pin	Function	Port 1 Pyboard Pin Name	Port 1 Interrupt Vector	Port 2 Pyboard Pin Name	Port 2 Interrupt Vector	Port 3 Pyboard Pin Name	Port 3 Interrupt Vector	Port 4 Pyboard Pin Name	Port 4 Interrupt Vector	Port 5 Pyboard Pin Name	Port 5 Interrupt Vector	Port 6 Pyboard Pin Name	Port 6 Interrupt Vector
1	DIO_A	W53~	2	W47~	5	W45	12	W10~	6	W58	11	W65	8
4	DIO_B	W57~	3	W51~	4	W43~	0	W68	10	W56	14	W71	15
5	DIO_C	W61	7	W49	13	W24~	1	W66	9				
3	POW_A	W16		W60		W32		W25		W64		W27	
7	POW_B	W50		W22		W30		W23		W62		W29	

Bottom Row

RJ45 Pin	Function	Port 7 Pyboard Pin Name	Port 7 Interrupt Vector	Port 7 Peripherals	Port 8 Pyboard Pin Name	Port 8 Interrupt Vector	Port 8 Peripherals	Port 9 Pyboard Pin Name	Port 9 Interrupt Vector	Port 10 Pyboard Pin Name	Port 10 Interrupt Vector	Port 10 Peripherals	Port 11 Pyboard Pin Name	Port 11 Interrupt Vector	Port 11 Peripherals	Port 12 Pyboard Pin Name	Port 12 Interrupt Vector	Port 12 Peripherals
1	DIO_A	W59	8	I^2C (1) - SCL	W12	10	I^2C (2) - SCL / UART (3) - TX	W63~	1	W19~	0	UART (4) - TX	W15~	2	UART (2) - TX	W54	9	UART (1) - TX
4	DIO_B	W55	9	I^2C (1) - SDA	W8	11	I^2C (2) - SDA / UART (3) - RX	W14~	7	W17~	1	UART (4) - RX	W11~	3	UART (2) - RX	W52	10	UART (1) - RX
5	DIO_C	W7~	4	DAC (1)	W6~	5	DAC (2)											
3	POW_A	W46			W20			W28		W9			W73			W74		
7	POW_B	W18			W26			W34		W3			W5			W33		

~ ADC

* User button is W67, interrupt vector 13