

User Guide

for

PEAK PROGRAMME METER

177-800C

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Introduktion

The Peak Programme Meter, type 177-800C, is designed primarily for recording and broadcasting studios. The instrument which is designed to fulfil the stringest international standards such as DIN45406 and IEC268-10, features:

- High impedance balanced transformer inputs.
- Very close tracking between the two channels and any number of identical peak programme meters.
- A digital memory for storing the highest peak value of the programme monitored.

Furthermore the 177-800C has numerous external functions:

- Additional gain.
- Display of stored peak value.
- Clearing of the memory.
- Fast integration time.
- Optional scale lines.
- Brightness control.
- Light emitting diode for overload indication.

These functions can be accessed out by making connections on the two edge connectors placed on the rearside, to which also power supply and input signals are fed. (See drawing 177-8002-A-4)

Construction & Mode of Operation

The Stereo peak Programme Meter, housed in a cabinet 165x40x90mm, consists of three units named Input Board, Driver Board and Mother Board.

The 177-800C makes used of a bar-graph display, which is a gas discharge indicator containing two separate bar-graph each composed of 200 closely spaced segments, orange glowing, providing a 0.5% resolution.

The segments are illuminated by using the "glow transfer principle" in which the glow is first established at the reset cathode and then, by using a repetitive scan, the glow is transferred sequentially up to the desired point of the display

Since the cathodes of both bars are connected together, a common drive circuit, consisting of a five-phase clock with a sixth reset phase, is used to control the transfer of glow along the panel.

The cathode drive circuit scans the entire array of 200 segments continuously and when the bar has reached the desired length, the anode voltage is turned off.

The signal from a ramp generator, starting from zero and reaching its maximum at 200, is fed to the two comparators (one for each channel) whose outputs are connected to the anode drivers (one for each channel). The extern signals to be monitored are fed to the other input on the comparator. When the ramp voltage reaches the level of the input voltage, the anode driver will be turned off and the glow will extinguish.

Description of the Block Diagram

Since the block diagram to a large extent is selfexplanatory, the following remarks are intended as a guide to the use of the diagram.

The <u>Input Board</u> comprises two identical circuits, one for each channel, for which reason only one channel is shown.

The input signal is fed to a input stage consisting of a current transformer followed by an amplifier in which the gain can be increased 20dB by connecting the wire "+20dB" to ground.

Then follows a low-pass filter which together with a third pole filter in the preceding stage provides a 18dB/octave roll-off at high frequencies.



The signal is then fed through a full-wave rectifier supplying current for the log-converter which generates a DC voltage corresponding to the logarithm of the input signal.

This voltage is fed to a circuit giving the standardized integration and fall-back time.

The integration time can be changed to "fast" by connecting the wire "Fast" to the supply voltage.

The output amplifier serves as buffer and gives the right slope dB/V and the reference of the output signal.

The output signal is fed directly to the driver board. When the wire "Display Peak" is connected to ground the switch changes position and the output signal is now fetched from the peak storage circuit.

The peak storage circuit consists of a comporator, a counter and 8-bit D/A converter forming a memory for the highest voltage peaks supplied from the log.-amplifier.

When the wire "Reset" is connected to ground the memory is cleared.

The <u>Driver Board</u> comprises the power supply for the circuits in the instrument and the driver circuitry required for the bar-graph display.

The power supply consists of three parts: A DC/DC converter generating the high voltage for the anodes and two 3-terminal regulators supplying +12V DC and +5V DC.

The bar-graph drive circuitry is controlled from the clock generator.

Each element in the bar-graph represents a discrete, reproducible display step which causes that each segment of the display is directly relatable to a digital number.

To initiate a scan, the reset cathode is switched to ground by turning the transistor associated on.

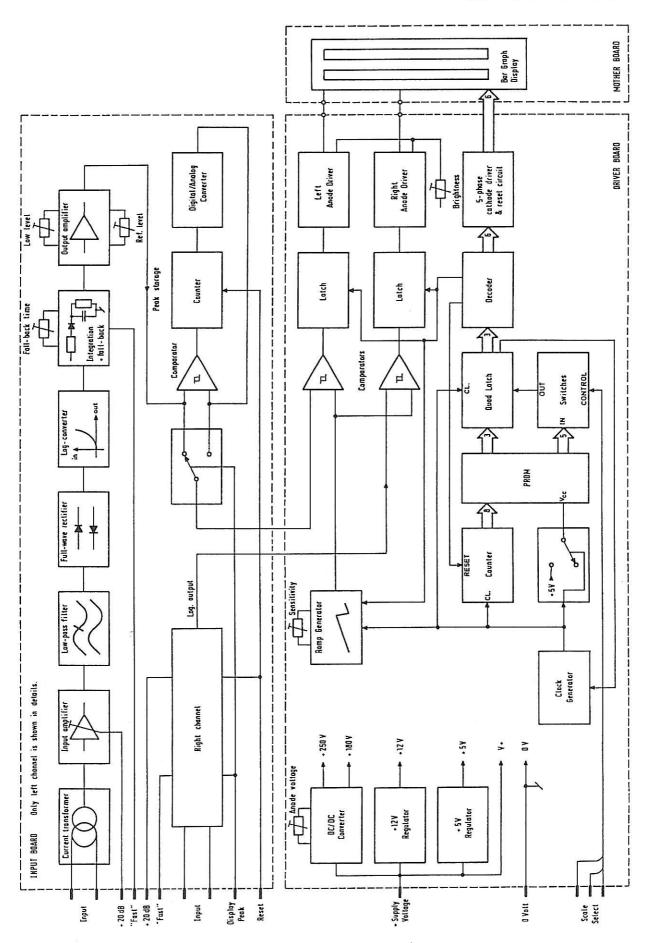
As the counter advances the cathodes are sequentially grounded, causing the glow to transfer along the bar. The intensified scale marks are stored in a Programmable Read Only Memory (PROM). This factory programmed memory may also contain alternative scale patterns. These can be selected by grounding one or more of the wires named "Scale Select".

In order to keep the current consumption of the instrument low, the supply voltage is only applied the PROM when read out is executed.

The two comparators control the anode drivers (one for each bar).

When the voltage from the ramp generator reaches the level of the voltage coming from the input board, the comparators will turn the anode voltage off causing the glow to extinguish.







General Specifications

Supply voltage Current consumption Temperature range 22-32V DC approx. 130m/

approx. 130mA at 24V supply 0 to 45°C ambient temperature

Input

Frequency range, 0.5dB point High frequency roll-off

20 Hz to 16kHz at 25kHz greater than 7dB at 40kHz greater than 20dB $20k\Omega\pm10\%$, balanced, floating 1.55V rms sine (+6dBu) 8.6V rms sine (+21dBu)

Input impedance
Input voltage for 0dB reading

Input overload level Dynamic measuring range

55dB

Measuring Errors

IkHz steady signal, 25°C Within full frequency range, 25°C Within full temperature range, 1kHz Polarity shift of unsymmetrical wave 10% change of supply voltage Tracking between channels

 $\begin{array}{lll} at +5 \text{ to } -10 \text{dB} & \text{below } -10 \text{dB} \\ \pm 0.5 \text{dB} & \pm 1 \text{dB} \\ +0.5 / -1 \text{dB} & \pm 0.5 / -2 \text{dB} \\ \pm 1 \text{dB} & \pm 2 \text{dB} \\ \pm 0.5 \text{dB} & \pm 1 \text{dB} \\ \pm 0.2 \text{dB} & \pm 0.2 \text{dB} \\ \end{array}$

Integration & Fall-back Time

Integration time Conforming to DIN45406 and IEC268-10 Integration time is measured with 5kHz tonebursts 10msec for -1dB \pm 0.5dB 5msec for -2dB \pm 1dB 3msec for -4dB \pm 1dB 0.4msec for -15dB \pm 2dB

Fall-back time, with linear scale Fall-back time with scale according to DIN45406. Conforms with IRT-ELA KE/Mr 4.5.70.

1.5sec for 0 to -20dB

Peak Store

Accuracy of peak storing ("Memory") in upper end of scale, above -30dB reading in lower end of scale, below -30dB reading

 ± 1 neonsegment or $\pm 0.25 dB$ +2/-1 neonsegment or $\pm 1 dB$

External Functions

(Available when making connections externally) Additional gain, scales according to DIN45406

"Display Peak"
"Reset"
"Fast" gives an integration time
Overload LED's

+40dB ±1dB for "Nordic" scales Displays peak storing Clears the memory 100usec for -1dB reading

Light Emitting Diodes placed above the bar-graph

Brightness control Scale select

Optional scale lines

+20dB ±0.5dB



General Data

Standard scales

DIN +5 to -50dB Nordic +9 to -36dB "BBC" 1 to 7 (4=0.775V)

All types are available for horizontal or vertical mounting.

Number of single elements in the bar-graph

Overload and scale lines indication

Connector

200 in each channel

4 times increase of light intensity

2 10-pole edge connector

Mechanical Data

The instrument is housed in a cabinet

 Height
 160mm

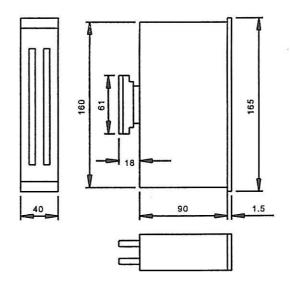
 Weidth
 40mm

 Depth
 90mm

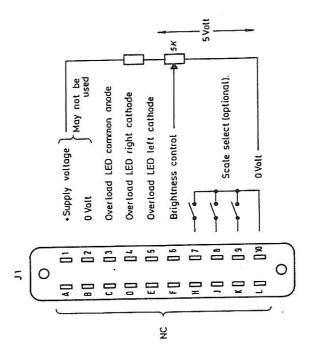
 Weight
 0.4kg

 Total scale length
 128mm

Mechanical Outline:







The two 20-pole connectors seen from the rearside (solderside).

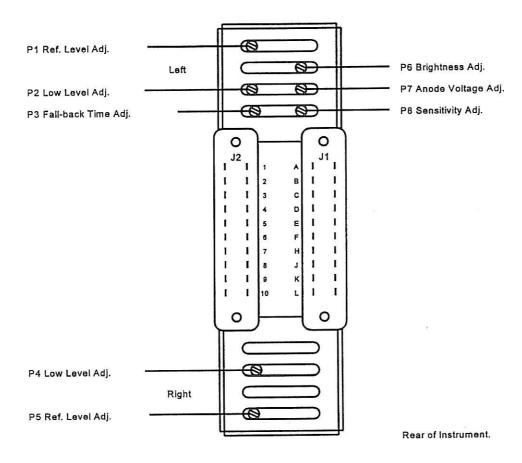
	NC .	NC	o "Fast", left	* Reset	* Display peak	UV	NC	a "Fast", right	NC	NC	
~ (5	13	<u> </u>	ð	0 5	0	0	<u></u>	6	0.0	
20	_ A_	8	0	0	E []	묘	B #	=	Ā	0-1	ل
[™] \ \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\							<i>ـــ</i>		_		
	 Supply voltage 	0 Volt	Chassis	* +20 dB gain left	* +20 dB gain right		Input left	NC	2	Input right	

* The function is established when the pin is connected to 0 Volt.

□ The function is established when the pin is connected to + Supply voltage.



Since the instrument is adjusted correctly on delivery, adjustment only has to be carried out in case of faulty mode of operation i.e. when a component has failed and has been replaced.



Test Set-up.

- 1. Connect +24V DC to pin A on J2 (+ Volt to pin B on J2).
- 2. Feed a signal e.g. 5kHz sinusoidal to the input terminals for both channels i.e. pin F and H as left input and pin K and L as right input. Adjust amplitude of the signal for max. reading on the display (Top of the scales).

Anode Voltage Adjust.

- 1. Adjust P6 for max. brightness (max. CW).
- 2. Turn P7 slowly CCW, as far as possible without getting a flickering display.
- 3. Measure the current consumption.
- 4. Adjust P7 to obtain an increase in the current consumption of approx. 15mA.

Brightness Adjustment.

- Turn P6 max. CCW (min. brightness) and then slowly CW in order to obtain a uniform glow in all segments.
- 2. Measure the current consumption.
- 3. Adjust P6 to obtain an increase in the current consumption of approx. 40mA (or to desired brightness).



Voltmeter Sensitivity Adjustment.

- 1. Remove the instrument from the cabinet by following the drawing 177-8020-C-4, "Disassemb ling".
 - CAUSION! Become aware of that when the supply voltage is applied high voltage (250V DC) is on the circuits.
- 2. Connect a voltmeter to the wiper (center pin) on P8. The voltage should be 3V. If not, adjust P8

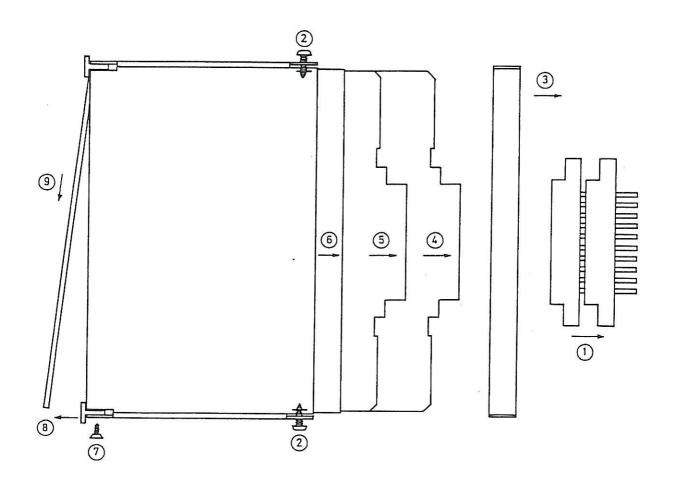
Low Level & Ref. Level Adjustment.

- 1. Assembl the instrument and connect a signal of -40dB to the input terminals (F and H as left input, K and L as right input on J2).
- 2. Adjust P2 for left channel and P4 for right channel to obtain correct reading ("-40" on DIN-scale).
- 3. Change the signal to ref. level and adjust P1 for left channel and P5 for right channel to obtain correct reading ("0" on DIN-scale).
- 4. Repeat the adjustment 1-3.

Fall-back Time Adjustment.

- 1. Connect a burst generator e.g. NTP type 507-100 to the input terminals (F and H is left input, K and L is right input).
- 2. Adjust P3 to obtain recommanded fall-back time. (1.5sec from "0" to "-20" on DIN-scale).





Disassembling the PPM 177-800C.

- 1. Remove the two 20-pole connectors ①
- 2. Loosen the two screws 2
- 3. Remove the rearplate ③
- 4. Remove the two screws 2 the lockplates
- 5. Pull out the two PCB's @ and 5
- 6. Pull out rhe third PCB (Display Board) © or pull out all three PCB's together

If the frontplate must be replaced:

- 1. Remove the rwo screws ⑦
- 2. Pull out the front panel ®
- 3. Remove the frontplate 9