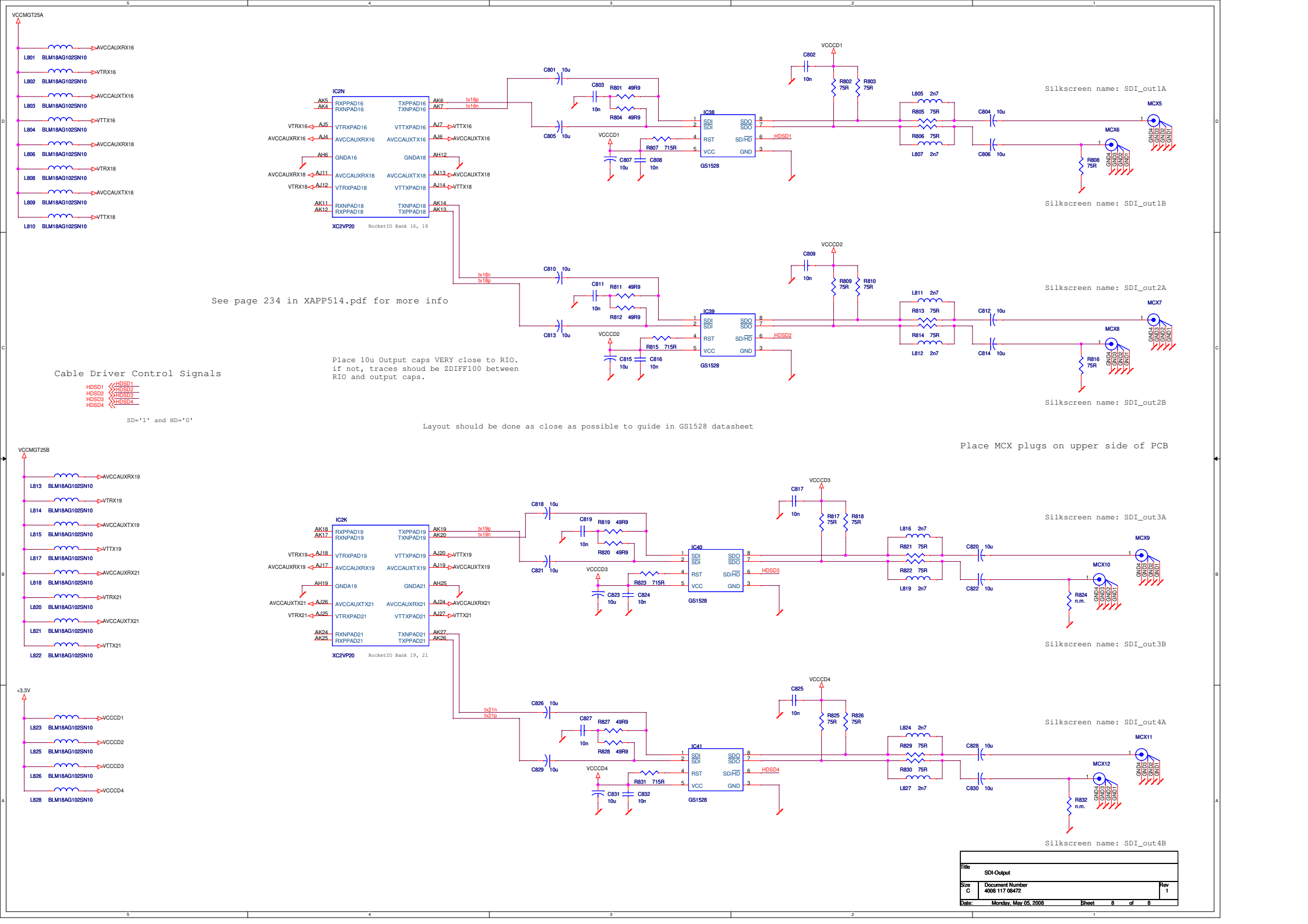
VCCMGT25A
L801 BLM18AG102SN10 AVCCAUXRX16
L802 BLM18AG102SN10 VTRX16
L803 BLM18AG102SN10 AVCCAUXTX16
L804 BLM18AG102SN10 VTTX16
L806 BLM18AG102SN10 AVCCAUXRX18
L808 BLM18AG102SN10 VTRX18
L809 BLM18AG102SN10 AVCCAUXTX18
L810 BLM18AG102SN10 VTTX18
IC2N
XC2VP20
RocketIO Bank 16, 18
See page 234 in XAPP514.pdf for more info
IC38
IC39
GS1528
VCCCD1
VCCCD2
Silkscreen name: SDI_out1A
Silkscreen name: SDI_out1B
Silkscreen name: SDI_out2A
Silkscreen name: SDI_out2B
Cable Driver Control Signals
HDSD1
HDSD2
HDSD3
HDSD4
SD='1' and HD='0'
Place 10u Output caps VERY close to RIO.
if not, traces shoud be ZDIFF100 between
RIO and output caps.
Layout should be done as close as possible to guide in GS1528 datasheet
Place MCX plugs on upper side of PCB
VCCMGT25B
L813 BLM18AG102SN10 AVCCAUXRX19
L814 BLM18AG102SN10 VTRX19
L815 BLM18AG102SN10 AVCCAUXTX19
L817 BLM18AG102SN10 VTTX19
L818 BLM18AG102SN10 AVCCAUXRX21
L820 BLM18AG102SN10 VTRX21
L821 BLM18AG102SN10 AVCCAUXTX21
L822 BLM18AG102SN10 VTTX21
+3.3V
L823 BLM18AG102SN10 VCCCD1
L825 BLM18AG102SN10 VCCCD2
L826 BLM18AG102SN10 VCCCD3
L828 BLM18AG102SN10 VCCCD4
IC2K
XC2VP20
RocketIO Bank 19, 21
IC40
IC41
GS1528
VCCCD3
VCCCD4
Silkscreen name: SDI_out3A
Silkscreen name: SDI_out3B
Silkscreen name: SDI_out4A
Silkscreen name: SDI_out4B
Title
SDI-Output
Size C
Document Number 4008 117 08472
Rev 1
Date: Monday, May 05, 2008
Sheet 8 of 8