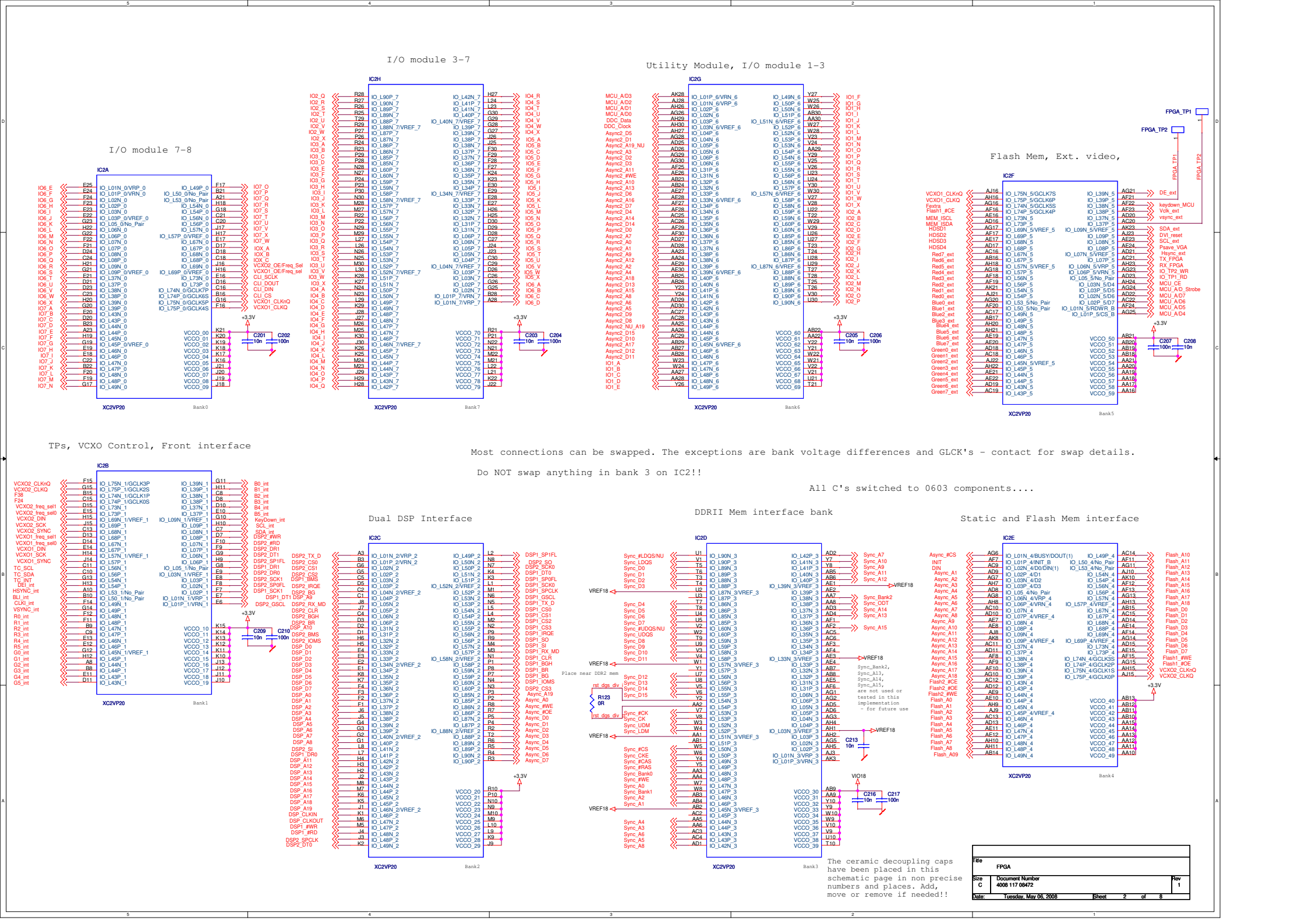
I/O module 3-7
Utility Module, I/O module 1-3
I/O module 7-8
Flash Mem, Ext. video,
TPs, VCXO Control, Front interface
Most connections can be swapped. The exceptions are bank voltage differences and GLCK's - contact for swap details.
Do NOT swap anything in bank 3 on IC2!!
All C's switched to 0603 components....
Dual DSP Interface
DDRII Mem interface bank
Static and Flash Mem interface
IC2A
IC2H
IC2G
IC2F
IC2B
IC2C
IC2D
IC2E
XC2VP20
Bank0
Bank7
Bank6
Bank5
Bank1
Bank2
Bank3
Bank4
FPGA_TP1
FPGA_TP2
Place near DDR2 mem
R123
0R
rst_dgs_div
C213
10n
VIO18
The ceramic decoupling caps have been placed in this schematic page in non precise numbers and places. Add, move or remove if needed!!
Title
FPGA
Size
C
Document Number
4006 117 08472
Rev
1
Date:
Tuesday, May 06, 2008
Sheet
2
of
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