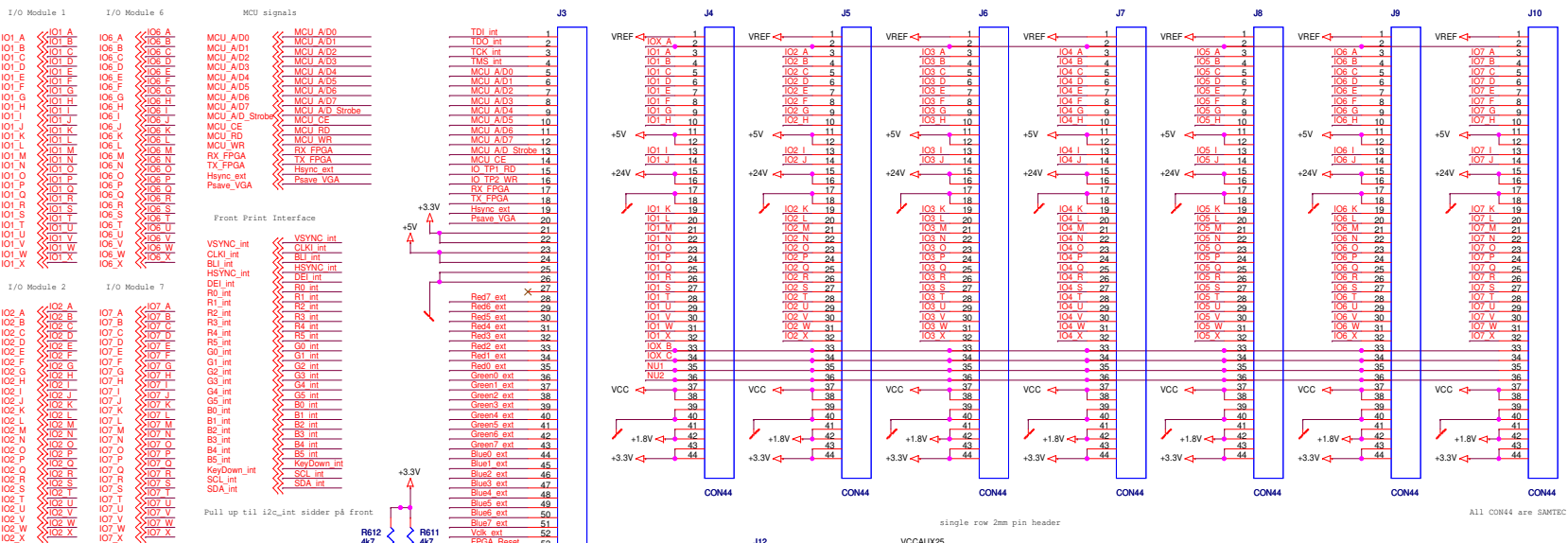


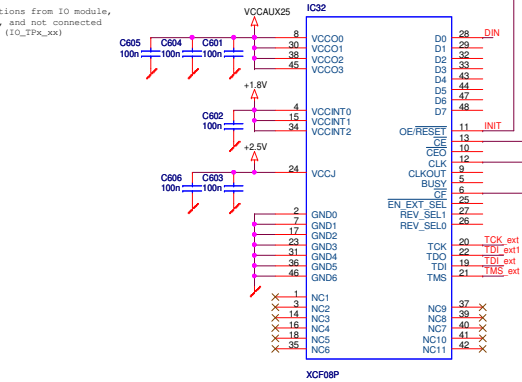
Signals to FPGA



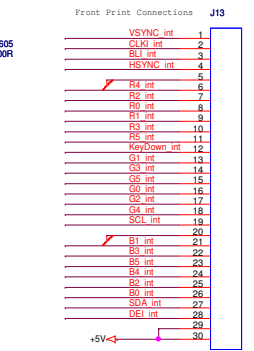
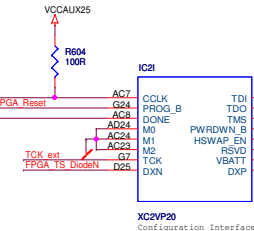
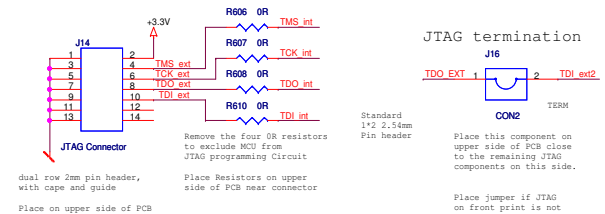
W01 & W02 should only be connected between module slots. They are reserved for future use in case another voltage is needed. In that case it should be generated on one of the modules....

All CON44 are SAMTEC MMS-122-01-L-DV

Extra connections from IO module, not yet used, and not connected on mainboard (IO_TPxx_xxx)



Place all these connectors on Upper side of PCB in same area



Place this according to description in CAD-file

Molex FFC 30pins 1mm SMD