



BMSCOLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous Institute, Affiliated to VTU)

Department Name: Computer Science and Engineering

INTERNALS-II

Course Code: 19CS4PCOPS

Course Title: Operating System

Semester: IV

Maximum Marks: 40 Marks

Date: 01/06/2020

Faculty Handling the Course:

Pradeep S, Dr. B G Prasad, Shyamala G

Instructions: Internal choice is provided in Part C.

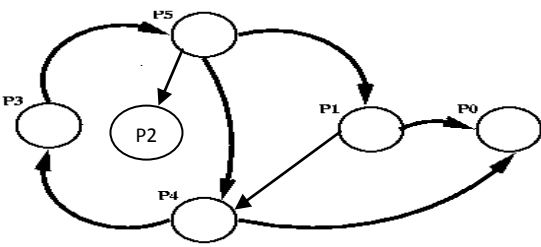
PART-A

Total 5 Marks (No choice)

No.	Question	Marks
1	Define semaphores. Explain different types of semaphores with example.	5

PART-B

Total 15 Marks (No Choice, each question of 5 marks)

No.	Question	Marks
2a)	Given that process P1 has a period of $p1=60$ and a CPU burst of $t1=30$ and Process P2 has a period of $p2=90$ and $t2=40$. Apply Rate Monotonic and Earliest-Deadline-First Scheduling and show the Gantt chart.	5
2b)	For the given wait-for graph (i) Construct the resource allocation graph (ii) Infer the sequence for deadlock, if present 	5
2c)	Memory partitions of 100KB, 500KB, 200KB, 300KB, 600KB (in order) are available. How would first-fit and best-fit algorithms place processes of 212KB, 417KB, 112KB and 426 KB (in order)?	5

PART- C

Total 20 marks (Choice between 3a or 3b and 4a or 4b)

No.	Question	Marks																																																																														
3a)	<div>i)Develop a Pseudo code for Producer & Consumer Process for solving Bounded-buffer problem.</div> <div>ii)Write a Pseudo code for wait and signal operation using structured variable</div>	10M																																																																														
OR																																																																																
3a)	<div>Calculate Average Waiting time and Average Turnaround time for the following processes below</div> <table><tr><th>Processes</th><th>Cpu Burst</th><th>TimePriority</th></tr><tr><td>P1</td><td>2</td><td>2</td></tr><tr><td>P2</td><td>1</td><td>1</td></tr><tr><td>P3</td><td>8</td><td>4</td></tr><tr><td>P4</td><td>4</td><td>2</td></tr><tr><td>P5</td><td>5</td><td>3</td></tr></table> <div>Processes are arrived in the order p1,p2,p3,p4,p5All at time 0. Draw Gantt chart and solve for the following scheduling types</div> <div>a)NonPreemptive SJF</div> <div>b) Priority(higher num has highest priority)</div> <div>c)Round robin(Time quantum=2 msec)</div>	Processes	Cpu Burst	TimePriority	P1	2	2	P2	1	1	P3	8	4	P4	4	2	P5	5	3	10M																																																												
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4a)	<div>Solve using Banker’s algorithm considering the following snapshot of a system</div> <table><tr><th rowspan="2">Pro cess es</th><th colspan="4">Allocation</th><th colspan="4">Max</th><th colspan="4">Available</th></tr><tr><th>A</th><th>B</th><th>C</th><th>D</th><th>A</th><th>B</th><th>C</th><th>D</th><th>A</th><th>B</th><th>C</th><th>D</th></tr><tr><td>P₀</td><td>0</td><td>0</td><td>1</td><td>2</td><td>0</td><td>0</td><td>1</td><td>2</td><td>1</td><td>5</td><td>2</td><td>0</td></tr><tr><td>P₁</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td><td>7</td><td>5</td><td>0</td><td colspan="4" rowspan="4"></td></tr><tr><td>P₂</td><td>1</td><td>3</td><td>5</td><td>4</td><td>2</td><td>3</td><td>5</td><td>6</td></tr><tr><td>P₃</td><td>0</td><td>6</td><td>3</td><td>2</td><td>0</td><td>6</td><td>5</td><td>2</td></tr><tr><td>P₄</td><td>0</td><td>0</td><td>1</td><td>4</td><td>0</td><td>6</td><td>5</td><td>6</td></tr></table> <div>Answer the following:</div> <div>i) What is the content of the need matrix?</div> <div>ii) Illustrate that the system is in safe state by demonstrating an order in which the processes may complete?</div> <div>iii) If a request from process P1 arrives for (0, 4, 2, 0) can the request be granted immediately?</div>	Pro cess es	Allocation				Max				Available				A	B	C	D	A	B	C	D	A	B	C	D	P ₀	0	0	1	2	0	0	1	2	1	5	2	0	P ₁	1	0	0	0	1	7	5	0					P ₂	1	3	5	4	2	3	5	6	P ₃	0	6	3	2	0	6	5	2	P ₄	0	0	1	4	0	6	5	6	10
Pro cess es	Allocation				Max				Available																																																																							
	A	B	C	D	A	B	C	D	A	B	C	D																																																																				
P ₀	0	0	1	2	0	0	1	2	1	5	2	0																																																																				
P ₁	1	0	0	0	1	7	5	0																																																																								
P ₂	1	3	5	4	2	3	5	6																																																																								
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OR																																																																																

4b)	<p>Explain with a neat diagram the working of Translation Look aside Buffer used for address translation.</p> <p>Calculate the effective access time for the given data.</p> <p>TLB Access Time – 15 ns</p> <p>Memory Access Time – 100 ns</p> <p>Hit Ratio – 80%</p>	10
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