

i3g4250d_int_z_threshold_set

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graph LR; A[i3g4250d_int_z_threshold_set] --> B[i3g4250d_read_reg]; A --> C[i3g4250d_write_reg];
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A diagram showing a single node on the left, 'i3g4250d_int_z_threshold_set', which is shaded gray. Two blue arrows originate from its right side and point to two separate white nodes on the right: 'i3g4250d_read_reg' (top) and 'i3g4250d_write_reg' (bottom). All nodes have a thin black border.

i3g4250d_read_reg

i3g4250d_write_reg