```
ctrl_reg1
  i3g4250d_ctrl_reg1_t
                                 ctrl_reg2
  i3g4250d_ctrl_reg2_t
  i3g4250d_ctrl_reg3_t
                                 ctrl_reg3
                                 ctrl reg4
  i3g4250d ctrl reg4 t
                                 ctrl reg5
  i3g4250d_ctrl_reg5_t
 i3g4250d_reference_t
 i3g4250d_status_reg_t
   i3g4250d_fifo_ctrl
         _reg_t
                               fifo ct
i3g4250d fifo src reg t
                                fifo src
  i3g4250d int1 cfg t
                                                 i3g4250d reg t
  i3g4250d_int1_src_t
i3g4250d_int1_tsh_xh_t
 i3g4250d_int1_tsh_xl_t
i3g4250d_int1_tsh_yh_t
 i3g4250d_int1_tsh_yl_t
                                int1
i3g4250d int1 tsh zh t
 i3g4250d_int1_tsh_zl_t
i3g4250d_int1_duration_t
                                  bitwise
        bitwise t
```