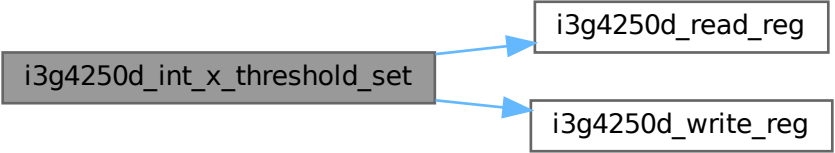


i3g4250d\_int\_x\_threshold\_set



```
graph LR; A[i3g4250d_int_x_threshold_set] --> B[i3g4250d_read_reg]; A --> C[i3g4250d_write_reg];
```

i3g4250d\_read\_reg

i3g4250d\_write\_reg