


i3g4250d\_pin\_int2\_route\_get



```
graph LR; A[i3g4250d_pin_int2_route_get] --> B[i3g4250d_read_reg]
```

The diagram consists of two rectangular boxes connected by a horizontal blue arrow pointing from left to right. The left box is filled with a dark gray color and contains the text 'i3g4250d\_pin\_int2\_route\_get'. The right box is white with a black border and contains the text 'i3g4250d\_read\_reg'.

i3g4250d\_read\_reg