```
i3g4250d_boot_set
               i3g4250d_data_format_set
                 i3g4250d data rate set
                i3g4250d_fifo_enable_set
                 i3g4250d_fifo_mode_set
              i3g4250d fifo watermark set
                  i3g4250d_filter_path
                      _internal_set
main
                 i3g4250d_filter_path_set
                 i3g4250d_full_scale_set
               i3g4250d_hp_bandwidth_set
                 i3g4250d_hp_mode_set
                 i3g4250d_hp_reference
                       _value_set
              i3g4250d_int_notification_set
                                                    i3g4250d_write_reg
               i3g4250d int on threshold
                        _conf_set
               i3g4250d_int_on_threshold
                        _dur_set
               i3g4250d_int_on_threshold
                       _mode_set
              i3g4250d_int_x_threshold_set
              i3g4250d_int_y_threshold_set
              i3g4250d_int_z_threshold_set
               i3g4250d_lp_bandwidth_set
              i3g4250d_pin_int1_route_set
              i3g4250d_pin_int2_route_set
                 i3g4250d_pin_mode_set
                i3g4250d_pin_polarity_set
                 i3g4250d_self_test_set
                 i3g4250d_spi_mode_set
```