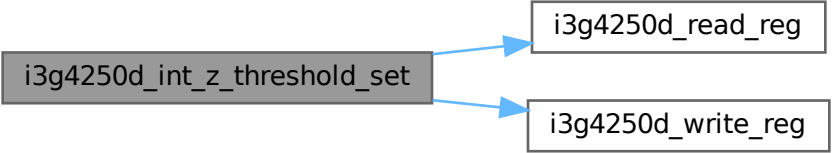


i3g4250d\_int\_z\_threshold\_set



```
graph LR; A[i3g4250d_int_z_threshold_set] --> B[i3g4250d_read_reg]; A --> C[i3g4250d_write_reg];
```

The diagram illustrates a branching structure. A single node on the left, labeled 'i3g4250d\_int\_z\_threshold\_set', has two outgoing arrows pointing to two separate nodes on the right. The top node on the right is labeled 'i3g4250d\_read\_reg' and the bottom node is labeled 'i3g4250d\_write\_reg'. All nodes are rectangular boxes with black outlines. The left node is filled with a dark gray color, while the two right nodes are white.

i3g4250d\_read\_reg

i3g4250d\_write\_reg