


i3g4250d_pin_int1_route_get



```
graph LR; A[i3g4250d_pin_int1_route_get] --> B[i3g4250d_read_reg]
```

A diagram showing a call from the function `i3g4250d_pin_int1_route_get` to the function `i3g4250d_read_reg`. The first function is in a grey box, and the second is in a white box, connected by a blue arrow.

i3g4250d_read_reg