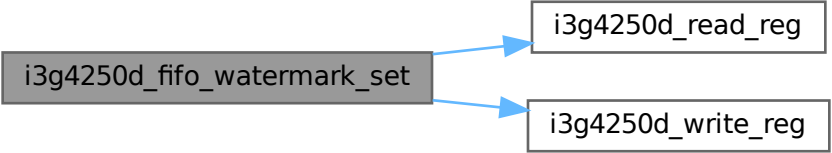


i3g4250d\_fifo\_watermark\_set



```
graph LR; A[i3g4250d_fifo_watermark_set] --> B[i3g4250d_read_reg]; A --> C[i3g4250d_write_reg];
```

The diagram illustrates a data flow from a single source to two destinations. On the left, a gray rectangular box labeled 'i3g4250d\_fifo\_watermark\_set' serves as the source. Two blue arrows originate from its right side. The top arrow points to a white rectangular box labeled 'i3g4250d\_read\_reg' located at the top right. The bottom arrow points to a white rectangular box labeled 'i3g4250d\_write\_reg' located at the bottom right. All boxes have a thin black border.

i3g4250d\_read\_reg

i3g4250d\_write\_reg