## Karthik Vivek Nayak

#### Address:

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# objective

An Electronics and Communication pre final-year undergraduate student seeking internship and training opportunities in embedded systems domain, to hone my skills with a vision to develop system(s) that would enhance life on earth.



### education

Degree	College/School	University/Board	Passing Year	Score
B.E.(ECE)	KLS Gogte Institute of	Autonomous Institute under	2019	9.14 CGPA
	Technology, Belgaum	Visvesvaraya Technological		(Sem 1-5)
		University		
12th	Expert PU College, Kodi-	Department of Pre-University	2015	96.67%
	albail, Mangalore	Education, Government of Kar-		
		nataka		
10th	Kendriya Vidyalaya NO.2	Central Board of Secondary Ed-	2013	93.1%
	Cantonment Belgaum	ucation (CBSE)		

# projects

1. Harvester Bot- Eyantra Robotics Competition 2017-18

A fully autonomous robot that harvests fruits in an abstract farm.

2. Real Time Obstacle Evading bot

A fully autonomous robot that can reach its destination even on introduction of obstacles in its path, by taking another best alternate path.

3. Mini Project: Traffic signal management using LPC2148.

# training and internships

- Hardware Modeling Using Verilog course offered by NPTEL.
- Course on Python Programming.

# research publications

1. none

#### technical skills

- Programming Languages
  - · Proficient: C, Verilog HDL, Embedded C.
  - · Intermediate: Assembly language, Python, C++.
  - · Currently learning to code in python, effectively.
- Hardware Knowledge: NXP LPC2148, ATmega2560, Raspberry pi.
- Software Knowledge: Familiar with Xilinx ISE, Vivado, Keil uvision, Cadence Virtuoso, Matlab, Scilab, Simulink.

### soft skills

- 1. Languages: English, Konkani, Hindi, Marathi, Kannada.
- 2. Analysis: Good at analysing situation(s) and ability to take appropriate decisions.
- 3. Teamwork: Responsible, dedicated, dynamic, friendly and capable.
- 4. Leadership: Capable of leading the team effectively.
- 5. Have sound work ethic and a philozoic by nature.

### extra-curricular activities

- Member of Lead Cell GIT, a social and innovation club.
- Regularly involved in creating awareness regarding the need to follow traffic rules in the city.

### co-curricular activities

- 1. 1st Prize: e-Yantra Robotics Competition 2017-18 in the theme Harvester Bot.
- 2. Topper (top 5% of certified candidates): Hardware Modeling Using Verilog, a course offered by NPTEL.

### personal details

1. Fatherś Name: Vivek Nayak

2. Motherś Name: Shanti V. Nayak

3. Sex: Male

4. **Date of Birth**: 17 June 1997

5. Nationality: Indian

6. Marital Status: Unmarried

### reference

- 1. **Dr. Prashant Patavardhan**, Head of Department, Electronics and Communication Engineering, KLS Gogte Institute of Technology, email: pppatavardhan@git.edu | cell: 9448941840
- 2. Mrs. Supriya S. Shanbhag, Assistant Professor, Electronics and Communication Engineering, KLS Gogte Institute of Technology, email: ssshanbhag@git.edu | cell: 9845306767

# declaration

I hereby declare that all details furnished are true to the best of my knowledge.

# date

17 April 2018