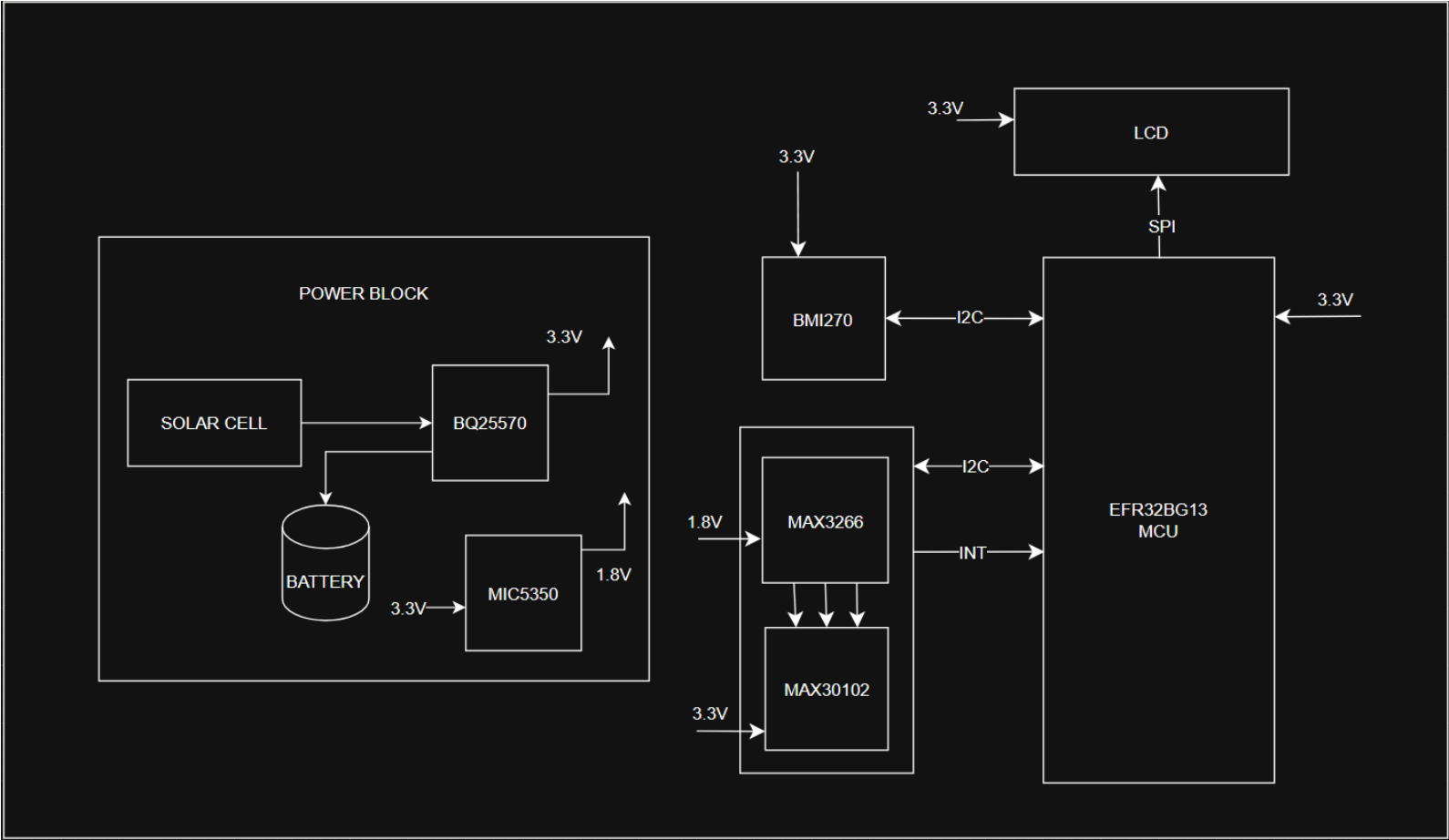
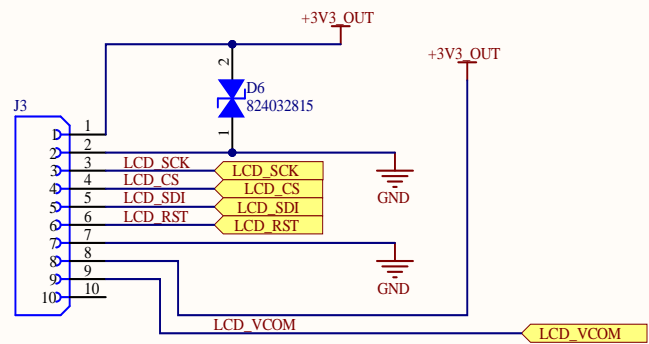


BLOCK DIAGRAM



Title			
BLOCK DIAGRAM			
Size	Number	Revision	
A4	1	1	
Date:	10/26/2025	Sheet of	
File:	C:\Users\...\BLOCK DIAGRAM.SchDoc	Drawn By: Induja/Karthik	

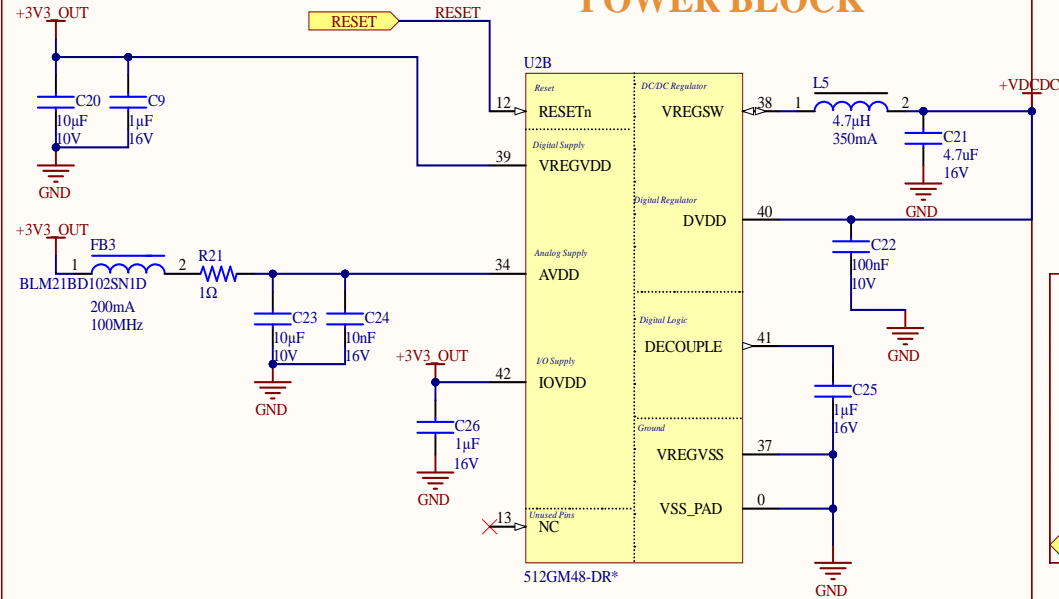
LCD CONNECTOR



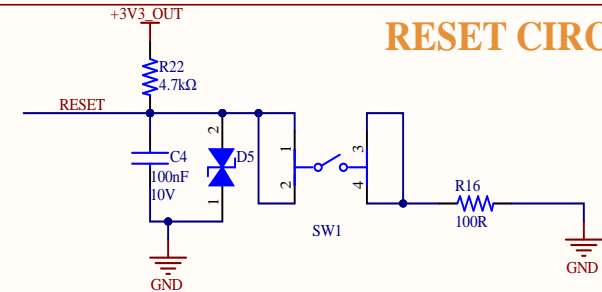
RST = "L": Display ON
RST = "H": Display OFF (Black display keeping pixel memory)
VCOM = "L" is necessary when RST = "H"
(When VCOM="H", Display does not turn to black, and current consumption increases by shoot-through-current in panel. (several mA))

Title			LCD
Size	Number	Revision	
A4	4	1	
Date:	10/26/2025	Sheet of	
File:	C:\Users\...\LCD.SchDoc	Drawn By:	Induja N/Karthik R

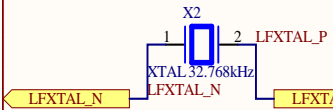
POWER BLOCK



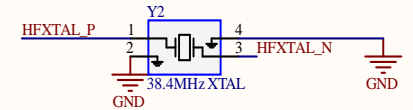
RESET CIRCUITRY



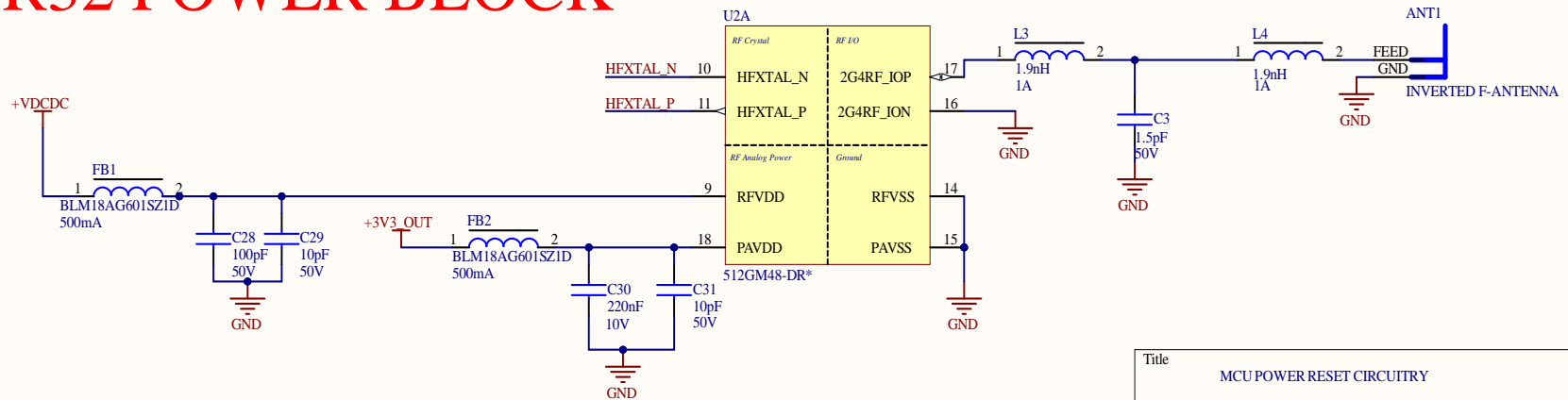
LOW FREQUENCY CRYSTAL



HIGH FREQUENCY CRYSTAL

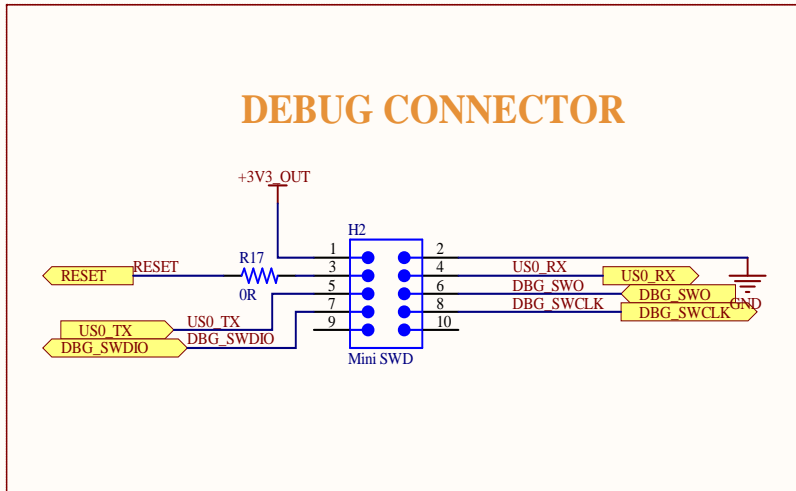
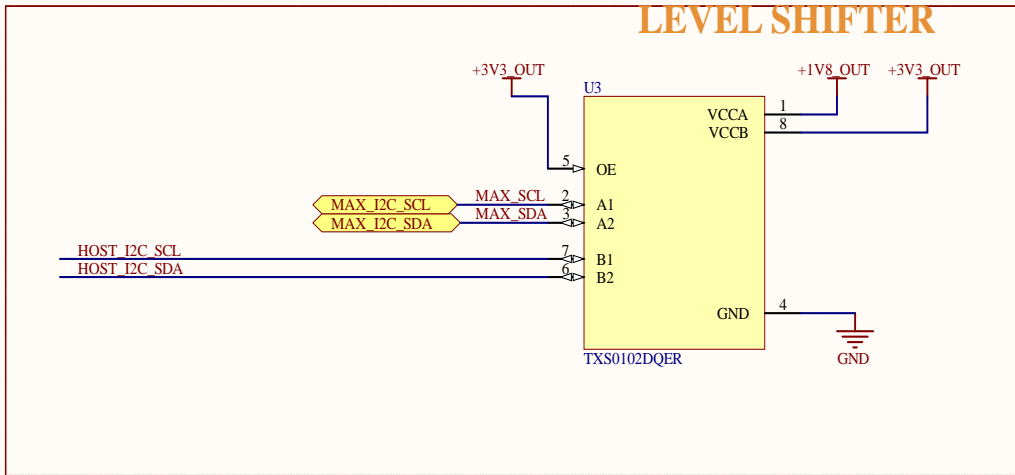
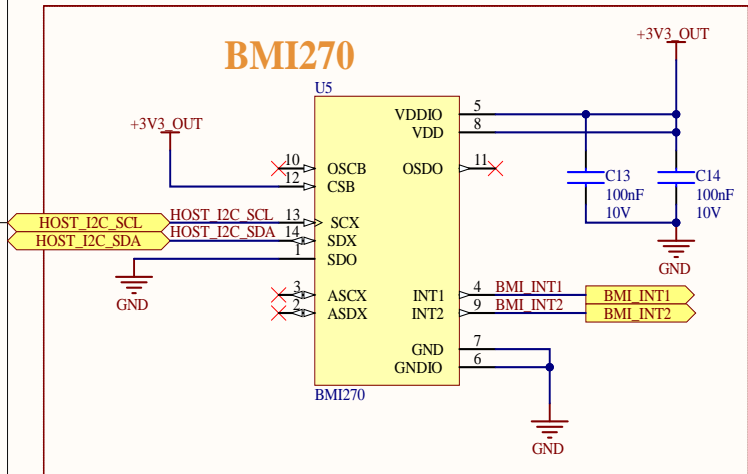


EFR32 POWER BLOCK



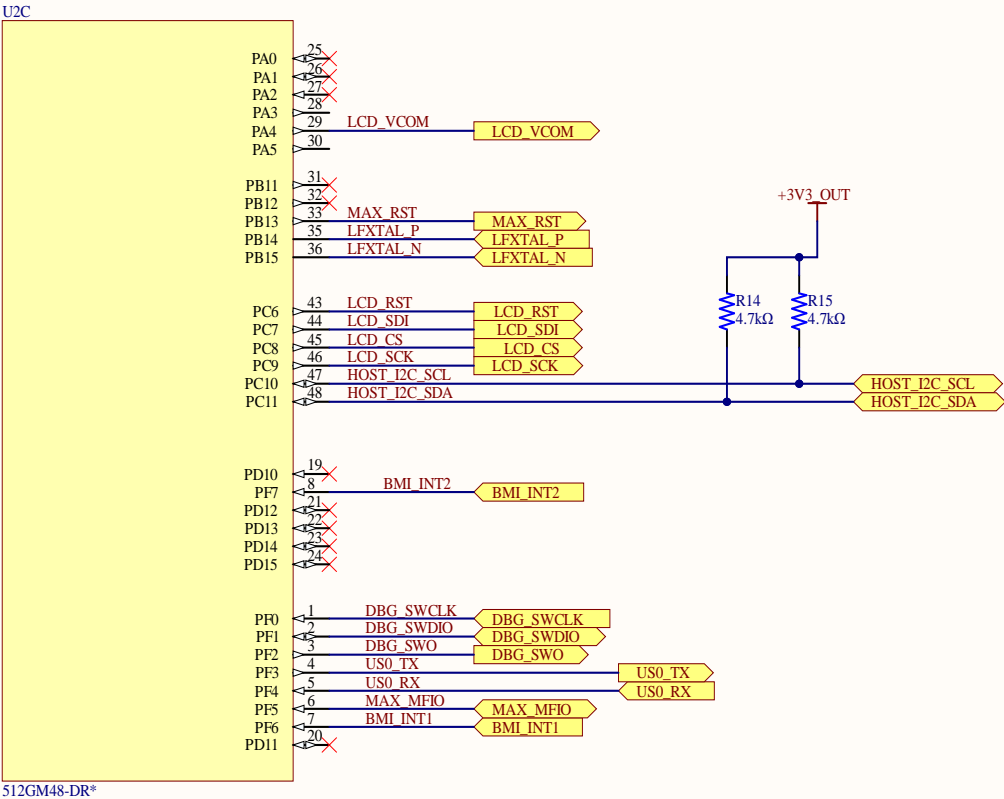
Title		
MCU POWER RESET CIRCUITRY		
Size	Number	Revision
A4	5	1
Date:	10/26/2025	Sheet of
File:	C:\Users\...EFR32G13_POWER_RESET.Sch	Drawn By: Induja N/Karthik R

PERIPHERAL INTERCONNECTIONS

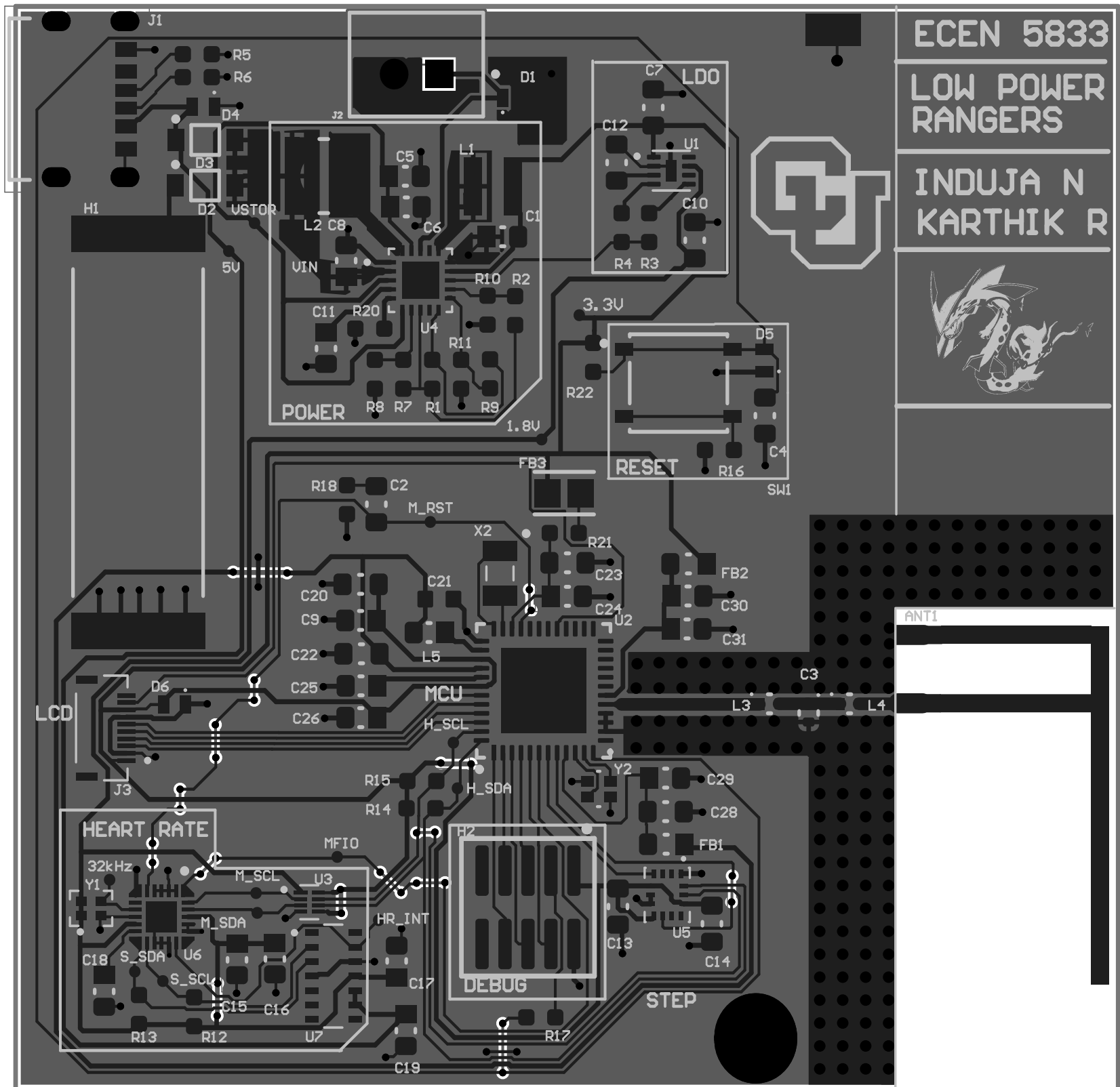
[illegible]

Title			
PERIPHERAL INTERCONNECTS			
Size	Number		Revision
A4	6		1
Date:	10/26/2025	Sheet of	
File:	C:\Users\... \COMMUNICATION_SchDoc	Drawn By:	Induja N\Karthik R

MCU INTERCONNECTS



Title			
MCU INTERCONNECTS			
Size	Number	Revision	
A4	7	1	
Date:	10/26/2025	Sheet of	
File:	C:\Users\...\EFR32BG13_INTERCONNECTS	Drawn By:	Induja N/Karthik R



ECEN 5833

LOW POWER
RANGERS

INDUJA N
KARTHIK R



ANT1

