



Summer Fellowship Report

On

Spice Simulations using Kicad nightly builds

Submitted by

Akshay NH and Athul MS

Under the guidance of

Prof.Kannan M. Moudgalya

Chemical Engineering Department IIT Bombay

 $July\ 3,\ 2018$

Acknowledgment

We are extremely thankful to Prof . Kannan Moudgalya for guiding and motivating us throughout the FOSSEE fellowship programme. We would also like to thank our mentors Mrs. Gloria Nandihal and Mr. Athul George for their immense support and advice. Moreover, we are grateful to our fellow friends Mudit Joshi and Ashutosh Gangwar for assisting us with their programming knowledge. Lastly, we extend our warm gratiutude to the managers and staff of FOSSEE for their co-operation and assistance.

Contents

1	Introduction		3
	1.1	Introduction	3
2	Steps to follow for better understanding of kicad nightly builds for electronic simulation		4
		Steps to follow for better understanding of kicad nightly	
		builds for electronic simulation	4

Chapter 1

Introduction

1.1 Introduction

KiCad is an open-source software tool for the creation of electronic schematic diagrams and PCB artwork. KiCad can be considered mature enough to be used for the successful development and maintenance of complex electronic boards. KiCad does not present any board-size limitation and it can easily handle up to 32 copper layers, up to 14 technical layers and up to 4 auxiliary layers. KiCad can create all the files necessary for building printed boards, Gerber files for photo-plotters, drilling files, component location files and a lot more. Being open source (GPL licensed), KiCad represents the ideal tool for projects oriented towards the creation of electronic hardware with an open-source flavour.

Stable builds Stable releases of KiCad can be found in most distributions package managers as kicad and kicad-doc. If your distribution does not provide latest stable version, please follow the instruction for unstable builds and select and install the latest stable version.

Unstable (nightly development) builds Unstable builds are built from the most recent source code. They can sometimes have bugs that cause file corruption, generate bad gerbers, etc, but are generally stable and have the latest features.

Chapter 2

Structure of the Signal Processing toolbox

The toolbox has two types of files:

2.1 Visible Files

- loader.sce
- builder.sce
- demos
- etc
- jar
- cleaner.sce
- testfile.sce
- unloader.sce
- README.md
- help

Reference

- http://docs.kicad-pcb.org/stable/en/getting_started_in_kicad.pdf
- https://github.com/KiCad/kicad-source-mirror
- $\bullet \ \mathtt{https://github.com/FOSSEE/eSIm-Kicad-Simulations}$
- https://forum.kicad.info/
- http://www.ecircuitcenter.com/Basics.htm
- http://www.ecircuitcenter.com/SPICEsummary.htm