





OPC	inst	regwrite	IMMsrc	ALUsrc	memwrite	resultsrc	ALUcontrol	PCsrc
3	lw	1	000	0	0	01	000	00
35	sw	0	001	1	1	xx	000	00
99	B-T	0	010	0	0	xx	001	<div> <div>beq → zero? a1 : 00</div> <div>bne → zero? a0 : a1</div> <div>blt → sign? a1 : 00</div> <div>bge → zero   ~sign? a1 : 00</div> </div>
51	R-T	1	xxx	0	0	00	<div> <div>add → 000</div> <div>sub → 001</div> <div>and → 010</div> </div>	00
19	I-T	1	000	1	0	00	<div> <div>or → 011</div> <div>slt → 101</div> <div>sltu → 110</div> </div>	00
111	jal	1	011	x	0	00	<div> <div>addi → 000</div> <div>xori → 100</div> <div>ori → 011</div> </div>	00
55	lui	1	100	x	0	10	xxx	01
103	jalr	1	000	1	0	11	xxx	00
					0	10	000	10



lw x8, 0(x0)

addi x6, x0, 4

slli x7, x6, 40

beq x7, x0, 28

خواب نهایی = x8

lw x9, 0(x6)

sllui x1, x8, x9

beq x1, x0, 8

add x8, x0, x9

addi x6, x6, 4

jal x0, -28