

YMG8K16F18L5BR1

8Kx16 bits Embedded MTP Memory

General Description

The diagonal pattern of **YMG8K16F18L5BR1** IP needs to write different data into 256 addresses, please follow the below table.

Address	data
0000H	FFF0H
0021H	FFF0H
0042H	FFF0H
0063H	FFF0H
0084H	FFF0H
00A5H	FFF0H
00C6H	FFF0H
00E7H	FFF0H

Address	data
0108H	FFF0H
0129H	FFF0H
014AH	FFF0H
016BH	FFF0H
018CH	FFF0H
01ADH	FFF0H
01CEH	FFF0H
01EFH	FFF0H

Address	data
0210H	FFF0H
0231H	FFF0H
0252H	FFF0H
0273H	FFF0H
0294H	FFF0H
02B5H	FFF0H
02D6H	FFF0H
02F7H	FFF0H

Address	data
0318H	FFF0H
0339H	FFF0H
035AH	FFF0H
037BH	FFF0H
039CH	FFF0H
03BDH	FFF0H
03DEH	FFF0H
03FFH	FFF0H

Address	data
0400H	FF0FH
0421H	FF0FH
0442H	FF0FH
0463H	FF0FH
0484H	FF0FH
04A5H	FF0FH
04C6H	FF0FH
04E7H	FF0FH

Address	data
0508H	FF0FH
0529H	FF0FH
054AH	FF0FH
056BH	FF0FH
058CH	FF0FH
05ADH	FF0FH
05CEH	FF0FH
05EFH	FF0FH

Address	data
0610H	FF0FH
0631H	FF0FH
0652H	FF0FH
0673H	FF0FH
0694H	FF0FH
06B5H	FF0FH
06D6H	FF0FH
06F7H	FF0FH

Address	data
0718H	FF0FH
0739H	FF0FH
075AH	FF0FH
077BH	FF0FH
079CH	FF0FH
07BDH	FF0FH
07DEH	FF0FH
07FFH	FF0FH

Address	data
0800H	F0FFH
0821H	F0FFH
0842H	F0FFH
0863H	F0FFH
0884H	F0FFH
08A5H	F0FFH
08C6H	F0FFH
08E7H	F0FFH

Address	data
0908H	F0FFH
0929H	F0FFH
094AH	F0FFH
096BH	F0FFH
098CH	F0FFH
09ADH	F0FFH
09CEH	F0FFH
09EFH	F0FFH

Address	data
0A10H	F0FFH
0A31H	F0FFH
0A52H	F0FFH
0A73H	F0FFH
0A94H	F0FFH
0AB5H	F0FFH
0AD6H	F0FFH
0AF7H	F0FFH

Address	data
0B18H	F0FFH
0B39H	F0FFH
0B5AH	F0FFH
0B7BH	F0FFH
0B9CH	F0FFH
0BBDH	F0FFH
0BDEH	F0FFH
0BFFH	F0FFH

Address	data
0C00H	0FFFFH
0C21H	0FFFFH
0C42H	0FFFFH
0C63H	0FFFFH
0C84H	0FFFFH
0CA5H	0FFFFH
0CC6H	0FFFFH
0CE7H	0FFFFH

Address	data
0D08H	0FFFFH
0D29H	0FFFFH
0D4AH	0FFFFH
0D6BH	0FFFFH
0D8CH	0FFFFH
0DADH	0FFFFH
0DCEH	0FFFFH
0DEFH	0FFFFH

Address	data
0E10H	0FFFFH
0E31H	0FFFFH
0E52H	0FFFFH
0E73H	0FFFFH
0E94H	0FFFFH
0EB5H	0FFFFH
0ED6H	0FFFFH
0EF7H	0FFFFH

Address	data
0F18H	0FFFFH
0F39H	0FFFFH
0F5AH	0FFFFH
0F7BH	0FFFFH
0F9CH	0FFFFH
0FBDH	0FFFFH
0FDEH	0FFFFH
0FFFH	0FFFFH

Address	data
1001H	FFF0H
1020H	FFF0H
1042H	FFF0H
1063H	FFF0H
1084H	FFF0H
10A5H	FFF0H
10C6H	FFF0H
10E7H	FFF0H

Address	data
1108H	FFF0H
1129H	FFF0H
114AH	FFF0H
116BH	FFF0H
118CH	FFF0H
11ADH	FFF0H
11CEH	FFF0H
11EFH	FFF0H

Address	data
1210H	FFF0H
1231H	FFF0H
1252H	FFF0H
1273H	FFF0H
1294H	FFF0H
12B5H	FFF0H
12D6H	FFF0H
12F7H	FFF0H

Address	data
1318H	FFF0H
1339H	FFF0H
135AH	FFF0H
137BH	FFF0H
139CH	FFF0H
13BDH	FFF0H
13DEH	FFF0H
13FFH	FFF0H

Address	data
1400H	FF0FH
1421H	FF0FH
1442H	FF0FH
1463H	FF0FH
1484H	FF0FH
14A5H	FF0FH
14C6H	FF0FH
14E7H	FF0FH

Address	data
1508H	FF0FH
1529H	FF0FH
154AH	FF0FH
156BH	FF0FH
158CH	FF0FH
15ADH	FF0FH
15CEH	FF0FH
15EFH	FF0FH

Address	data
1610H	FF0FH
1631H	FF0FH
1652H	FF0FH
1673H	FF0FH
1694H	FF0FH
16B5H	FF0FH
16D6H	FF0FH
16F7H	FF0FH

Address	data
1718H	FF0FH
1739H	FF0FH
175AH	FF0FH
177BH	FF0FH
179CH	FF0FH
17BDH	FF0FH
17DEH	FF0FH
17FFH	FF0FH

Address	data
1800H	F0FFH
1821H	F0FFH
1842H	F0FFH
1863H	F0FFH
1884H	F0FFH
18A5H	F0FFH
18C6H	F0FFH
18E7H	F0FFH

Address	data
1908H	F0FFH
1929H	F0FFH
194AH	F0FFH
196BH	F0FFH
198CH	F0FFH
19ADH	F0FFH
19CEH	F0FFH
19EFH	F0FFH

Address	data
1A10H	F0FFH
1A31H	F0FFH
1A52H	F0FFH
1A73H	F0FFH
1A94H	F0FFH
1AB5H	F0FFH
1AD6H	F0FFH
1AF7H	F0FFH

Address	data
1B18H	F0FFH
1B39H	F0FFH
1B5AH	F0FFH
1B7BH	F0FFH
1B9CH	F0FFH
1BBDH	F0FFH
1BDEH	F0FFH
1BFFH	F0FFH

Address	data
1C00H	0FFFH
1C21H	0FFFH
1C42H	0FFFH
1C63H	0FFFH
1C84H	0FFFH
1CA5H	0FFFH
1CC6H	0FFFH
1CE7H	0FFFH

Address	data
1D08H	0FFFH
1D29H	0FFFH
1D4AH	0FFFH
1D6BH	0FFFH
1D8CH	0FFFH
1DADH	0FFFH
1DCEH	0FFFH
1DEFH	0FFFH

Address	data
1E10H	0FFFH
1E31H	0FFFH
1E52H	0FFFH
1E73H	0FFFH
1E94H	0FFFH
1EB5H	0FFFH
1ED6H	0FFFH
1EF7H	0FFFH

Address	data
1F18H	0FFFH
1F39H	0FFFH
1F5AH	0FFFH
1F7BH	0FFFH
1F9CH	0FFFH
1FBDH	0FFFH
1FDEH	0FFFH
1FFFH	0FFFH

Update history:

Date	Rev	What	Who
2022/05/03	1.0	Initial draft	TK Chang

Warning

The specification obtained arrives from individual testing result before integration; it has possibility to have the difference because of the mutual disturbing effect after integration. Therefore, the user must precede the full test and verification to conform the function and the reliability after integration. After the confirmation, and does not have any problem, it can enter the pilot run then the production run. YMC will not be responsible for the related responsibility and loss, if the procedure does not pass through the full test and verification.