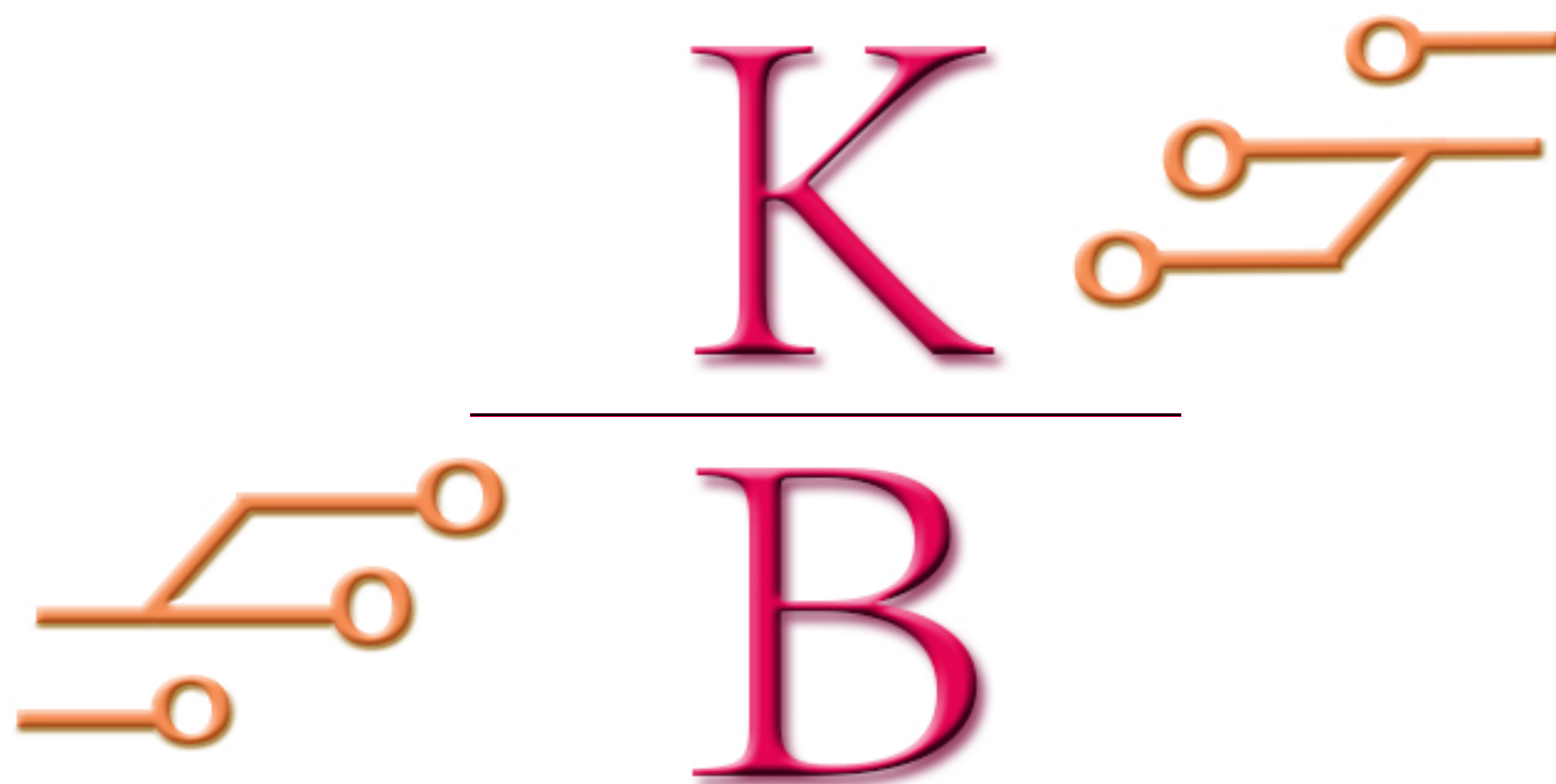


$$\begin{array}{c}
 \text{K} \\
 \hline
 \text{B}
 \end{array}$$


The image shows a Karnaugh map (K-map) with the letters 'K' and 'B' separated by a horizontal line. Surrounding the K-map are four orange circuit symbols: a NAND gate at the top, a NOR gate at the bottom, and two OR gates on the left and right sides.