

CS & IT ENGINEERING



THEORY OF COMPUTATION

Mealy and Moore Machine

Lecture - 05



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Recap of Previous Lecture



Topic

Topic

{ closure properties of }
Regular Languages



Topics to be Covered



Topic

MEALY MACHINE

Topic

?? Moore Machine

Topic

??

Topic

??



Topic : Closure Properties of Language Families

Operation	Regular	DCFL	CFL	CSL	Recursive	RE
Union	yes	no	yes	yes	yes	yes
Intersection	yes	no	no	yes	yes	yes
Complement	yes	yes	no	yes	yes	no
Concatenation	yes	no	yes	yes	yes	yes
Homomorphism	yes	no	yes	no	no	yes
Substitution	yes	no	yes	yes	no	yes
Inverse Homomorphism	yes	yes	yes	yes	yes	yes
Reverse	yes	no	yes	yes	yes	yes
Intersection with a regular language	yes	yes	yes	yes	yes	yes

	Regular
① Subset op	X
② Union op	✓
③ Concatenation	✓
④ Complement	✓
⑤ Kleene closure	✓
⑥ positive closure	✓
⑦ Reversal	✓

	Regular
⑧ Intersection op	✓
⑨ Difference op	✓
⑩ Prefix op	✓
⑪ Suffix op	✓
✓ ⑫ Quotient op	✓ $L_1/L_2 = \text{regular}$
✓ ⑬ Substitution op	✓
⑭ Homomorphism op	✓
⑮ Inverse Homomorphism	✓

Regular languages not closed under

① Subset	X
② Infinite Union	X
③ Infinite Intersection	X

Regular

① Subset op ✗

② Union op ✓

③ Concatenation ✓

④ Complement ✓

⑤ Kleene closure ✓

⑥ positive closure ✓

⑦ Reversal ✓

Regular

⑧ Intersection op ✓

⑨ Difference op ✓

⑩ Prefix op ✓

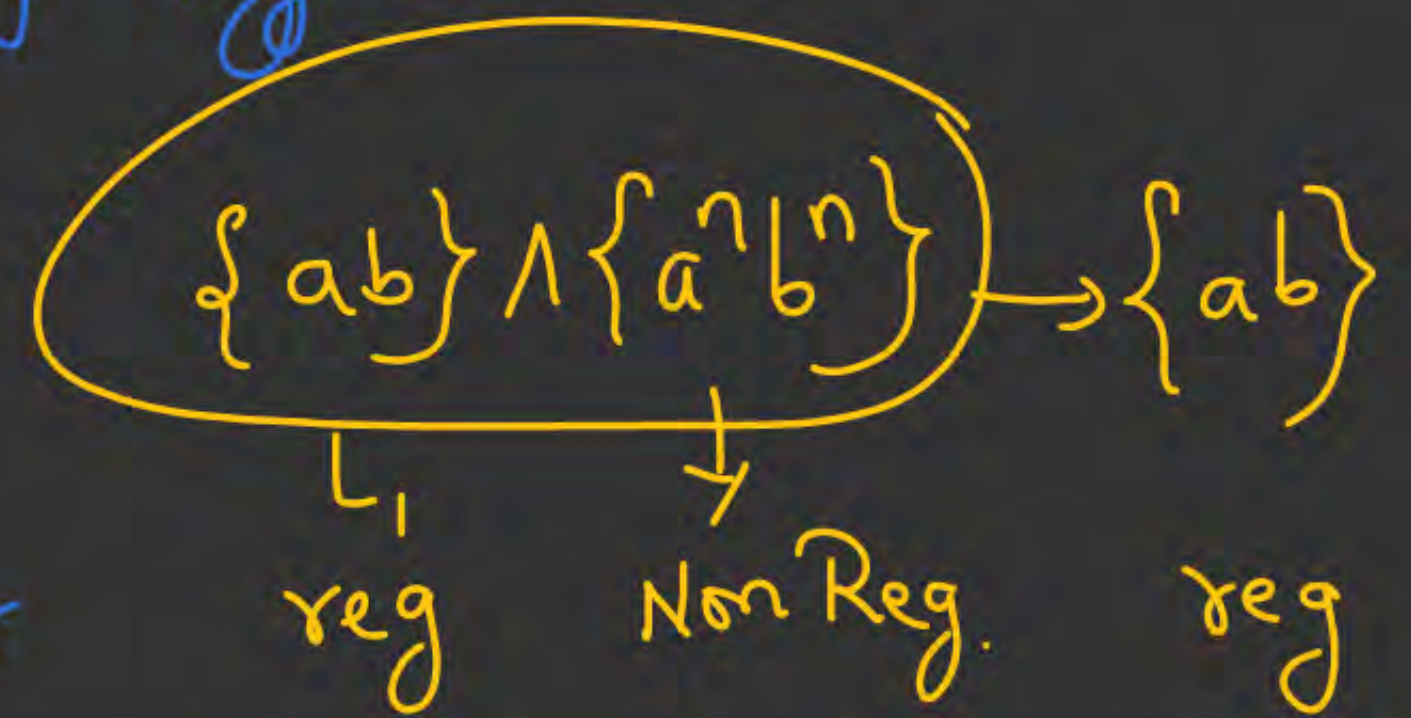
✓ ⑪ Suffix op ✓

✓ ⑫ Quotient op ✓ ✓ $L_1/L_2 = \text{regular}$ { ⑬ Substitution op ✓ ✓ ✓ $S(L) = \text{always Regular.}$

⑭ Homomorphism op ✓ ✓ ✓

⑮ Inverse Homomorphism ✓ ✓ ✓

If $L_1 \cap L_2$ is Regular, L_1 is Regular
then L_2 ?



(a) Always Regular

~~(b) need not be Regular~~

↓

Quotient op $L_1/L_2 = \{x \mid xy \in L_1, y \in L_2\}$

$$L_1/L_2 = \frac{00\cancel{0}}{\cancel{0}} = 0$$

$$\frac{\cancel{xy}}{\cancel{y}} = x \checkmark$$

$$\frac{00\cancel{1}}{\cancel{1}} = 00$$

$$L_1 \quad \frac{00\cancel{01}}{\cancel{01}} = \{00\}$$

$$L_1 \quad \frac{\epsilon \cancel{00}}{\cancel{00}} = \epsilon$$

$$\cancel{L_1} \quad \frac{\cancel{01}}{\cancel{10}} = \emptyset$$

$$\frac{L_1}{L_2} = \frac{a^*b}{\boxed{b^*a}} = \emptyset$$

$$\begin{aligned} L_1/L_2 &= \frac{a^*}{a} = \left\{ \frac{\epsilon}{a}, \frac{a}{a}, \frac{a^2}{a}, \frac{a^3}{a}, \frac{a^4}{a}, \frac{a^5}{a} \dots \right\} \\ &\quad \epsilon, a, a^2, a^3, a^4 \dots \end{aligned}$$

$$L_1/L_2 = a^*$$

$$\begin{aligned}
 \Sigma^* &= \frac{\Sigma^*}{\Sigma^*} = \frac{(a+b)^*}{(a+b)^*} = \underline{(a+b)^*} = \left\{ \frac{\epsilon}{\epsilon}, \frac{a}{\epsilon}, \frac{b}{\epsilon}, \frac{aa}{\epsilon}, \frac{ab}{\epsilon}, \frac{ba}{\epsilon}, \frac{bb}{\epsilon} \dots \right\} \\
 &= (a+b)^* + \dots = \underline{(a+b)^*} \checkmark
 \end{aligned}$$

(Q)

$$\left. \begin{array}{l} L_1 = 0^*1 \\ L_2 = 1^*0 \end{array} \right\} = \emptyset \quad L_1/L_2 = ?$$

(a) 0^*

(b) 1^*

(c) 0^*1^*

(d) \emptyset ✓

(Q) $L_1 = \underline{a^* b a^*}$ & $L_2 = \underline{b a^*}$ What is $L_1/L_2 = ?$

~~(a) $a^* + b^*$~~

~~(b) $a^* b a^*$~~

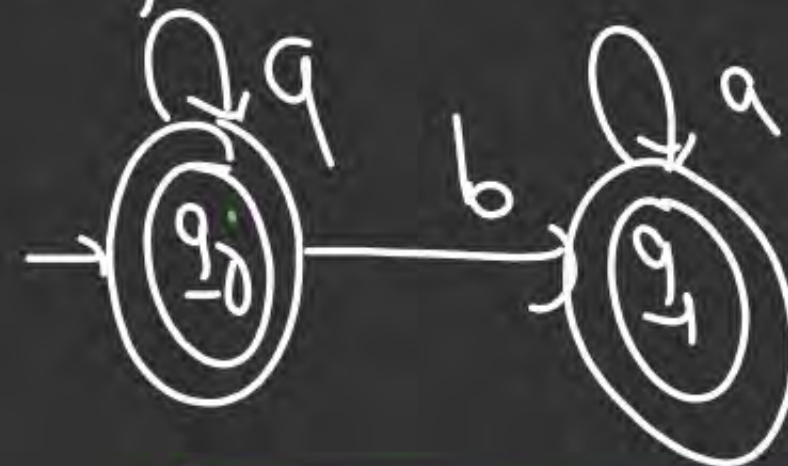
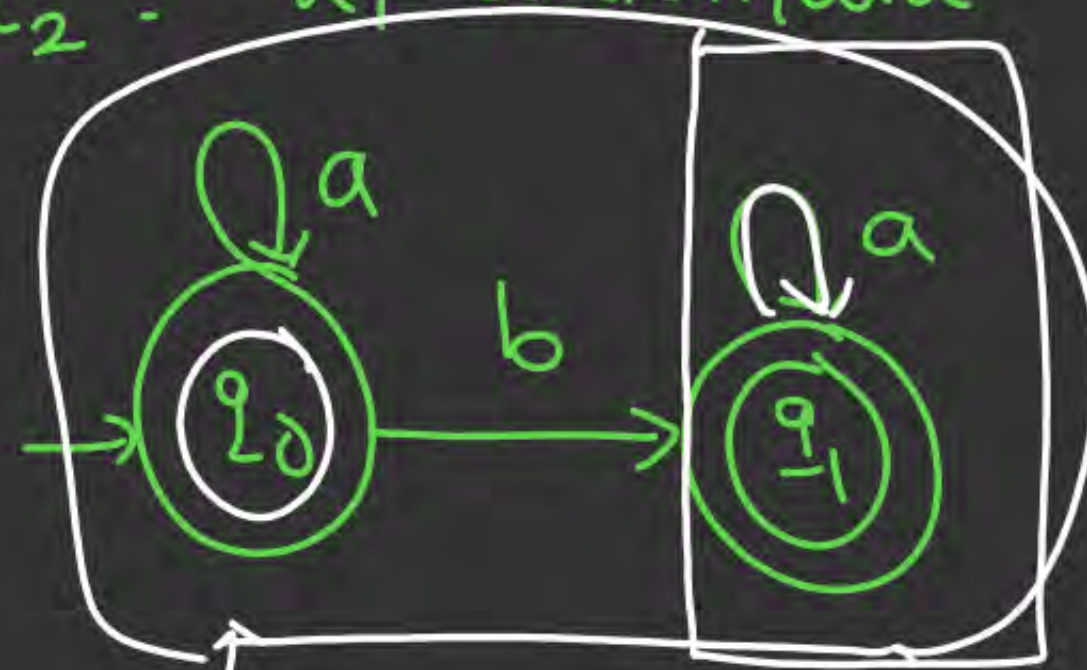
~~(c) $a^* \underline{b} a^* + a^*$~~

~~(d) $a^* b^*$~~

$L_1/L_2 = L_1$ automata

accept

$L_1/L_2 =$



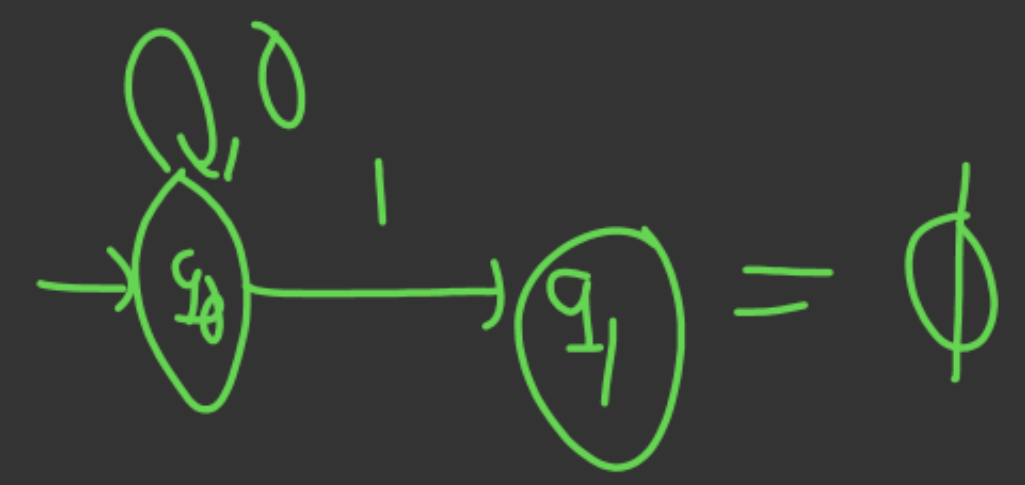
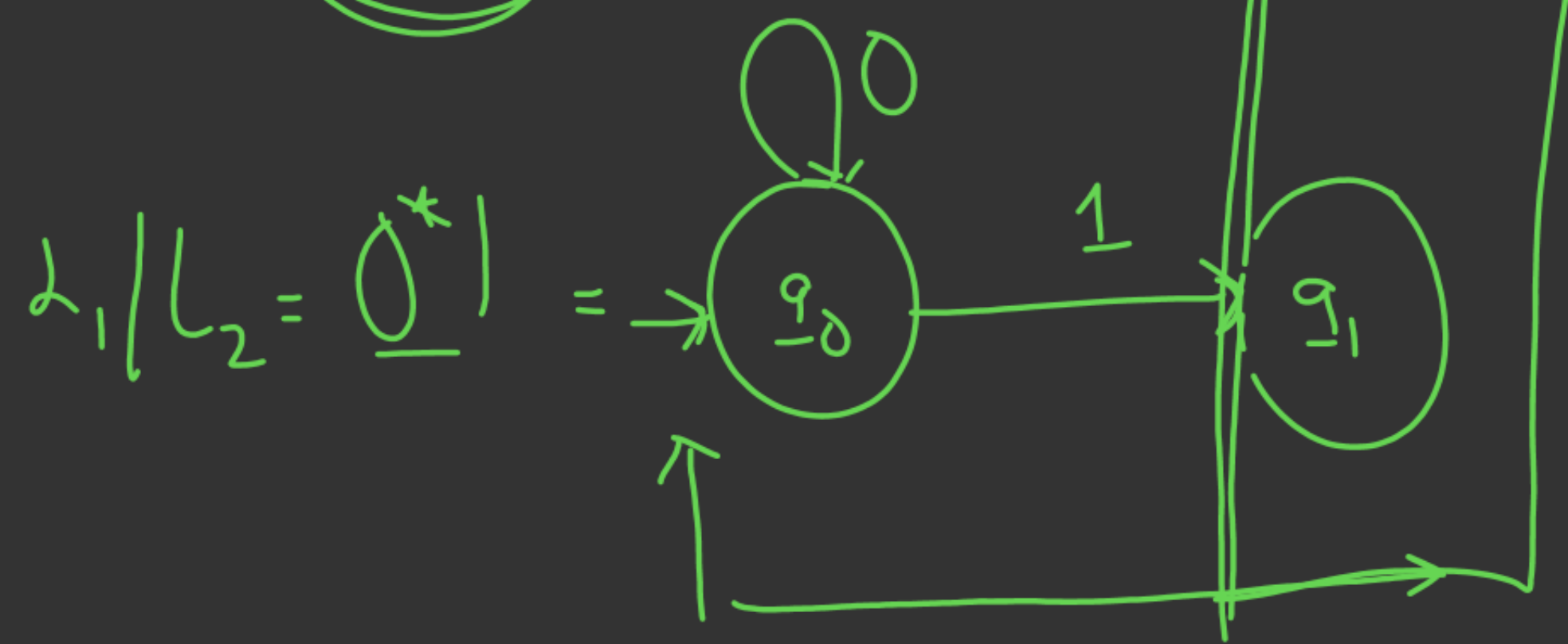
$a^* + a^* b a^*$

$L_1/L_2 = L_1$ finite Automata

L_1/L_2 Finite Automata

- ① Starting from first state Construct total automata if this automata accepts any string of L_2 then make first state of final.
- ② Starting from 2nd state Construct total automata if this automata accepts any string of L_2 then make 2nd state of final.
- ③ Repeat this process for every state to decide that state is final (or) not.

$$\frac{L_1}{L_2} = \frac{0^*1}{\boxed{1^*0}} = \emptyset \quad \underline{N.F.}$$



Substitution op :-

Substitution is a mapping from $\Sigma \rightarrow \Delta$ where each symbol of Σ is replaced by Regular Language over the alphabet Δ .

$$1) S(\emptyset) = \emptyset$$

$$2) S(\epsilon) = \epsilon$$

$$3) S(a+b) = S(a) + S(b)$$

$$4) S(a \cdot b) = S(a) \cdot S(b)$$

$$5) S(a^*) = (S(a))^*$$

(Q)

$$L = \emptyset^*$$

$$S(\emptyset) = (a+b)(a+b)$$

$$S(L) = ? \left[(a+b)(a+b) \right]^*$$

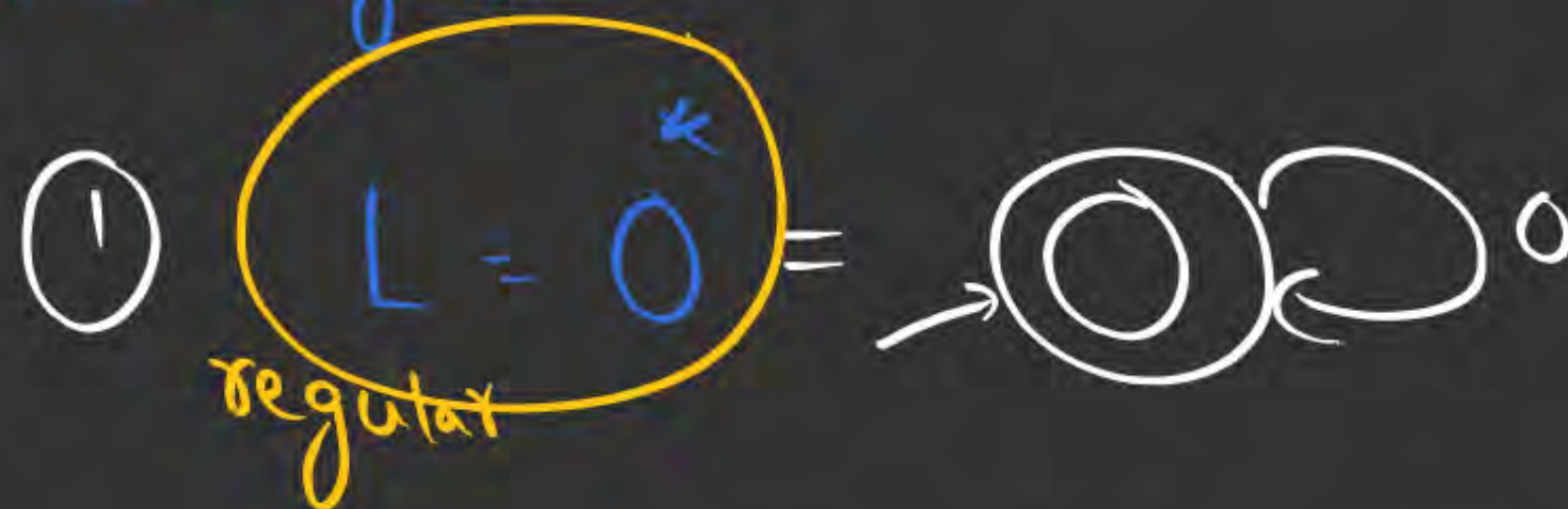
$$S(\emptyset) = \underline{\text{length exactly 2}}$$

✓ closed

$$S(\emptyset^*)$$

$$\left[S(\emptyset) \right]^*$$

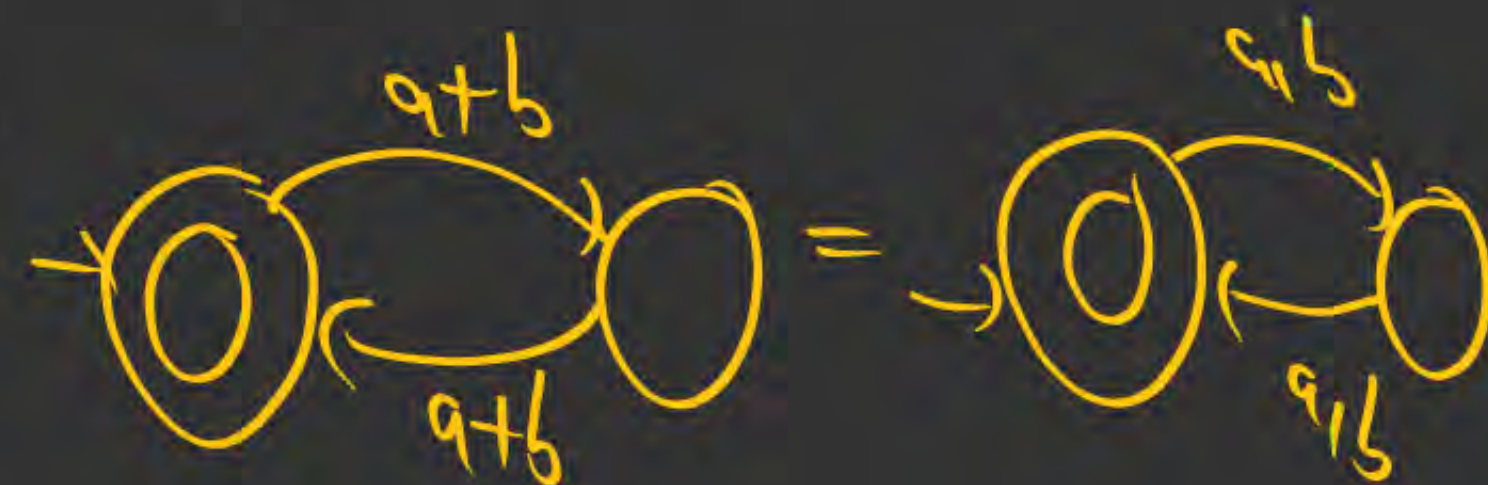
length div by 2



$$\left[(a+b)(a+b) \right]^*$$



$S(L) =$
Always Regular



Homomorphism op:

Homomorphism is a special case Substitution where each symbol of Σ is replaced by a single string over the alphabet Δ .

(Q)

$$\Sigma = \{0, 1\} \quad \Delta = \{a, b\}$$

$$h(0) = aa$$

$$h(1) = \epsilon$$

closed

$$L_1 = 1^* 0 1^*$$

$$h(L_1) = ?$$

$$h(L_1) = h(1^* 0 1^*)$$

$$= \underline{h(1)^*} \underline{h(0)} \underline{h(1)^*}$$

$$= \epsilon^* aa \epsilon^* = aa$$

$$h(L_1) = aa$$

Inverse Homomorphism:

Applying homomorphism in reverse way is known as
inverse homomorphism
(string is replaced by symbol)

$$h(\underline{a}) = \underline{aa}$$

(Q)

$$\Sigma = \{a, b, c\}$$
$$\left\{ \begin{array}{l} h(a) = 0 \\ h(b) = 1 \\ h(c) = 10 \end{array} \right\}$$

$$\Delta = \{0, 1\}$$

	b	a	b	a
L =	1	0	1	0
	c	b	a	

closed

$$h^{-1}(L) = ?$$

$$h^{-1}(L) = \{cc, baba, cba, bac\}$$

[MCQ]



#Q. Let R_1 and R_2 be regular sets defined over the alphabet then

L
 $\Sigma^* - L$

A

regular

$R_1 \cap R_2$ is not regular \rightarrow false

C

$\Sigma^* - R_1$ is regular \rightarrow true

Complement
Regular

B

regular

$R_1 \cup R_2$ is not regular \rightarrow false

D

R_1^* is not regular \rightarrow false

regular

[MCQ]



#Q. If $L_1 = \{a^n \mid n \geq 0\}$ and $L_2 = \{b^n \mid n \geq 0\}$, consider

I. $L_1 \cdot L_2$ is a regular language

II. $L_1 \cdot L_2 = \{a^n b^n \mid n \geq 0\}$

Which one of the following is CORRECT?

A

Only I

B

Only II

C

Both I and II

D

Neither I nor II

regular a^*

regular b^*

$a^* b^*$ → true

→ false

$L_1 \cdot L_2 = \{a^n b^n\}$

$L_1 \cdot L_2 = a^* b^*$

$\{a^n b^m\}$

Q

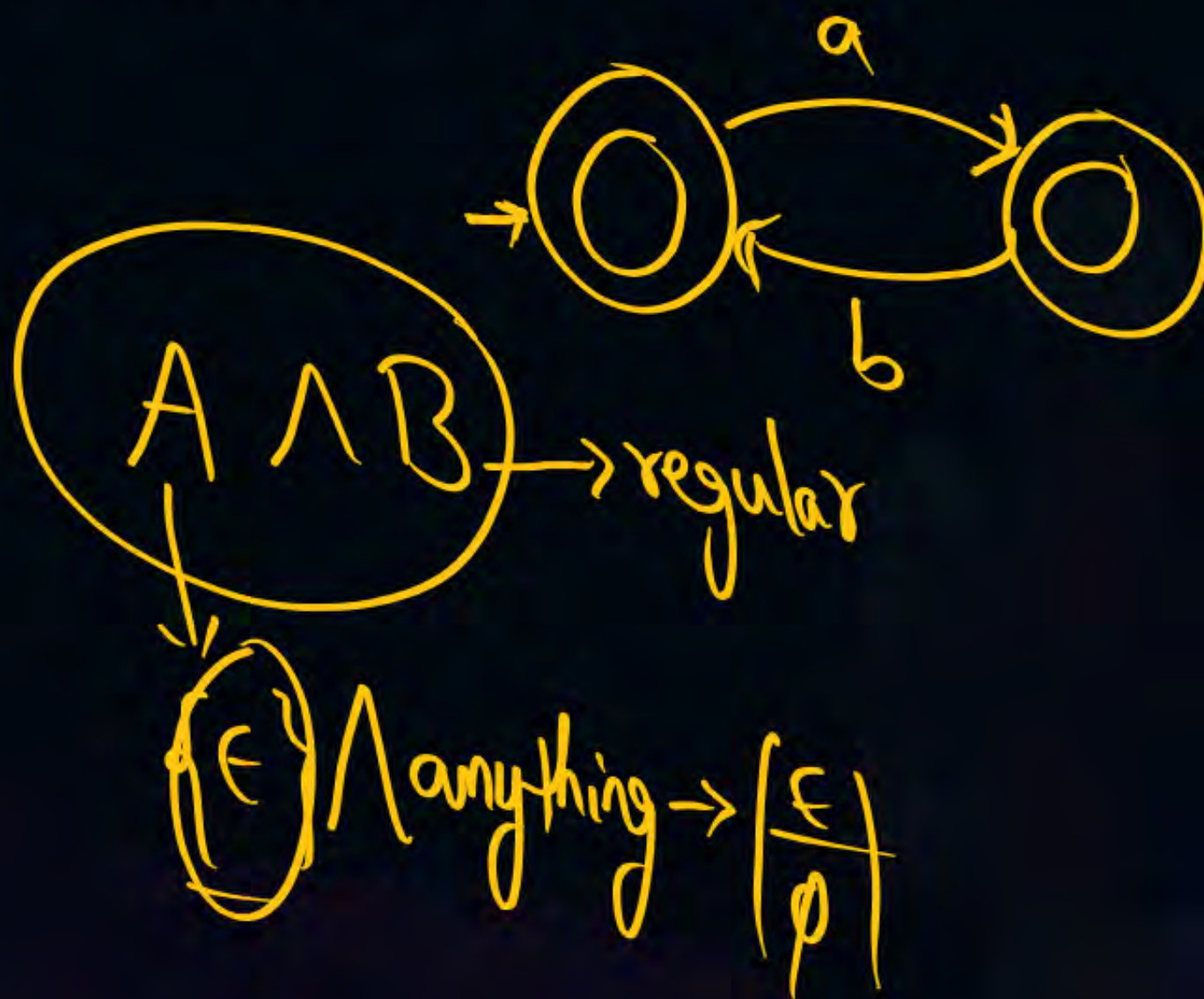
Consider the following two statements:

- I. If all states of an ^{DFA} NFA are accepting states then the language accepted by the NFA is Σ^* . *false*
- II. There exists a regular language A such that for all language B, $A \cap B$ is regular. *true*

[2016-Set2: 2 Marks]

Which one of the following is CORRECT

- A Only I is true
- B Only II is true
- C Both I and II are true
- D Both I and II are false



Q

Consider the following statements:

- I. ~~True~~ If $L_1 \cup L_2$ is regular, then both L_1 and L_2 must be regular.
- II. The class of regular languages is closed under infinite union. } false

$(a+b)^* \cup \{a^n b^n\} \rightarrow \text{regular}$
 $(a+b)^*$

Which of the above statements is/are TRUE?

[2020: 1 Mark]

- A Neither I nor II
- B II only
- C I only
- D Both I and II

~~not closed~~

Infinite Union

$\{L_1, L_2, L_3, L_4, \dots\}$ regular

$$\begin{array}{ccccccc} L_1 \cup L_2 \cup L_3 \cup L_4 \cup L_5 \dots & = & \text{may (a) may not} \\ \downarrow \quad \downarrow \quad \downarrow \quad \downarrow & & \text{regular} \\ \underline{\{ab\}} \cup \underline{\{a^2b^2\}} \cup \underline{\{a^3b^3\}} \cup \underline{\{a^4b^4\}} \cup \dots & = & \underline{\{a^n b^n \mid n \geq 1\}} \\ & & \underline{\text{non regular}} \end{array}$$

Regular languages closed under finite Union

not closed under infinite Union

Infinite Intersection of (not closed)

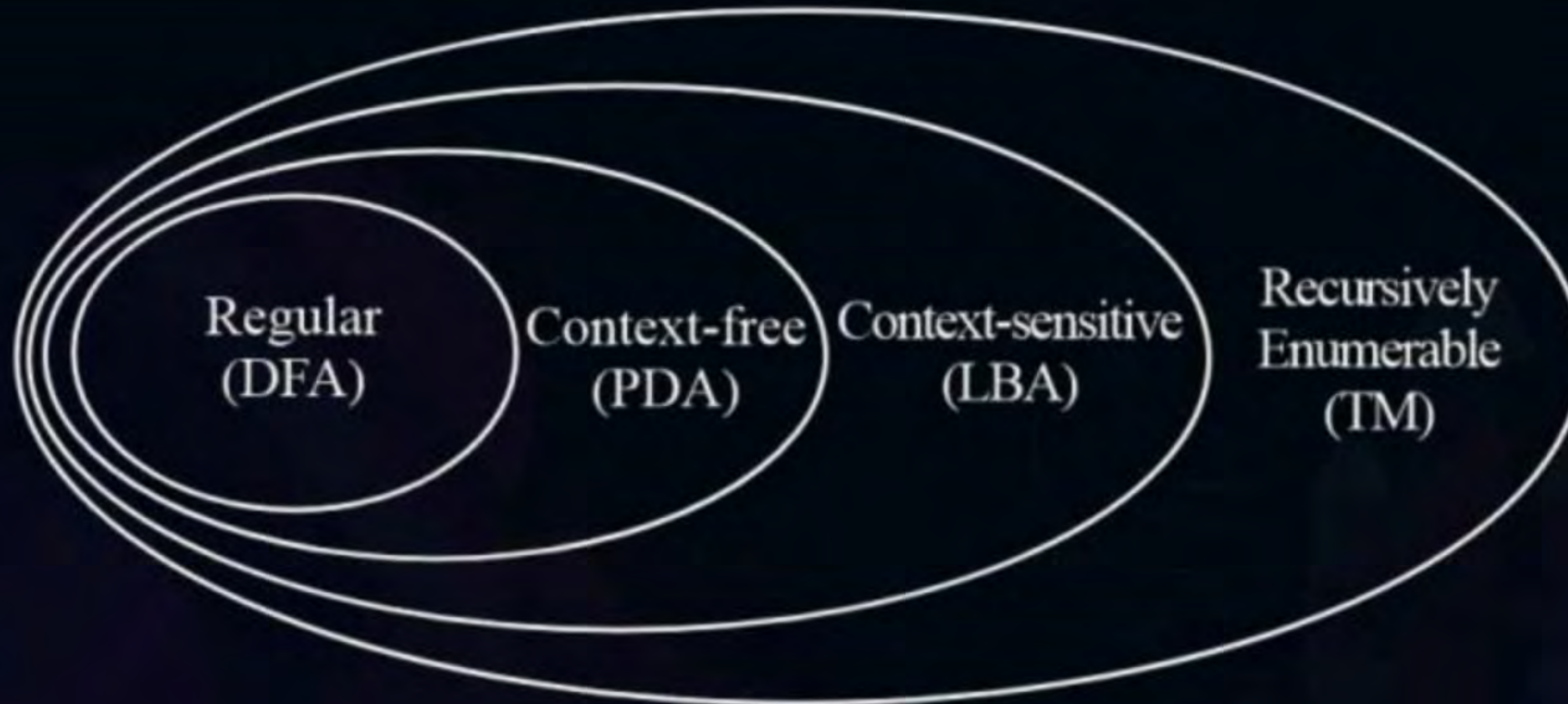
$$L_1 \cap L_2 = L_1^c \cup L_2^c$$

$$L_1 \cap L_2 \cap L_3 \cap L_4 \dots = L_1^c \cup L_2^c \cup L_3^c \cup L_4^c \dots$$

↓
Infinite Union



Topic : Regular expression



Circuit Design

0101

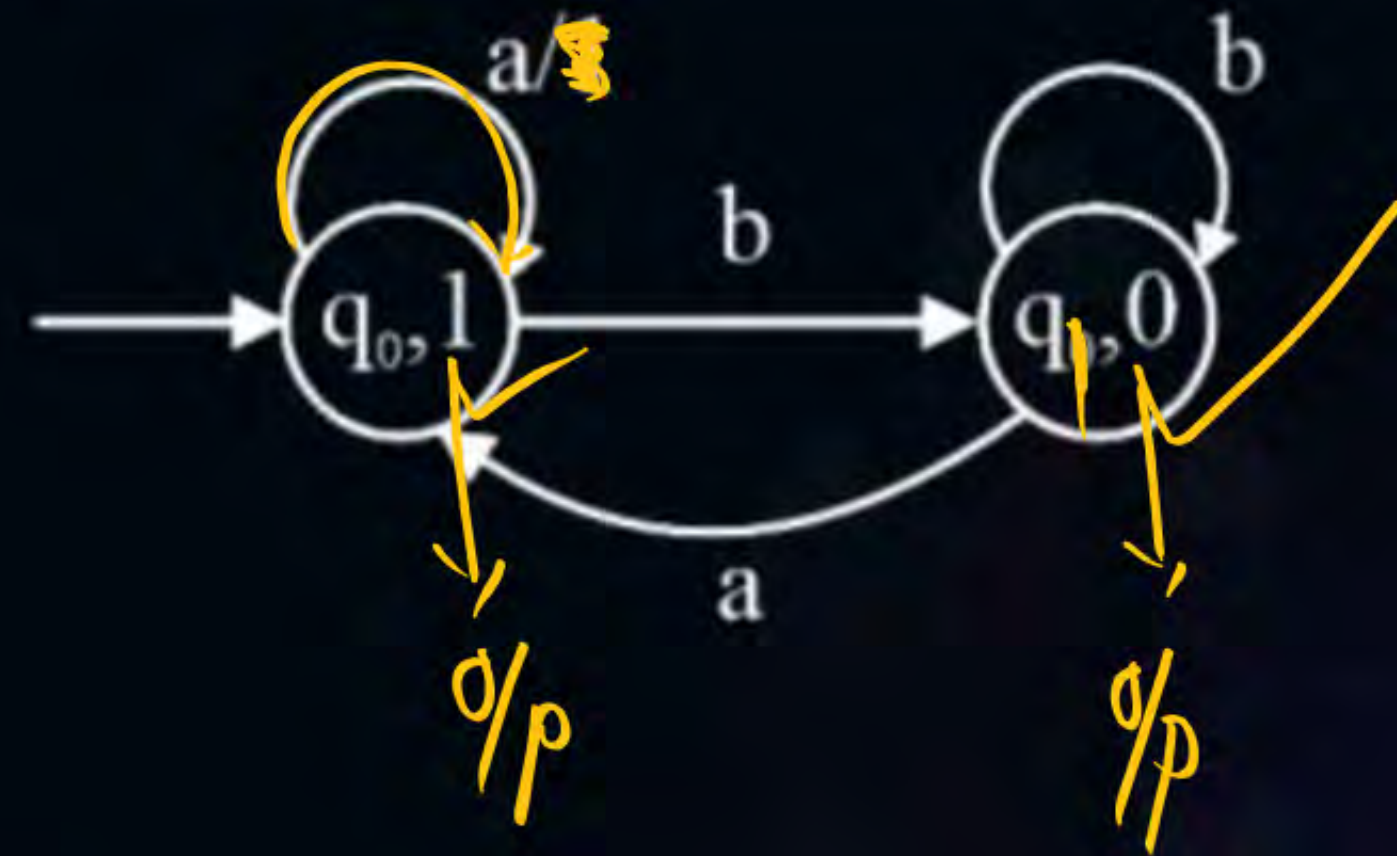
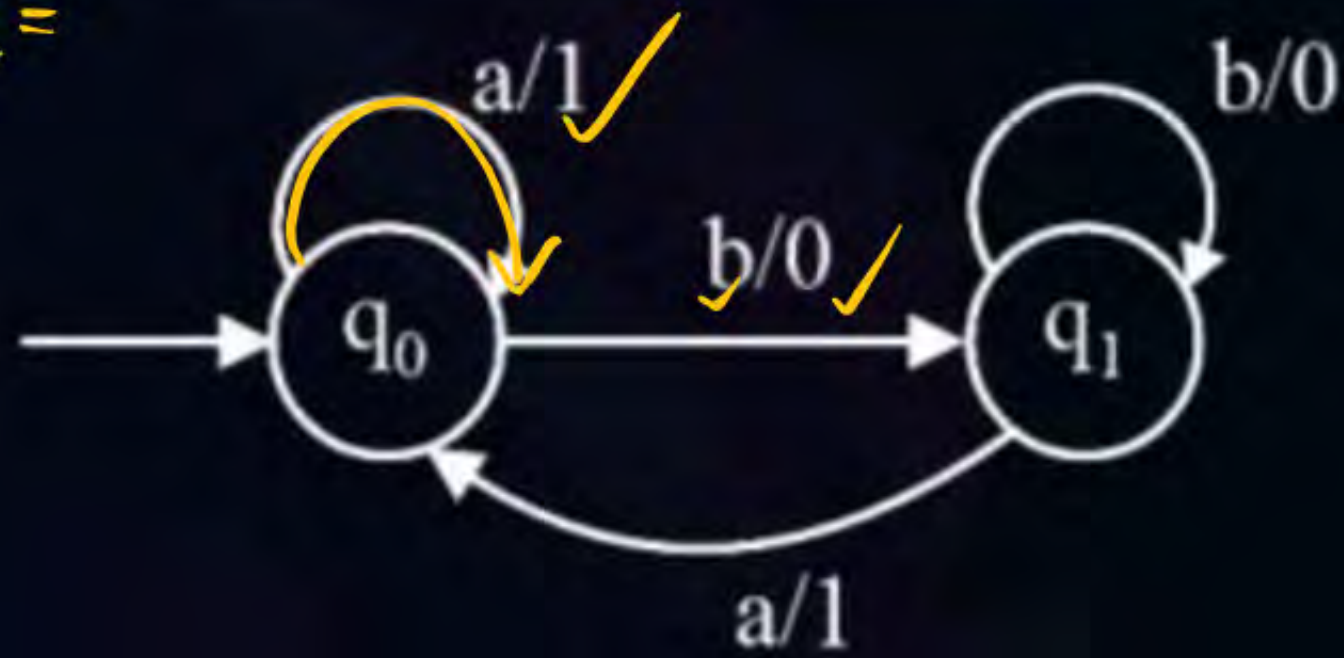
F.A with output

1010

Mealy Machine ✓

Moore Machine

$\alpha =$



- Mealy Machine:

- It is a mathematical model in which output is associated
- with transition.

- Moore Machine:

- It is a mathematical model in which output is associated
- with state.

Formal Definition

$$\boxed{Q, \Sigma, q_0, \delta, \Delta, \lambda} \left. \vphantom{\begin{matrix} Q, \Sigma, q_0, \delta, \Delta, \lambda \end{matrix}} \right\} \begin{matrix} \text{Mealy} \\ \text{Moore} \end{matrix}$$

Q : finite no. of states

Σ : input alphabet

q_0 : initial state

δ : transition function: $\boxed{Q \times \Sigma \rightarrow Q}$

Δ : output alphabet

λ : output function

Mealy: $\lambda: Q \times \Sigma \rightarrow \Delta$

Moore: $\lambda: Q \rightarrow \Delta$

[NAT]

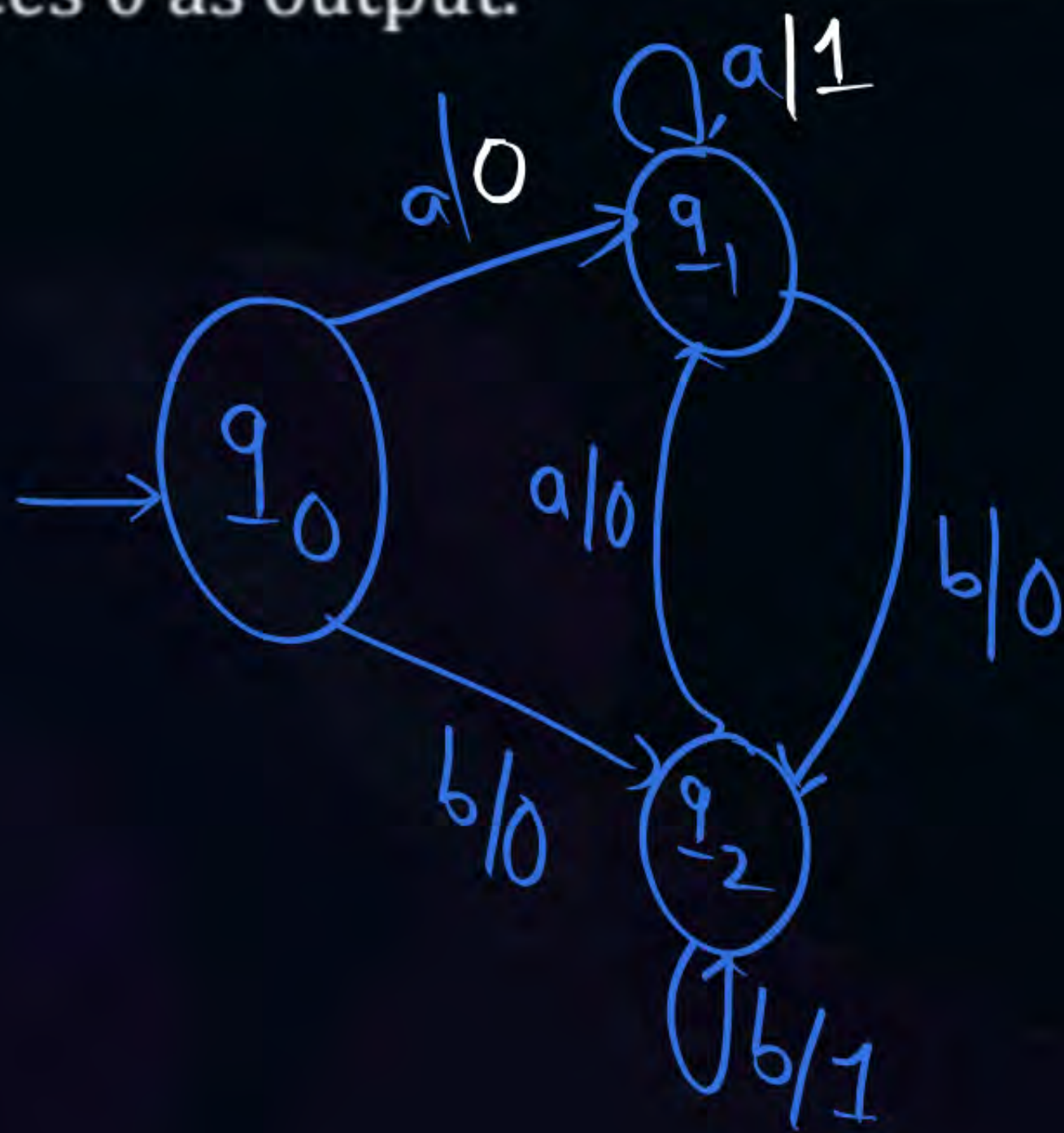
$$Q \times \Sigma \rightarrow Q$$

$$\Sigma = \{a, b\}$$

$$\Delta = \{0, 1\}$$



#Q. Construct mealy machine that takes all strings of a's and b's as input and produces 1 as output if last two symbols in the input are same otherwise produces 0 as output.



aaaaa → 1

$\left. \begin{matrix} a \\ b \\ ab \end{matrix} \right\} \rightarrow 0$

bbbaa

aaabb
0

[NAT]

Home Work



#Q. Construct mealy machine that takes all strings of a's and b's as input and produces 1 as output if last two symbols in the input are different otherwise produces 0 as output.

Home Work

- #Q. Construct mealy machine that takes all strings of 0's and 1's as input and produces A as output if input ending with 10 or produces B as output if input ending with 11 otherwise produces output C.

[NAT]

Home Work



#Q. Construct mealy machine that produces 1's complement of given binary number as output.

#Q. Construct mealy machine that produces 2's complement of given binary number as output.(assume we are reading string from LSB to MSB)

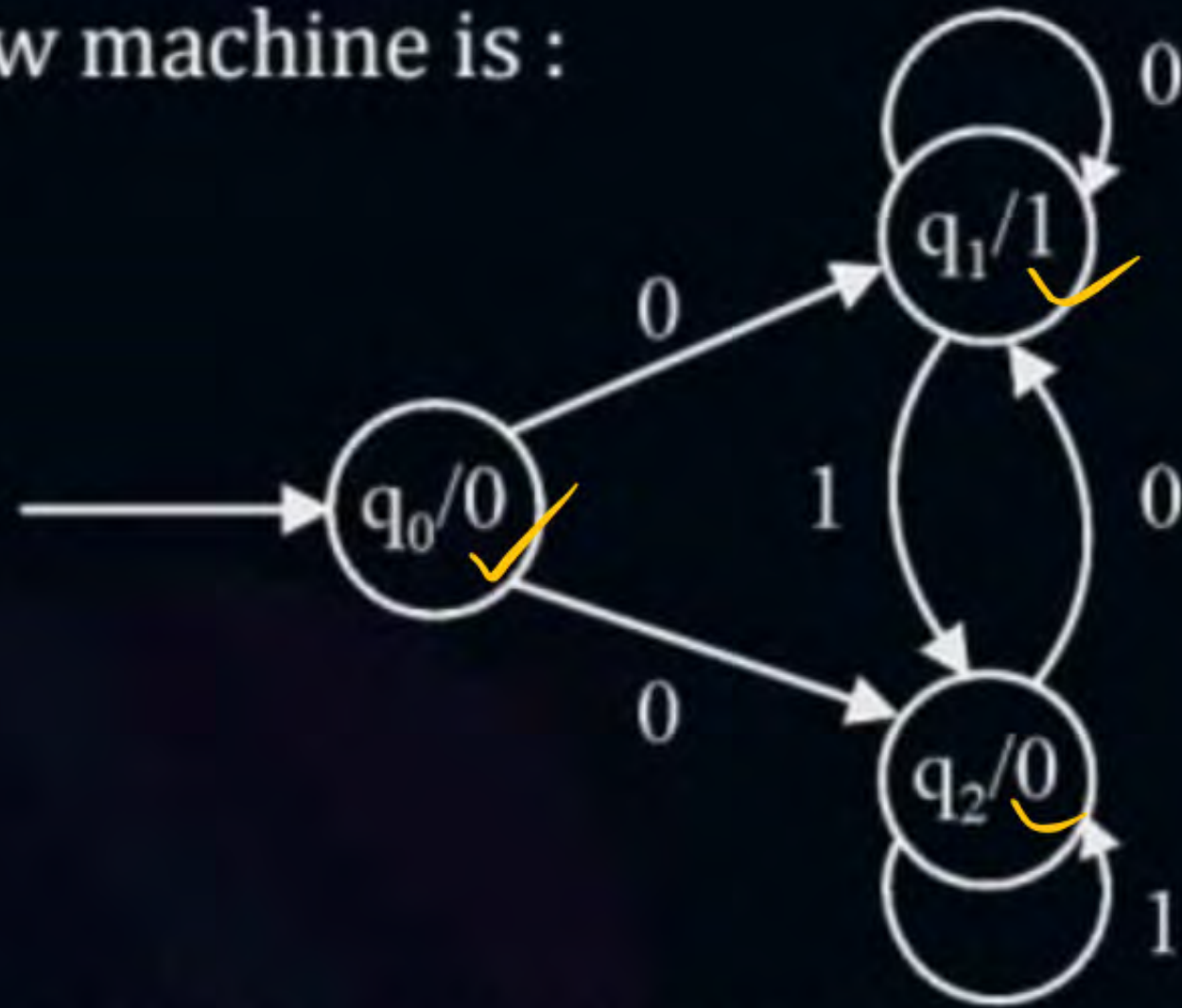
- #Q. Construct Moore machine that takes all binary strings as input and produces Residue modulo 4 as output.

- #Q. Construct Moore machine that takes all binary strings as input and produces Residue modulo 5 as output.

#Q. Construct Moore machine that takes all base 3 numbers as input and produces Residue modulo 4 as output.

[MCQ]

#Q. The below machine is :



A

A Mealy machine to find 2's complement of a number

B

A Moore machine to find 2's complement of a number

C

A Mealy machine to find 1's complement of a number

D

A Moore machine to find 1's complement of a number

#Q. A finite state machine with the following state table has a single input x and a single output z .

	Present state $x = 1$	Next state, z $x = 0$
A	D, 0	B, 0
B	B, 1	C, 1
C	B, 2	D, 1
D	B, 1	C, 0

If the initial state is unknown, then the shortest input sequence to reach the final state C is :

A

0 1

B

1 0

C

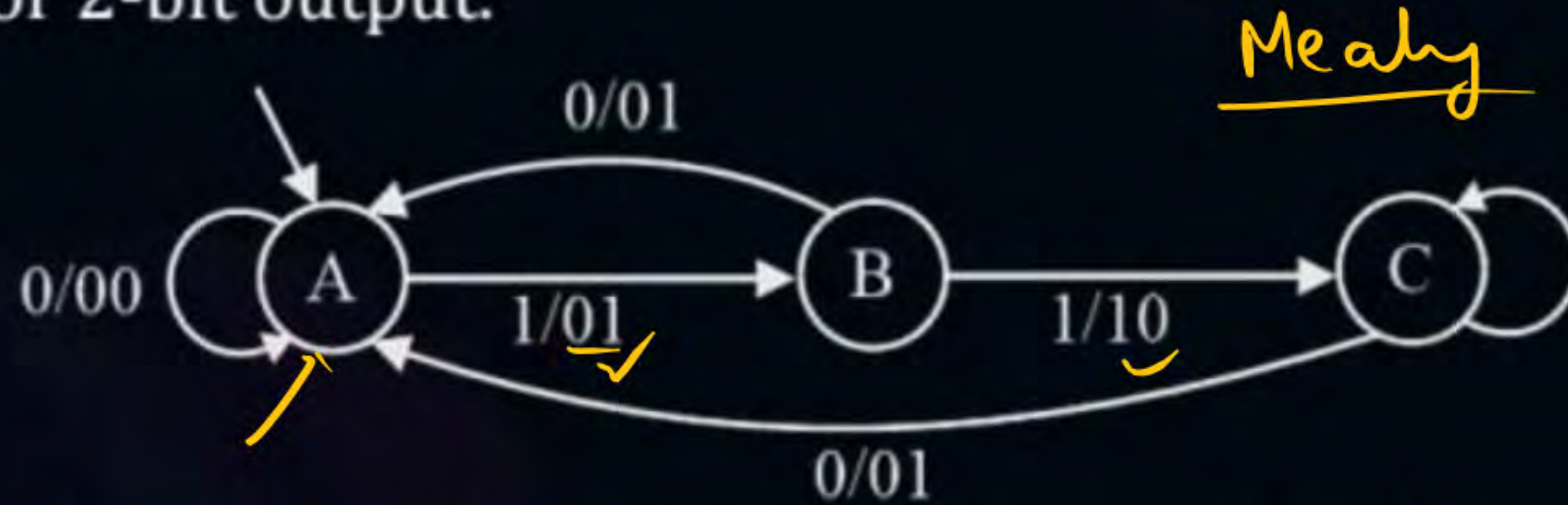
1 0 1

D

1 1 0

[MCQ]

#Q. The Finite state machine described by the following state diagram with A as starting state, where an arc label is x/y and x stands for 1-bit input and y stands for 2-bit output.



- A** Outputs the sum of the present and the previous bits of the input
- B** Outputs a "01" whenever the input sequence contain "11"
- C** Outputs a "00" whenever the input sequence contains "10"
- D** None of the above



2 mins Summary



Topic

One

Topic

Two

Topic

Three

Topic

Four

Topic

Five



THANK - YOU