COMPUTER SCIENCE & IT

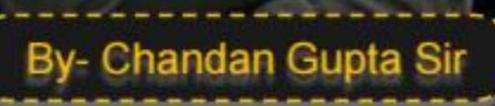






Lecture No. 12

Combinational Circuit







MUX &	DEMVX		

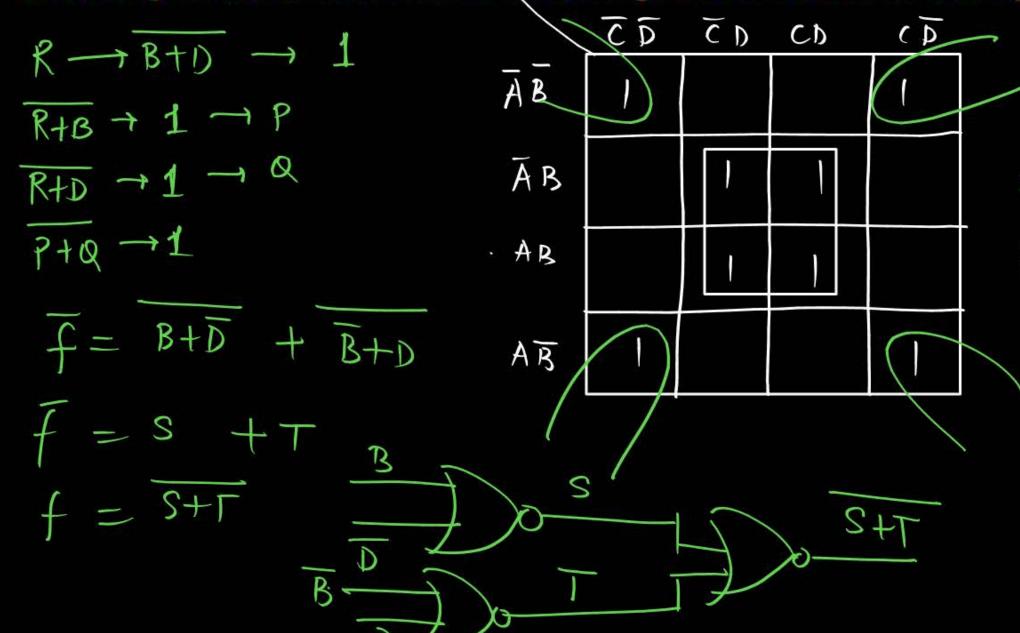




Question	Practice		

[NAT]

What is the minimum number of 2-input NOR gates required to implement a 4-variable function expressed in sum-of-minterms form as $f = \Sigma(0, 2, 5, 7, 8, 10, 13, 15)$? Assume that all the inputs and their complements are available.



$$\frac{[GATE-2019-CS: 2M]}{[AB+AB]=(A+B)(A+B)}$$

$$= BD + \overline{B}\overline{D} = BOD = (\overline{B} + D)(B + \overline{D})$$

$$= \overline{B}\overline{D} + BD = \overline{B} + D + BD$$

$$f = R + BD = (R + B)(R + D)$$

$$f = \overline{R} + \overline{B} + \overline{R} + \overline{D}$$

$$f = \overline{R} + \overline{B} + \overline{R} + \overline{D}$$

$$f = \overline{R} + \overline{B} + \overline{R} + \overline{D} = \overline{D} + \overline{D}$$

Pw

Let \oplus denote the Exclusive OR (XOR) operation. Let '1' and '0' denote the binary constants. Consider the following Boolean expression for F over two variables P and Q:

QOP = POQ = POQ

$$F(P, Q) = ((1 \oplus P) \oplus (P \oplus Q)) \oplus ((P \oplus Q) \oplus (Q \oplus 0))$$

The equivalent expression for F is

[GATE-2014-CS: 1M]

$$A P + Q$$

$$B \quad (P+Q)'$$

$$\circ$$
 $P \oplus Q$

$$\mathbb{D}$$
 $(P \oplus Q)'$

Given f1,f3 and f in canonical sum of products from (in decimal) for the circuit.

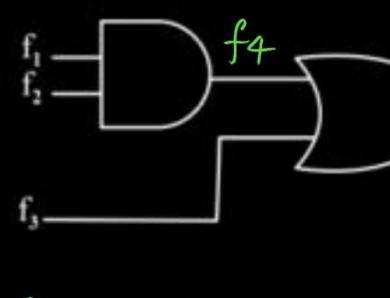
$$F_1 = \sum m(4,5,6,7,8)$$

$$F_3 = \sum m (1, 6, 15)$$

$$F = \sum m (1, 6, 8, 15)$$

Then f_2 is

- Σ m (4, 6) \times
- Σ m (4, 8) \times
- $\sum m (6, 8)$
- $\sum m (4, 6, 8)$



$$f_2 = \overline{2}$$
, g

$$\geq 1,8, \leq 1,6,8 \leq 1,8,15)$$

5 1,6,

[GATE-2008-CS: 1M]



A logical function f(x, y, z) is given as

$$f(x,y,z) = x\overline{y} + yz$$

HW

Another logical function

$$f_1(x,y,z) = f[\overline{f},f,z]$$
 will be
 $a.\overline{x} + y + z$
 $b.\overline{x}y + yz$
 $c.(x + \overline{y})(\overline{y} + \overline{z})$
 $d.(x + \overline{y})(\overline{y} + \overline{z})(y + z)$



A 3-input majority gate is defined by the logic function

M (a, b, c) = ab + bc + ca. Which one of the following gate is

$$M(\overline{M(a,b,c)},M(a,b,\overline{c}),c)$$
?

- (a) 3-input NAND gate
- (b) 3-input XOR gate
- (c) 3-input NOR gate
- (d) /3-input XNOR gate

$$M(a_1b,C) = ab+bc+ca = \Xi(3,5,6,7)$$

 $f_1 = \overline{M(a_1b,C)}, f_2 = M(a_1b,C) = 2476$
 $M(f_1,f_2,C) = f_1f_2 + f_2C + f_1C$

$$f_1(a_1b_1) = \sum (0,1,2,4) f_2(a_1b_1c) = M(a_1b_1,\overline{c}) = \sum (2,4,6,7)$$

$$ab\bar{c} + \bar{a}bc + ab\bar{c} = f(a_1b,c)$$

 $f(a_1b,\bar{c}) - f(a_1b,d) =$

$$M(f_1,f_2,C) = f_1f_2 + f_1 \cdot (+f_2 \cdot C)$$

= $\Xi(2,4) + \Xi(1) + \Xi(7) = \Xi(1,2,4,7) = a \oplus b \oplus C$

$$f_1 = \sum_{i=1}^{n} (0,1,2,4) \cdot C = \sum_{i=1}^{n} (1)$$

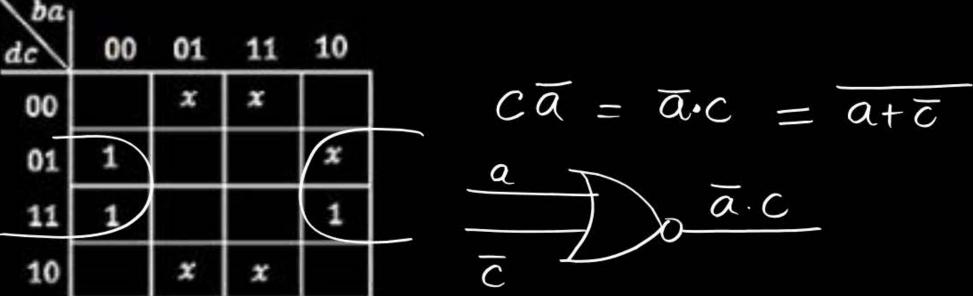
 $f_2 = \sum_{i=1}^{n} (2,4,6,7) \cdot C = \sum_{i=1}^{n} (7)$

[NAT]



Consider the Karnaugh map given below, where X represents "don't care" and

blank represents 0.



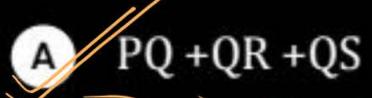
Assume for all inputs(a, b, c, d), the respective complements $(\bar{a}, \bar{b}, \bar{c}, \bar{d})$ are also available. The above logic is implemented using 2-input NOR gates only. The minimum number of gates required is ______. [GATE-2017-CS: 1M]



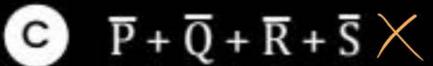
Consider the following Boolean expression for

$$F: F(P, Q, R, S) = PQ + P'QR + P'QR'S$$

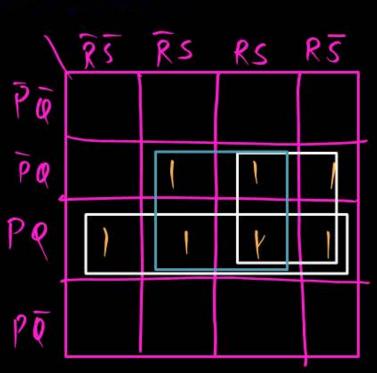
The minimal sum-of-products form of F is











[GATE-2014-CS: 1M]



Consider the following minterm expression for

F: F (P,Q,R,S) = $\sum 0$, 2, 5, 7, 8,10,13,15

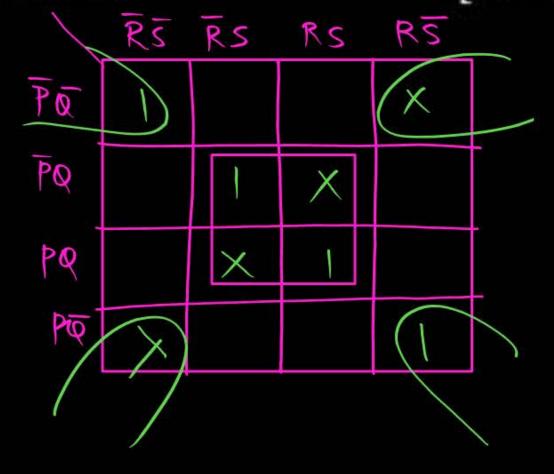
The minterms 2, 7, 8 and 13 are 'do not care' terms.

The minimal sum-of-products form for F is

[GATE-2014-CS: 1M]

A
$$Q\bar{S} + \bar{Q}S$$
 = $\bar{Q}\bar{S}$
B $\bar{Q}\bar{S} + QS$
C $\bar{Q}R\bar{S} + \bar{Q}R\bar{S} + QRS + QRS \times$

 $\overline{PQS} + \overline{PQS} + \overline{PQS} + \overline{PQS} \times$





What is the minimal form of the karnaugh map shown below? Assume that X denotes a don't care term

[GATE-2012-CS: M]

	77	٠,
A 1	n	$\boldsymbol{\alpha}$
A	-	

$$b\bar{d} + b\bar{c}$$

$$\bar{b}\bar{d} + a\bar{b}\bar{c}d$$

		-	
D	hd.	+ b c +	$-\overline{c}d$
	Du	T DC	cu

cd ab	00	01	11	10
00	1	×	×	1
01	×			1
11				
10	1			×

H-W



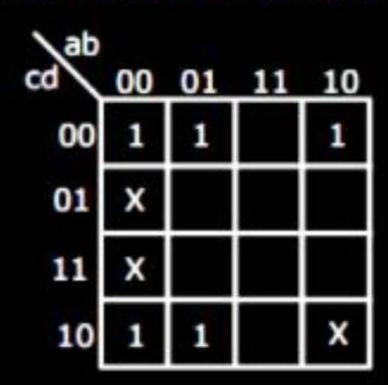
In the Karnaugh map shown below, X denotes a don't care term. What is the minimal form of the function represented by the Karnaugh map?

$$\bar{b}.\bar{d} + \bar{a}.\bar{d}$$

$$\bar{a}.\bar{b} + \bar{b}.\bar{d} + \bar{a}.b.\bar{d}$$

$$\bar{b}.\bar{d} + \bar{a}.b.\bar{d}$$

$$\bar{a}.\bar{b} + \bar{b}.\bar{d} + \bar{a}.\bar{d}$$



[GATE-2008-CS: M]

H-W

Consider the following Boolean function of four variables $f(A, B, C, D) = \Sigma(2, 3, 6, 7, 8, 9, 10, 11, 12, 13)$ The function is [GATE-2008-CS: 1M]

- A independent of one variable
- B Independent of two variables
- Independent of three variables
- Dependent on all the variables

HW.

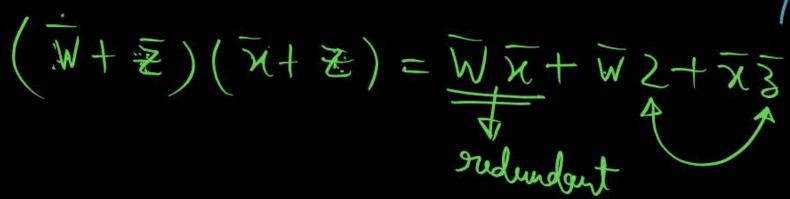
Given $f(w, x, y, z) = \Sigma m(0, 1, 2, 3, 7, 8, 10) + \Sigma d(5, 6, 11, 15)$, where d represents the don't-care condition in Karnaugh maps. Which of the following is a minimum product-of-sums (POS) form of f(w, x, y, z)? [GATE-2017-CS: 1M]

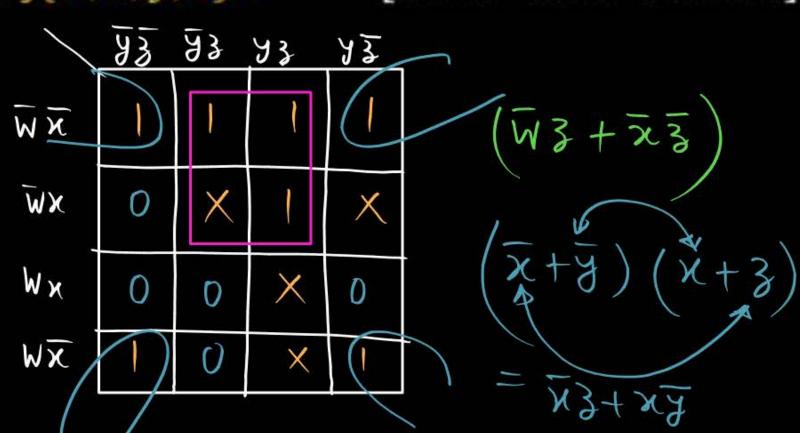
$$A f = (\overline{w} + \overline{z})(\overline{x} + z)$$

$$B \times f = (\overline{w} + \underline{z})(x + \underline{z}) \quad z + \overline{w} \times$$

$$\bigcirc \times f = (w+z)(\bar{x}+z) \ \forall \forall x$$

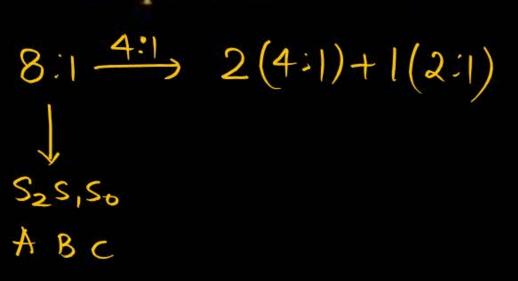
$$D / f = (w + \bar{z})(\bar{x} + z) /$$

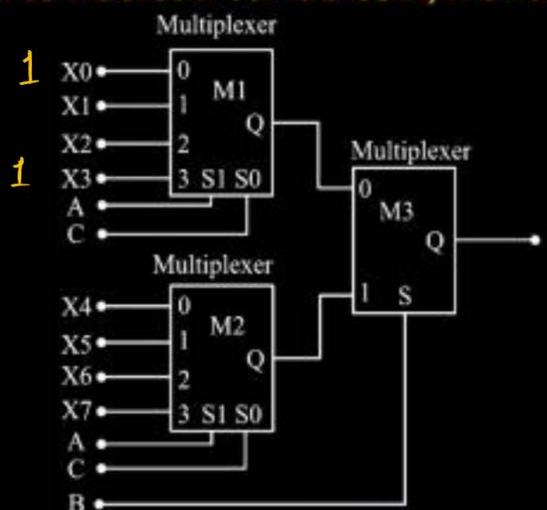






A Boolean digital circuit is composed using two 4-input multiplexers (M1 and M2) and one 2-input multiplexer (M3) as shown in the figure. X0-X7 are the inputs of the multiplexers M1 and M2 and could be connected to either 0 or 1. The select lines of the multiplexers are connected to Boolean variables A, B and C as shown.





$$f(A,B,C) = \overline{A} + \overline{A} \overline{C} + A \overline{B} C$$

 $f(0,0,0) = 1$, $x_0 = 1$
 $f(1,0,1) = 1$, $x_3 = 1$

Which one of the following set of values of (X0, X1, X2, X3, X4, X5, X6, X7) will realise the Boolean function $\overline{A} + \overline{A} \, \overline{c} + \overline{A} \, \overline{B} \, c$. [GATE-2023-CS: 2M]



$$X_i = 1$$

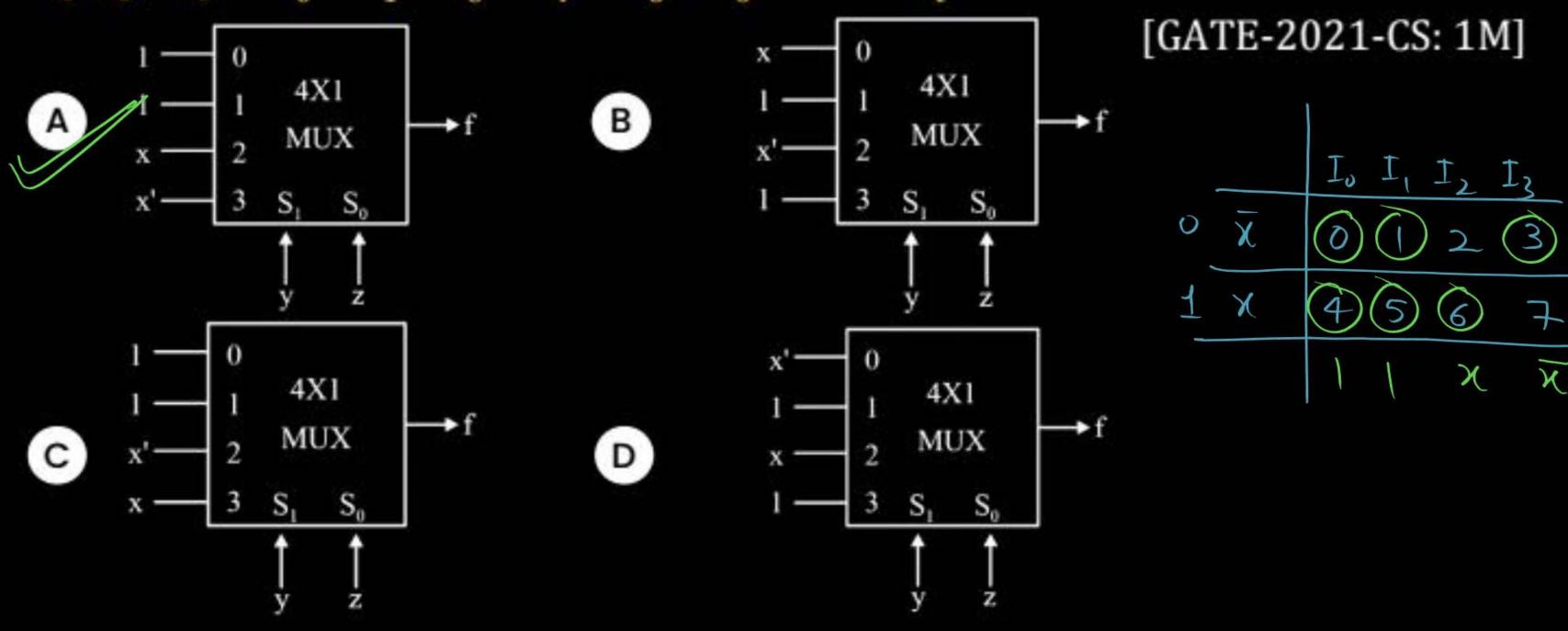
$$X_3 = 1$$

- A $(1, 1, 0, 0, 1, 1, 1, 0) \times$
- B (1, 1, 0, 0, 1, 1, 0, 1) X
- c (1, 1, 0, 1, 1, 1, 0, 0)
- 0, 0, 1, 1, 0, 1, 1, 1)



Which one of the following circuits implements the Boolean function given below?

 $f(x^2, y^2, z^3) = m_0 + m_1 + m_3 + m_4 + m_5 + m_6$ where m_t is the ith minterm.



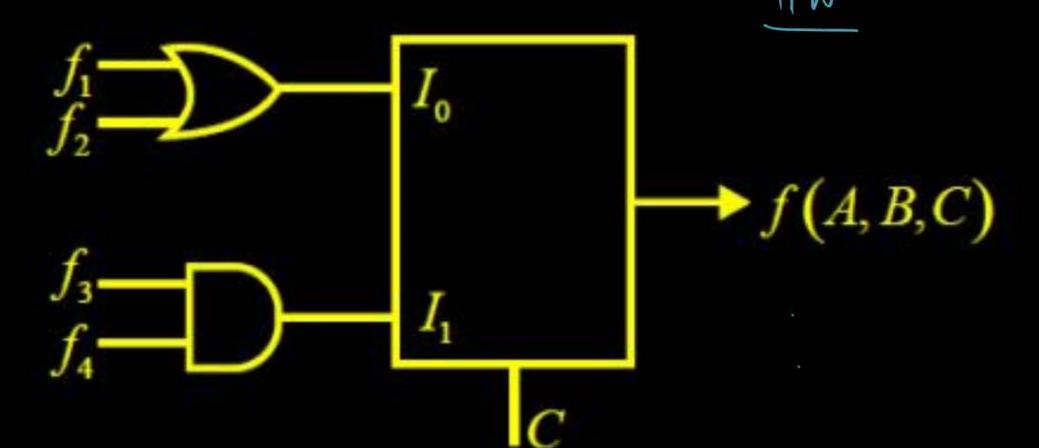


A digital circuit is as shown below:

When
$$f_1(A, B) = \Sigma(0, 1) f_2(A, B) = \Sigma 1 = f_3(A, B) = \Sigma(0, 2, 3)$$
,

$$f_4(A, B) = \Sigma(1,2,3)$$
 then $f(A, B, C)$ will be

- (a) $A \oplus C$
- (b) A ⊙ C
- (c) $A \oplus B \oplus C$
- (d) A ⊙ B ⊕ C



[NAT]



A multiplexer is placed between a group of 32 registers and an accumulator to regulate data movement such that at any given point in time the content of only one register will move to the accumulator. The number of select lines needed for the multiplexer is ____.

[GATE-2020-CS: 1M]

HW

The Boolean function realized by the logic circuit shown is

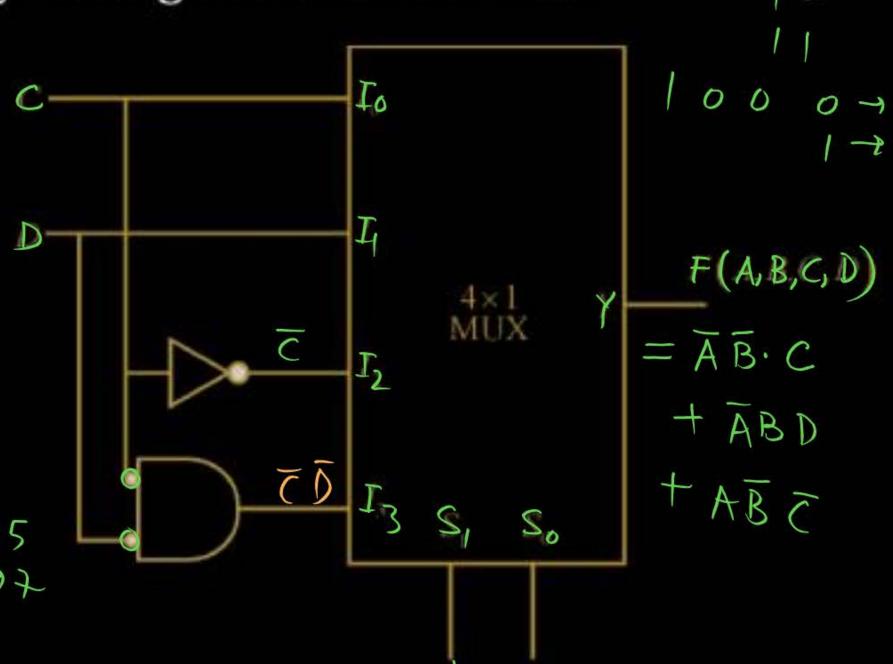
(a)
$$F = \Sigma m(0,1,3,5,9,10,14)$$

(b)
$$F = \Sigma m(2,3,5,7,8,12,13)$$

(c)
$$F \neq \Sigma m(l,2,4,5,ll,14,15)$$

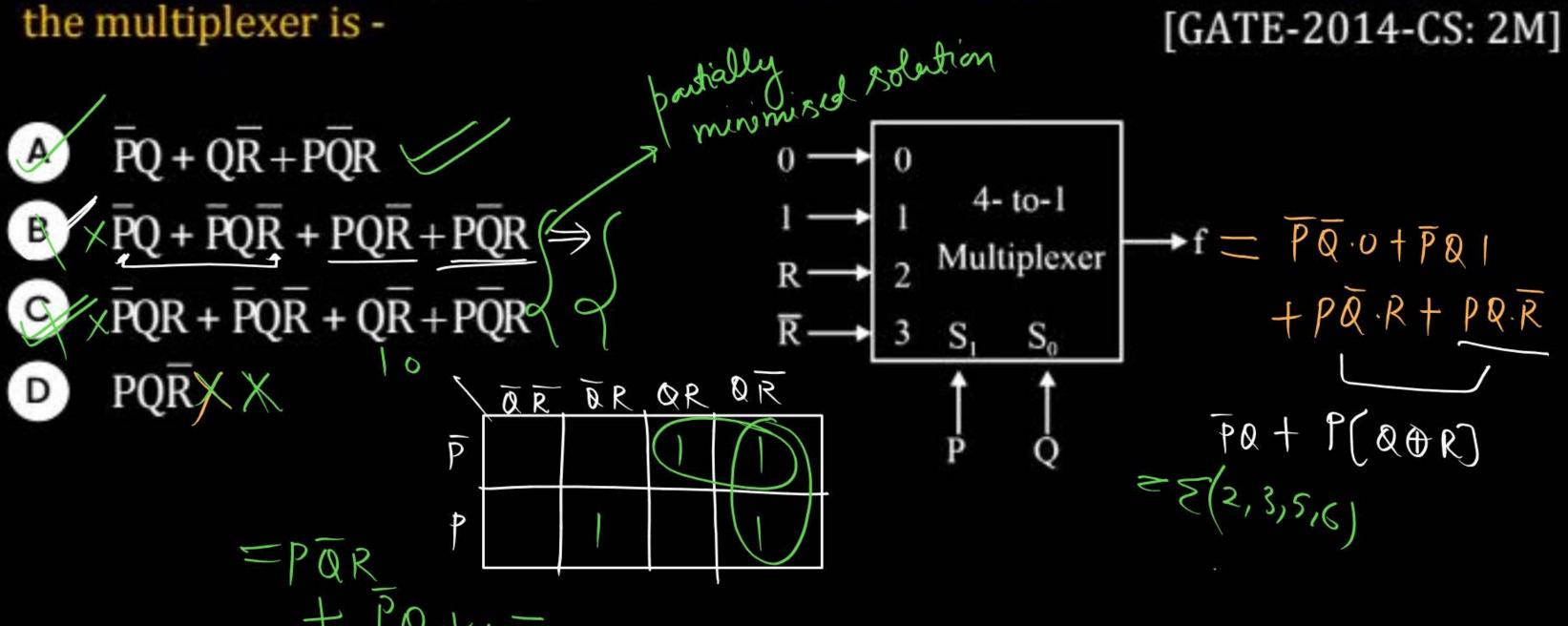
(d)
$$F = \Sigma m(2,3,5,7,8,9,12)$$

$$0010-3$$
 010 0





Consider the 4 – to – 1 multiplexer with two select S_1 and S_0 given below. The minimal sum of products from the Boolean expression for the output F of the multiplexer is - [GATE-2014-CS: 2M]

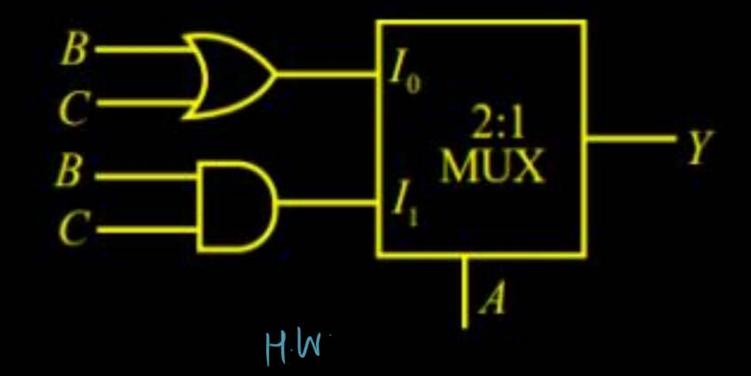




Logical circuit is implemented as

Y represents

- (A)Borrow output of full subtractor
- (B) Carry output of full adder
- (C) 3-input XOR gate
- (D) 3-input NOR gate





Consider the two cascaded 2 to 1 multiplexers as shown in the figure. [GATE-2016-CS: 1M]

The minimal sum of products form of the output X is:

$$\overline{P}\overline{Q} + PQR$$

$$\overline{PQ} + QR$$

$$\sim$$
 PQ + \overline{PQR}

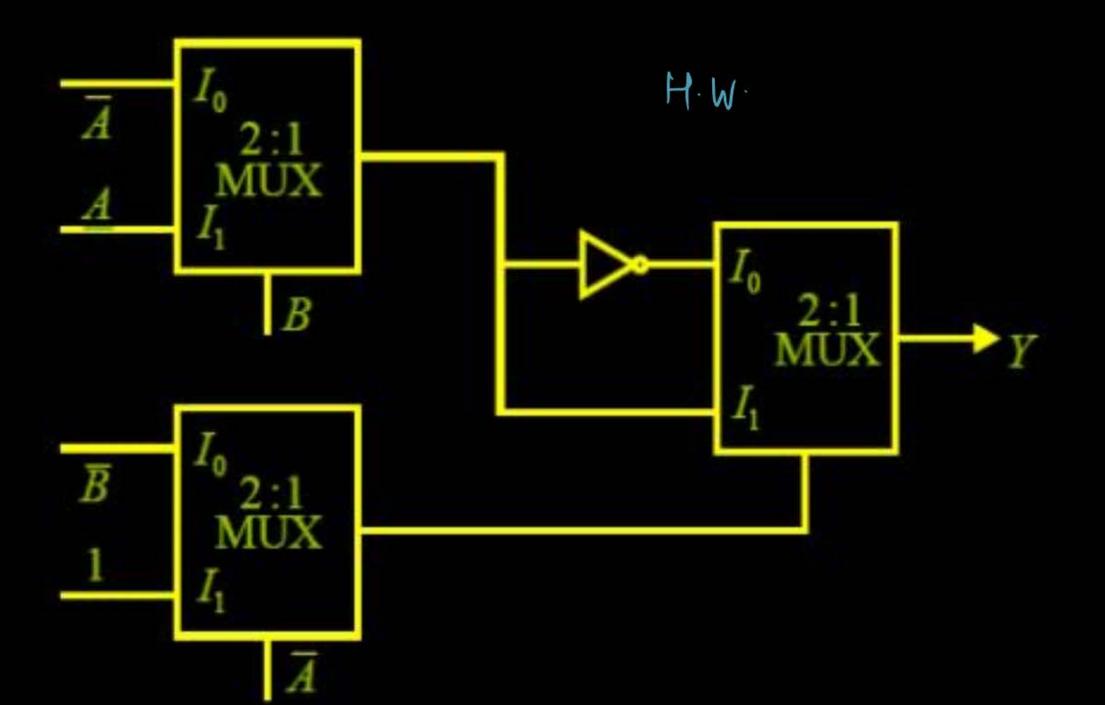
$$\overline{QR} + PQR$$



Combination circuit in designed as shown below:-

Output Y is

- (a) 2 input NOR gate
- (b) 2 input AND gate
- (c) 2 input OR gate
- (d) 2 input NAND gate



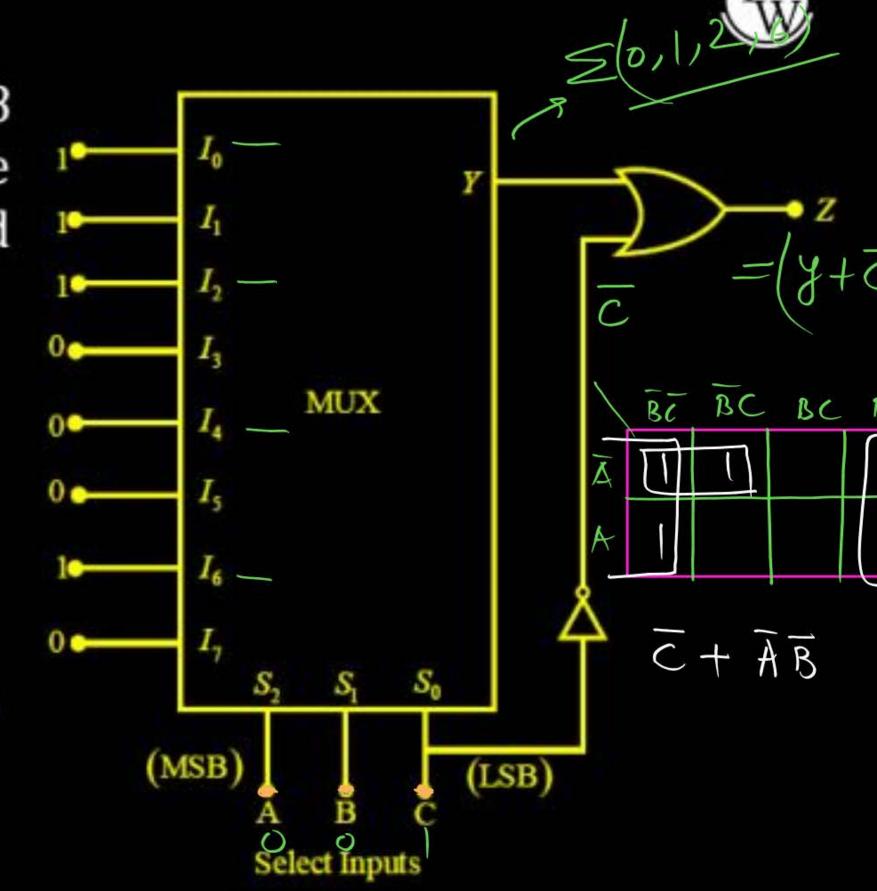
A combinational circuit using an 8 to 1 multiplexer is shown in the following figure. The minimized expression for the output (Z) is

(a)
$$C(\bar{A} + \bar{B}) \times O$$

(b)
$$C(A + B) \times 6$$

$$\overline{C} + \overline{A}\overline{B} \qquad | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C + | C$$

$$(d)$$
 $(\overline{C} + AB)$ $O + O \cdot O = O$





The Boolean expression of the output f of the multiplexer shown below is:

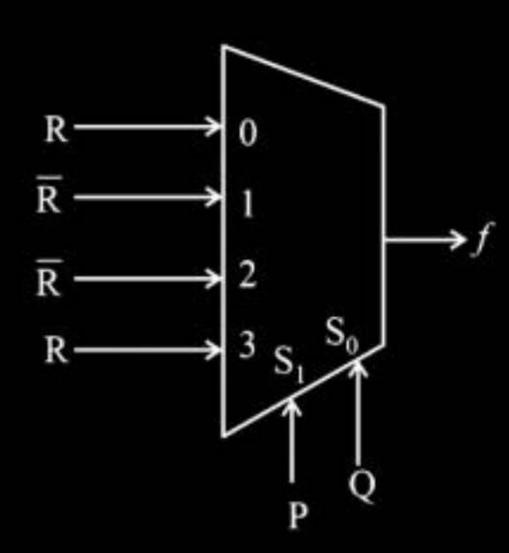
[GATE-2010-CS: 1M]



 $P \oplus Q \oplus R$

C P+Q+R

 $\overline{P+Q+R}$



H.W.

Question XXX





logical function f (A, B, C) is given as

$$f(A,B,C) = \overline{AB + BC + CA}$$

Then
$$f_1(A,B,C) = f[AB,BC,CA]$$

f₁ implemented using 2 : 1 MUX with C as select input then input I₀ & I_1 of 2: 1 MUX will be

(a)
$$I_0 = 1, I_1 = \overline{A}$$

(b)
$$I_0 = 0, I_1 = \overline{A} + \overline{B}$$

(c)
$$I_0 = 1, I_1 = \overline{A} + \overline{B}$$

(d)
$$I_0 = 0, I_1 = \overline{A} + B$$

Q.
$$f(A_1B_1C) = \sum (12_13_15_17)$$

(i)
$$f(\overline{A}, B, c) = ?$$

(II)
$$f(\overline{A}, \overline{B}, c) = ?$$

$$(III) f(A, \overline{B}, C) = ?$$

(iv)
$$f(A,\overline{B},\overline{c})=?$$

$$(V)$$
 $f(A_1B_1C)$ Z

$$(VI) \frac{f(A,\overline{B},c)}{A\overline{B}} \ge$$

$$(Vii)$$
 $f(\overline{A},B,C)$ \geq

$$(VIII)$$
 $f(A,B,c)$ \geq

$$(ix) \cdot \frac{f(A,\overline{B},c)}{\overline{B}}$$



2 Minute Summary



- Questions Practice



Thank you



