COMPUTER SCIENCE & IT



DIGITAL LOGIC

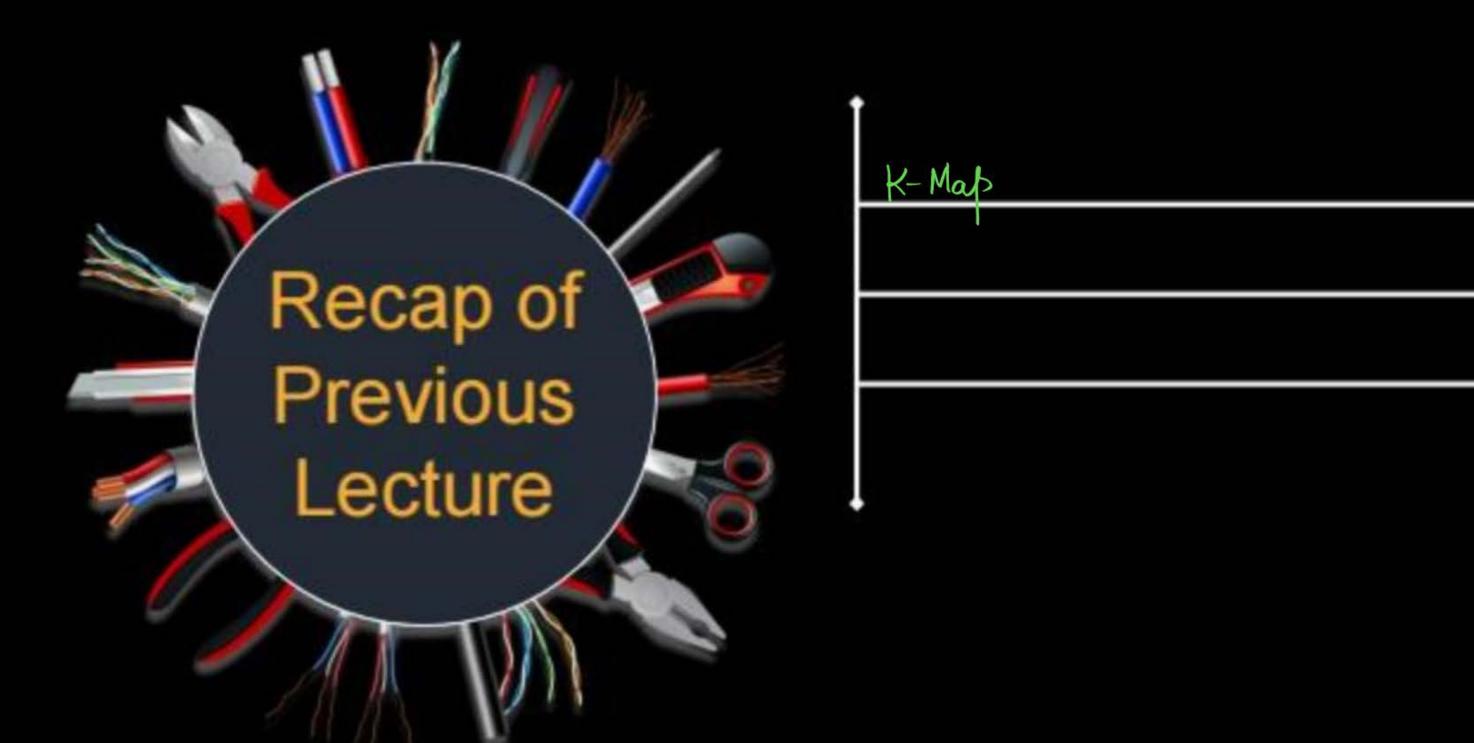


Lecture No. 06

Combinational Circuit











Questions	Practice	



The Boolean expression XY + (X' + Y') Z is equivalent to

(a)
$$XYZ' + X'Y'Z$$

$$(c)/(X+Z)(Y+Z)$$

(d)
$$(X' + Z) (Y' + Z)$$

$$P + (\overline{P} \cdot \overline{z}) = (P + \overline{P}) \cdot (P + \overline{z})$$

$$= (P + \overline{z})$$

$$= (XY + \overline{z})$$

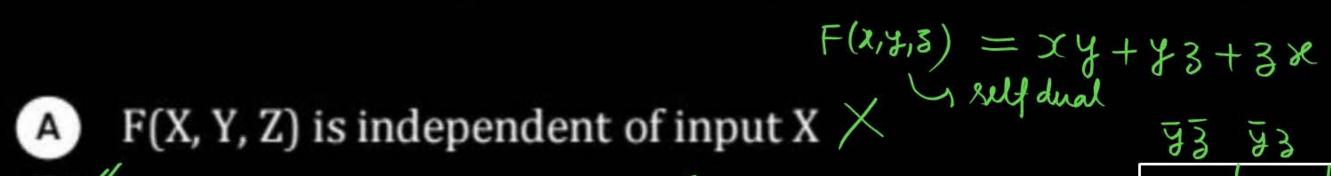
$$= (Z + XY)$$

$$= (Z + XY) \cdot (Z + YY)$$

$$= (X + \overline{z}) (Y + \overline{z})$$

Consider a Boolean expression given by $F(X, Y, Z) = \Sigma(3,5,6,7)$. $= \pi(0,1,2,4)$ Which of the following statements is lower CORDER and

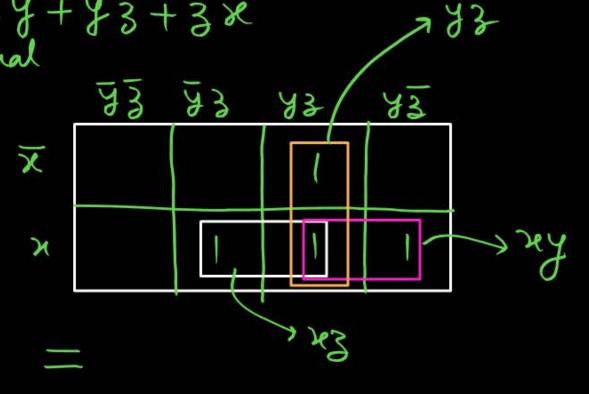
[GATE-2024-CS: 1M]



$$F(X, Y, Z) = XY + YZ + XZ$$

F(X, Y, Z) is independent of input $Y \times$

$$D/F(X, Y, Z) = \pi(0, 1, 2, 4)$$



$$F^{0}(x_{1}y_{1}y_{3}) = (x+y)(y+y)(y+y)(3+x) = F(x_{1}y_{1}y_{3})$$



The dual of a Boolean function $F(x_1, x_2, ..., x_n, +, \cdot, \cdot)$, written as F^D , is the same expression as that of F with + and \cdot swapped. F is said to be self-dual if F = F^D. The number of self-dual functions with n Boolean variables is

2n

2n-1

 $\begin{array}{c|c} C & 2^{2^n} \\ \hline D & 2^{2^{n-1}} \end{array}$

$$n \longrightarrow 2^n \longrightarrow Combinotion (terms) = N$$

Total $f^n M_1 = 2^N = 2^n$
Self dual $f^n M_2 = 2^n$

[GATE-2022-CS: 1M]



If w, x, y, z are Boolean variables, then which one of the following is [GATE-2017-CS: 1M]



If X = 1 in the logic equation $[X + Z(\overline{Y} + (\overline{Z} + X\overline{Y}))](\overline{X} + \overline{Z}(X + Y)) = 1$ then

(a)
$$Y = Z$$

(b)
$$\overline{Y} = Z$$

(c)
$$Z = 1$$

$$(d)/Z = 0$$

y3=1=1

y=1 gu 2=1

y =0 and 3=0

7+3=1

$$1 \cdot \left[0 + \overline{z} \cdot \right] = 1$$

$$Z = 0$$

$$33 = 1$$
 $3 = 1$
 $3 = 1$
 $3 = 1$



The Boolean expression
$$AB + A\overline{C} + BC$$
 simplifies to

(a)
$$BC + A\overline{C}$$

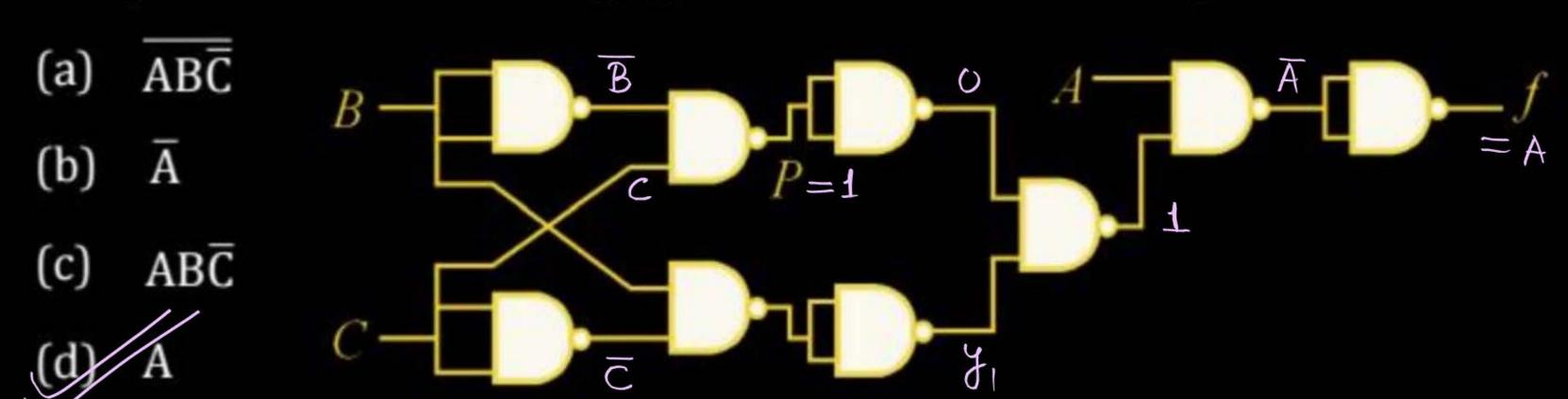
(b)
$$AB + A\overline{C} + B$$

(c)
$$AB + A\overline{C}$$

(d)
$$AB + BC$$

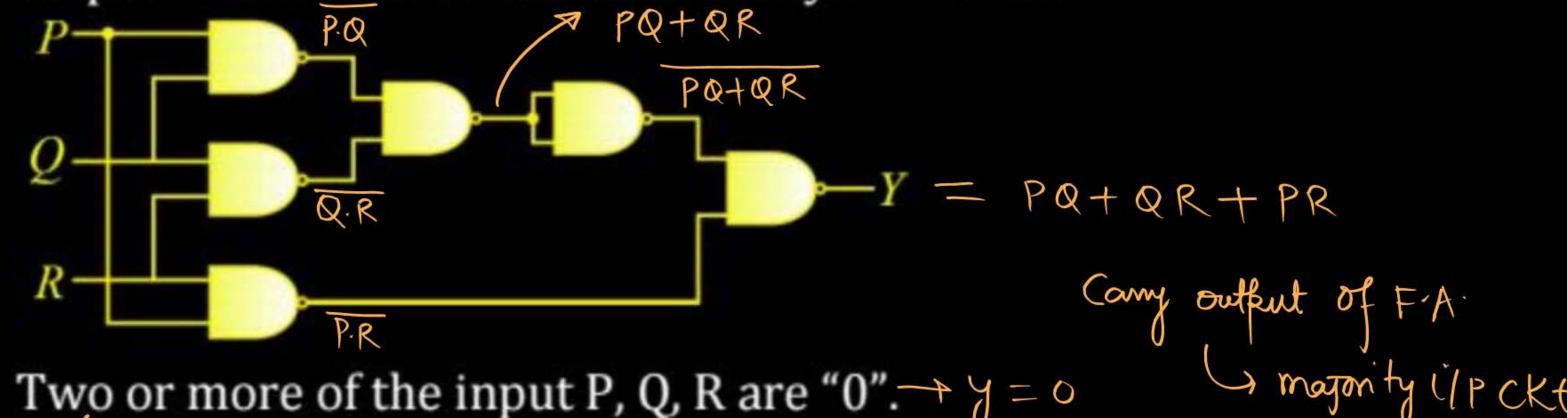


The point P in the following figure is stuck at 1. The output f will be





The output Y is the circuit below is always "1" when



- (a) Two or more of the input P, Q, R are "0". \rightarrow = 0
- (b) Two or more of the inputs P, Q, R are "1".
- 1-0-7-11-1 3-70-A-0 (c) Any odd number of the inputs P, Q, R are "0".
- (d) Any odd number of the inputs P, Q, R are "1".



The binary operator ≠ is defined by the following truth table

P	Q	p≠q
0	0	0
0	1	1
1	0	1
1	1	0

$$xor = #$$

Which one of the following is true about the binary operator ≠?

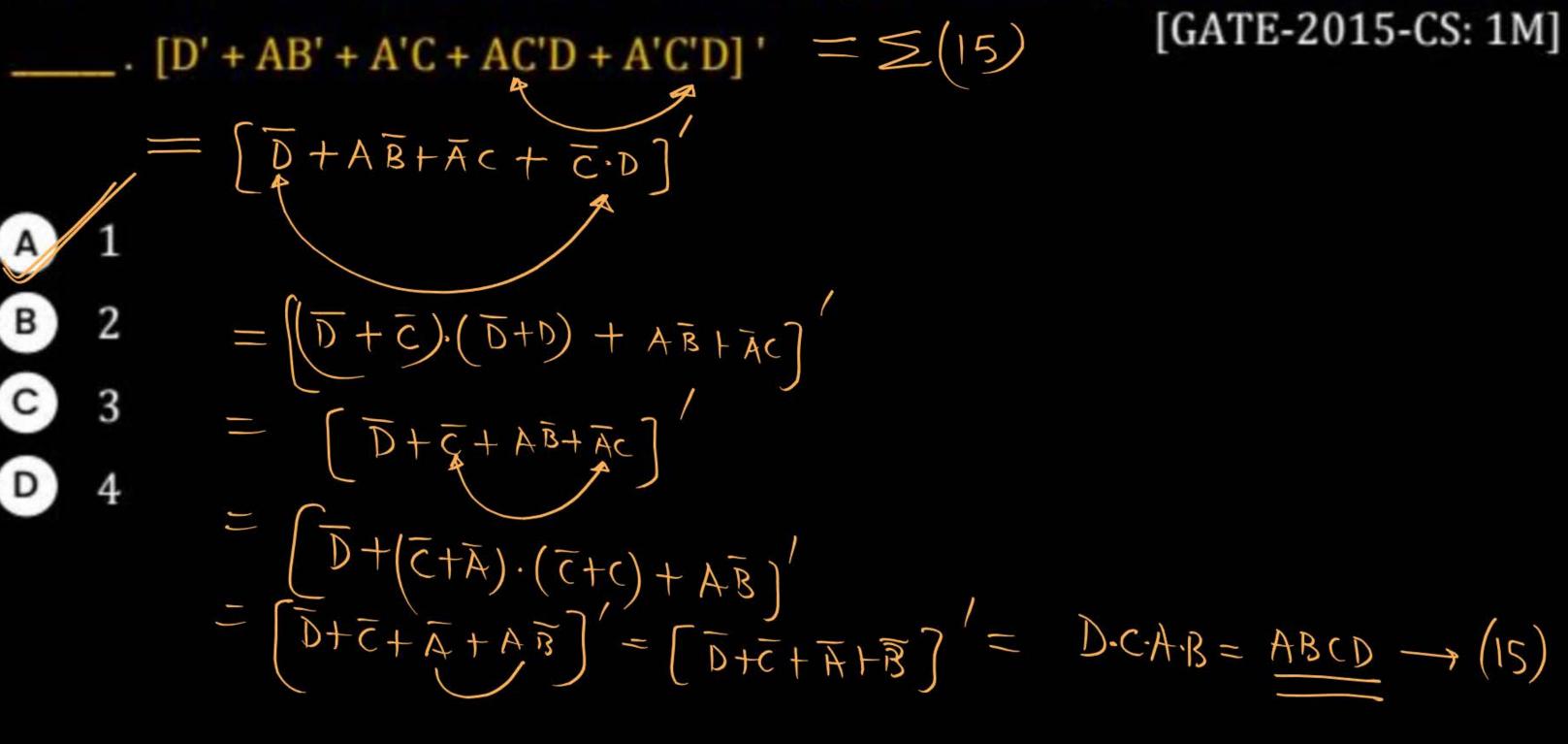
A Both commutative and associative

[GATE-2015-CS: 1M]

- B Commutative but not associative
- Not commutative but associative
- Neither commutative nor associative



The number of min-terms after minimizing the following Boolean expression is





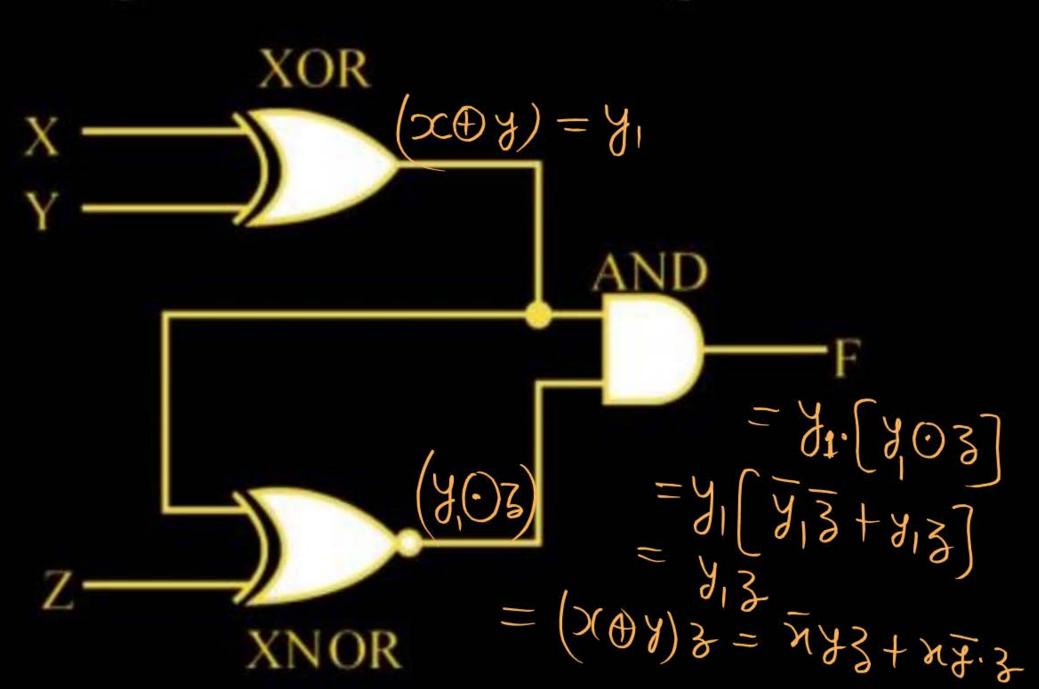
The output F in the digital logic circuit shown in the figure is

(a)
$$F = \overline{X}YZ + X\overline{Y}Z$$

(b)
$$F = \overline{X}\overline{Y}Z + X\overline{Y}\overline{Z}$$

(c)
$$F = \overline{X}\overline{Y}Z + XYZ$$

(d)
$$F = \overline{X}\overline{Y}\overline{Z} + XYZ$$





+ ABC C+AC]

The Boolean expression for the truth table shown below is

(a)
$$B(A+C)(\overline{A}+\overline{C})$$

(b)
$$B = (A + \overline{C})(\overline{A} + C)$$

(c)
$$\overline{B}(A+C)(\overline{A}+C)$$

(d)
$$\overline{B}(A+C)(\overline{A}+\overline{C})$$

Α	В	С	f	
0	0	0	0	= ABC
0	0	1	0	$=B(\bar{A})$
0	1	0	0	$= B \left(\frac{A}{A} \right)$
0	1	1	1	
1	0	0	0	2
1	0	1	0	
1	1	0	1	
$\frac{1}{1}$	1	1	0	



Given the function F = P' + QR, wehre F is a function in three Boolean variables P,Q and R and P' = !P, consider the following statements.

(S1)
$$F = \Sigma(4, 5, 6)$$

(S2) $F = \Sigma(0,1,2,3,7)$ $= P + QR$ QR $0 00 \rightarrow 0$
(S3) $F = \Pi(4, 5, 6)$ $0 \mid 1 \rightarrow 3$ $0 \mid 1 \rightarrow 3$

[GATE-2015-CS: 2M]

Which of the following is true?

- (S1) False, (S2) True, (S3) True, (S4) False
- (S1) True, (S2) False, (S3) False, (S4) True
- (S1) False, (S2) False, (S3) True, (S4) True
- (S1) True, (S2) True, (S3) False, (S4) False

The truth table

X	Y	F(X, Y)
0	0	0

0



H.W.

represents the Boolean function

A 2

в х+у

c x⊕y

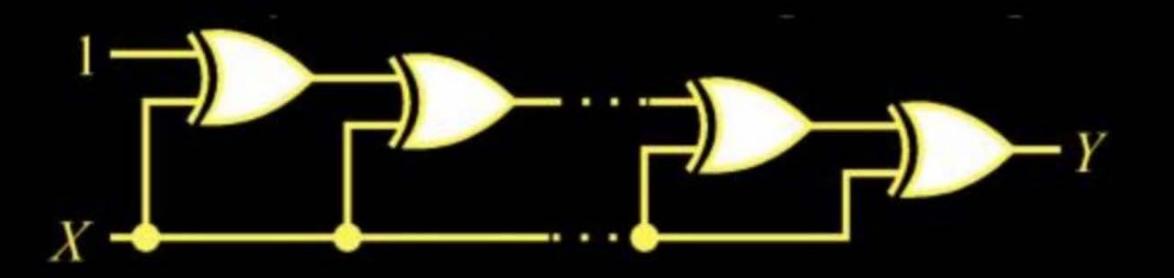
D Z

[GATE-2012-CS: 1M]



If the input to the digital circuit consisting of a cascade of 20 X-OR gates is X, then the output Y is equal to

- (a) 0
- (b) 1
- (c) \bar{X}
- (d) X





The simplified SOP (sum of product) form of the Boolean expression

$$(P + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + R) \cdot (P + Q + \bar{R})$$

$$(Q + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + R) \cdot (P + Q + \bar{R})$$

$$(Q + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + R) \cdot (P + Q + \bar{R})$$

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$$(Q + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + \bar{R})$$

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$$(Q + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + \bar{R})$$

$$(Q + \bar{Q} + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + \bar{R})$$

$$(Q + \bar{Q} + \bar{R}) \cdot (P + \bar{Q} + \bar{R$$

$$A \qquad \left(\overline{P}.Q + \overline{R}\right)$$

(P + Q.'R')

$$(P'.Q + R)$$

(P.Q + R)

$$= \left(\left(P + \overline{\alpha} \right) + \overline{R} \cdot R \right) \left(P + Q + \overline{\lambda} \right) \overline{P}$$

$$= (P+\overline{Q})(P+Q+\overline{R})$$

$$= P+\overline{Q}(Q+\overline{R})$$

$$= P+\overline{Q}(Q+\overline{R})$$



A Boolean function f of two variable x and y is defined as follows

$$f(0,0) = f(0,1) = f(1,1)=1; f(1,0) = 0$$

Assuming complements of x and y are not availed, the minimum cost solution for realizing f using only 2-input NOR gates and 2-input OR gates (each having unit cost) would have a total cost of

(a) 1 unit

(b) 4 unit

(c) 3 unit

(d) 2 unit

[GATE-2011-CS: 1M]

The minterm expansion of $f(P, Q, R) = PQ + Q\overline{R} + P\overline{R}$ is

H.W.

$$m_2 + m_4 + m_6 + m_7$$

$$m_0 + m_1 + m_3 + m_5$$

$$m_0 + m_1 + m_6 + m_7$$

$$m_2 + m_3 + m_4 + m_5$$

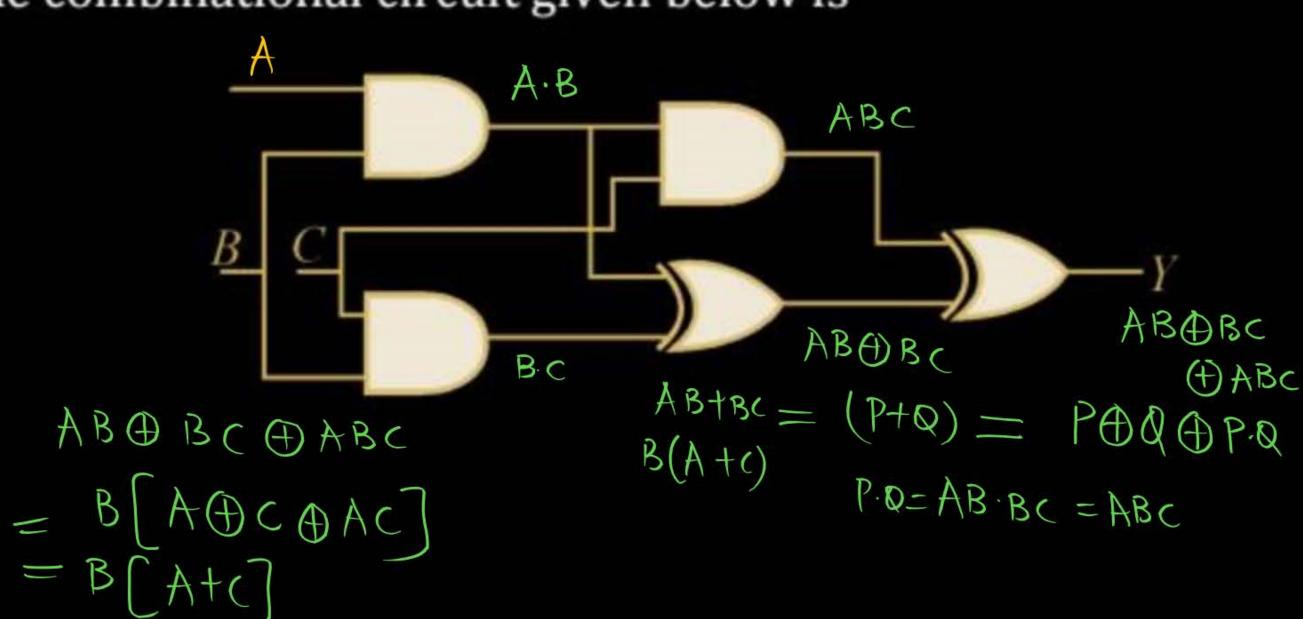


The input of the combinational circuit given below is

(a)
$$A + B + C$$

(b)
$$A(B+C)$$

(d)
$$C(A + B)$$



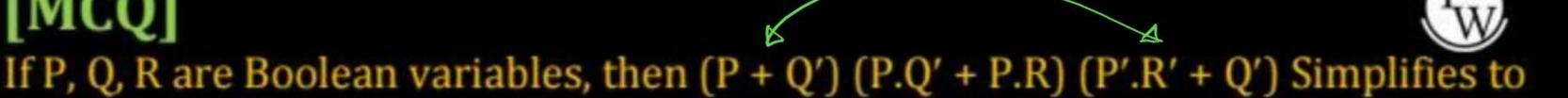
ABOBCOABC

$$= \frac{B \oplus B \oplus B \oplus ABC}{B \oplus ABC}$$

$$= \frac{B (A \oplus C \oplus AC)}{B \oplus (A + C)}$$

$$= \frac{B + AC}{B + AC}$$

Ī



[GATE-2008-CS: 1M]

$$= \left(\overline{Q} + P(\overline{P}\overline{R})\right) \left[P\overline{Q} + PR\right]$$

$$P. \overline{Q} + R$$

$$D = P.R + Q$$

$$P[P+\overline{Q}][\overline{Q}+R)$$

$$P[\overline{Q}+(R)(\overline{P}R)]$$

$$= P\overline{Q}$$

[MSQ]



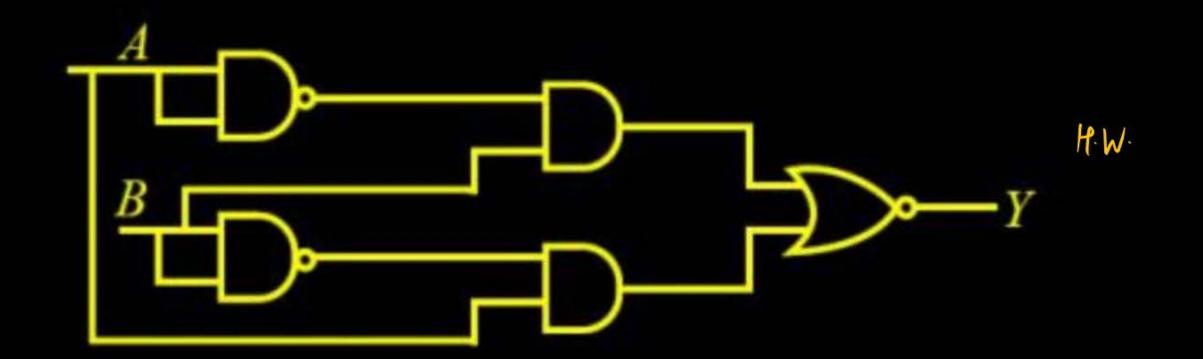
Let, $x_1 \oplus x_2 \oplus x_3 \oplus x_4 = 0$ where x_1, x_2, x_3, x_4 are Boolean variables, and \oplus is the XOR operator. Which one of the following must always be TRUE?

[GATE-2022-CS: 1M]

A
$$x_1x_2x_3x_4 = 0$$
 (0, 2, 4)
B $x_1x_3+x_2x_4 = 0$ (0, 2, 4)
C $\overline{x}_1 \oplus \overline{x}_3 = \overline{x}_2 \oplus \overline{x}_4 \implies x_1 \oplus x_3 = x_2 \oplus x_4 \implies 0, 2, 4$
D $x_1+x_2+x_3+x_4=0$ (0, 2, 4)



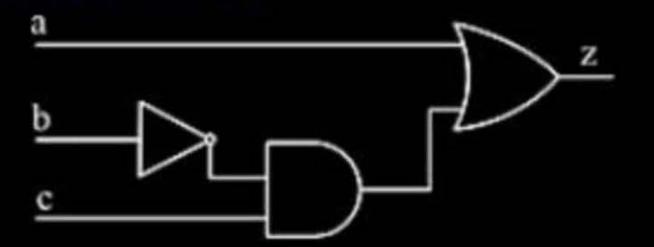
A circuit is as given below:



If all the gates are replaced by its dual gates in above circuit then output Y will changes to Y_1 and to implement Y_1 minimum no. of two input NAND gate required is _____.

Consider the Boolean function z(a,b,c). Which one of the following minterm lists represents the circuit given above?

[GATE-2020-CS: 2M]

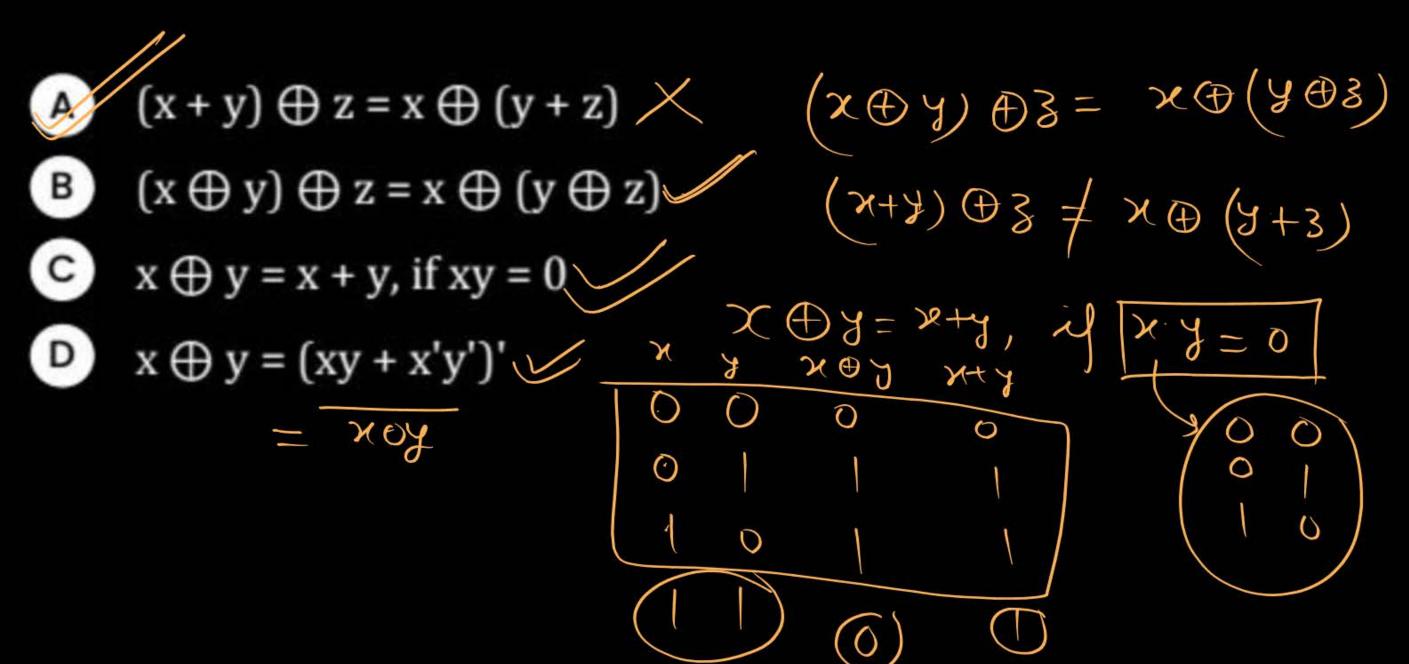


H.W.

- A $Z = \Sigma (0, 1, 3, 7)$
- B $Z = \Sigma (2, 4, 5, 6, 7)$
- $Z = \Sigma (1, 4, 5, 6, 7)$
- D $Z = \Sigma (2, 3, 5)$

Which one of the following is NOT a valid identity?





[NAT]

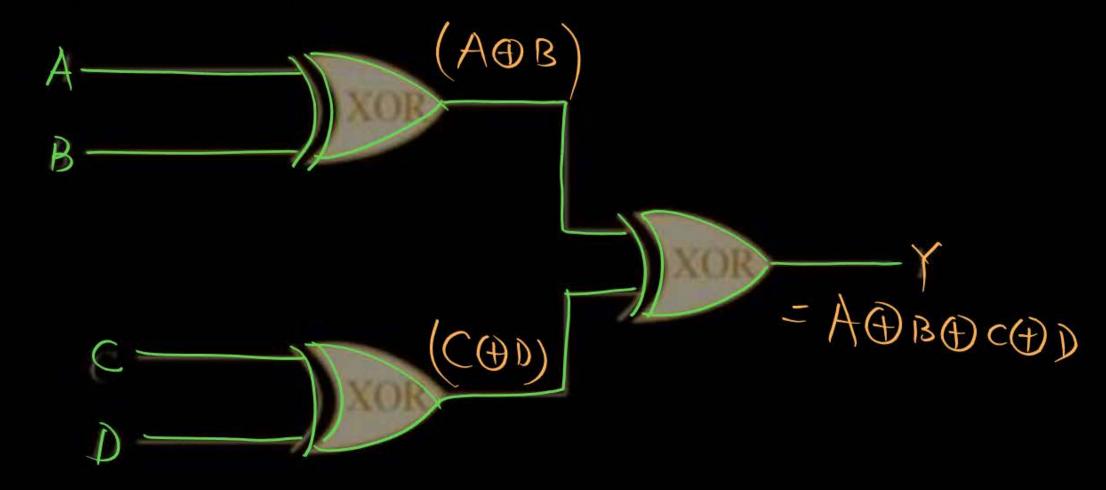
What is the minimum number of 2-input NOR gates required to implement a 4-variable function expressed in sum-of-minterms form as $f = \Sigma(0, 2, 5, 7, 8, 10, 13, 15)$? Assume that all the inputs and their complements are available.

[GATE-2019-CS: 2M]

H.W.



A, B, C and D are input bits and Y is the output bit into the XOR gate circuit of the figure below. Which of the following statements about the sum S of A, B, C, D and Y is correct?





- (a) S is always either zero or odd,
- (b) S is always either zero or even.

- (c) S = 1 only if the sum of A, B, C and D is even. $\Rightarrow S = 0$ (0) $\Rightarrow S = 2$ (1)
- (d) S = 1 only if the sum of A, B, C and D is odd. $\Rightarrow S = 2$ (2)

$$f(A_1B_1(D)) = A_1B_1(D) + (A_1B_1(D)) = A_1B_1(D) = A_1B_1(D) + (A_1B_1(D)) = A_1B_1(D) =$$

Consider three 4-variable functions f1, f2 and f3, which are expressed in sum-ofminterms as

$$f_1 = \Sigma(0, 2, 5, 8, 14), f_2 = \Sigma(2, 3, 6, 8, 14, 15), f_3 = \Sigma(2, 7, 11, 14)$$

For the following circuit with one AND gate and one XOR gate, the output

function f can be expressed as:

[GATE-2019-CS: 2M]

$$||x_{0R}-r|=f_1f_2\oplus f_3=f_4\oplus f_3$$

A)
$$\Sigma$$
 (2, 14)

$$f_4 = \sum (2, 8, 14)$$

$$f_4 = \Sigma(2,8,14)$$

 $f = f_4 \oplus f_3 = \Sigma(7,8,11)$

Let ⊕ and ⊙ denote the Exclusive OR and Exclusive NOR operations, respectively. Which one of the following is NOT CORRECT? [GATE-2018-CS: 1M]

$$A P \oplus Q = P \odot Q \checkmark /$$

$$\overline{P} \oplus Q = P \odot Q \checkmark /$$

c
$$\overline{P} \oplus \overline{Q} = P \oplus Q$$

Pw

Let \oplus denote the Exclusive OR (XOR) operation. Let '1' and '0' denote the binary constants. Consider the following Boolean expression for F over two variables P and Q:

$$F(P, Q) = ((1 \oplus P) \oplus (P \oplus Q)) \oplus ((P \oplus Q) \oplus (Q \oplus 0))$$

The equivalent expression for F is

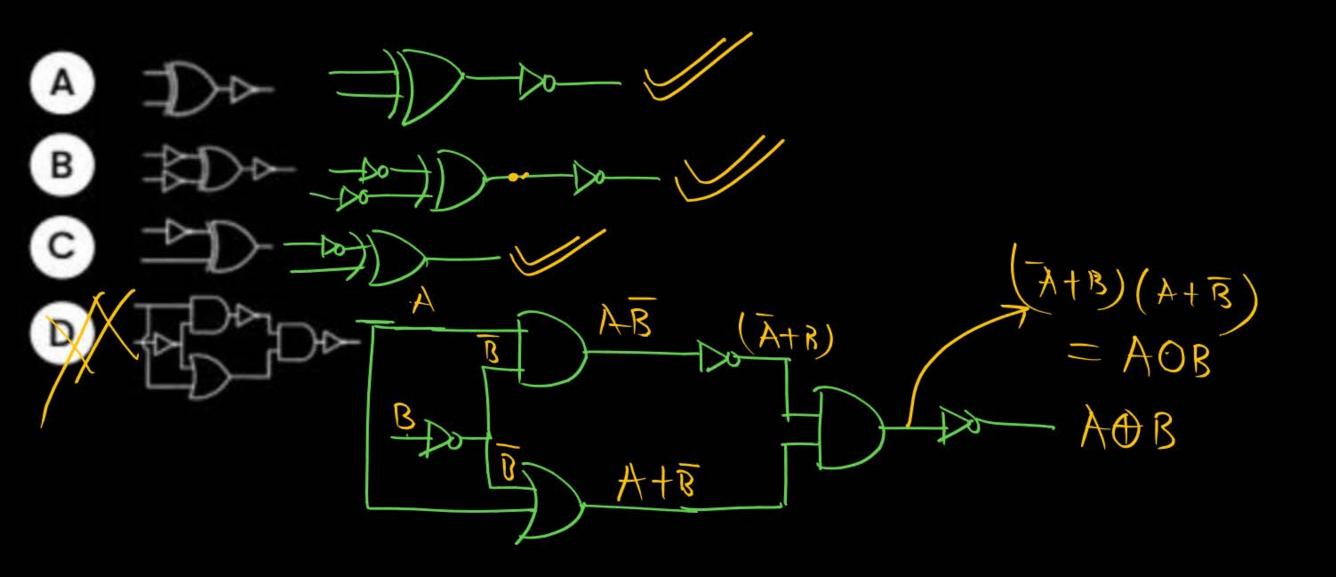
[GATE-2014-CS: 1M]



- A P + Q
- B (P + Q)'
- \circ P \oplus Q
- $\mathsf{D} \quad (\mathsf{P} \oplus \mathsf{Q})'$

Which one of the following circuits is NOT equivalent to a 2-input X-NOR (exclusive NOR) gate?

[GATE-2011-CS: 1M]





What is the Boolean expression for the output f of the combinational logic

circuit of NOR gates given below?



$$\overline{Q + R}$$

$$B \overline{P+Q}$$

$$\overline{P+R}$$

$$\overline{P+Q+R}$$

What is the minimum number of gates required to implement the Boolean function (AB + C) if we have to use only 2-input NOR gates?[GATE-2009-CS: 1M]

H-W

A 2

B 3

C 4

D 5

Given f1,f3 and f in canonical sum of products from (in decimal) for the circuit.

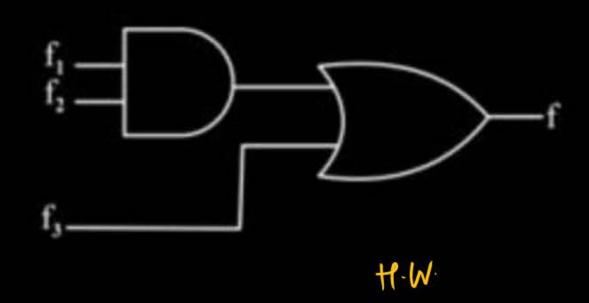


$$F_3 = \sum m (1, 6, 15)$$

$$F = \sum m (1, 6, 8, 15)$$

Then f2 is

- A $\sum m(4,6)$
- B $\sum m (4, 8)$
- $\sum m(6,8)$
- $\sum m(4, 6, 8)$



[GATE-2008-CS: 1M]

[NAT]



Consider the Karnaugh map given below, where X represents "don't care" and blank represents 0.

dc	00	01	11	10	
00		x	x		
01	1			x	H·l
11	1			1	
10		x	x		

Assume for all inputs(a, b, c, d), the respective complements $(\bar{a}, \bar{b}, \bar{c}, \bar{d})$ are also available. The above logic is implemented using 2-input NOR gates only. The minimum number of gates required is ______. [GATE-2017-CS: 1M]

Given $f(w, x, y, z) = \Sigma m(0, 1, 2, 3, 7, 8, 10) + \Sigma d(5, 6, 11, 15)$, where d represents the don't-care condition in Karnaugh maps. Which of the following is a minimum product-of-sums (POS) form of f(w, x, y, z)? [GATE-2017-CS: 1M]

$$A f = (\overline{w} + \overline{z})(\overline{x} + z)$$

B
$$f = (\overline{w} + z)(x + z)$$

$$f = (w+z)(\bar{x}+z)$$

$$D f = (w + \bar{z})(\bar{x} + z)$$

H.W.



Consider the following Boolean expression for

$$F: F(P, Q, R, S) = PQ + P'QR + P'QR'S$$

The minimal sum-of-products form of F is

A
$$PQ + QR + QS$$

$$B P + Q + R + S$$

$$\overline{P} + \overline{Q} + \overline{R} + \overline{S}$$

$$\overline{P}R + \overline{P}\overline{R}S + P$$

[GATE-2014-CS: 1M]



A logical function f(A, B, C) is given as $f(A, B, C) = A \oplus B \oplus C + (A \oplus B)C$

Then minimum no. of 2-input NAND gate required to implement





2 Minute Summary



-> Practic Remion.



Thank you

Seldiers!

