

# CS & IT ENGINEERING

## Operating System

Virtual Memory

Lecture No. 01

By- Vishvadeep Gothi sir



# Recap of Previous Lecture



**Topic**

**TLB Mapping**

**Topic**

**Segmentation**



# Topics to be Covered



**Topic**

**Virtual Memory**

**Topic**

**Demand Paging**

**Topic**

**Page Fault**



## Topic : Virtual Memory

- Feature of OS
- Enables to run larger process with smaller available memory



## Topic : Virtual Memory

```
int x;  
printf("%d", x);
```

Page 0	000	
Page 1	001	
Page 2	010	
Page 3	011	
Page 4	100	
Page 5	101	
Page 6	110	
Page 7	111	

00

01

10

11




# Topic : Virtual Memory

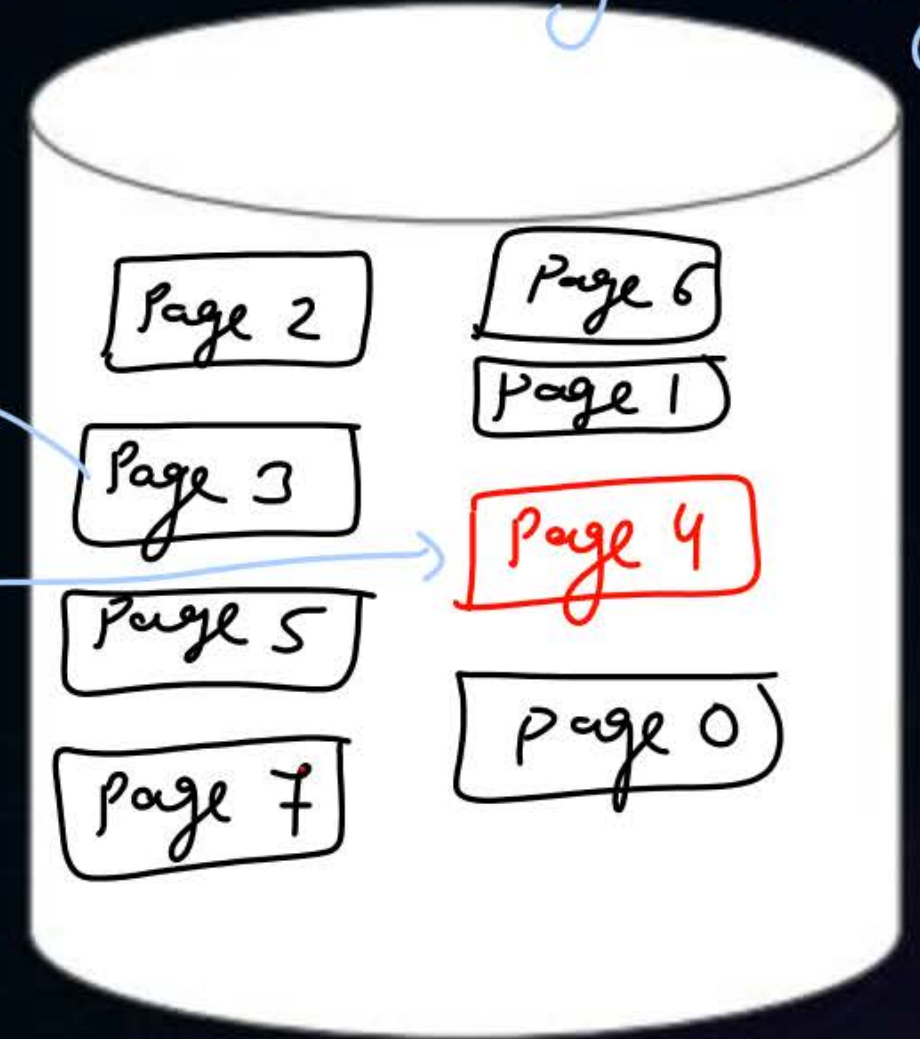
Process

Page 0	000	11
Page 1	001	10
Page 2	010	-
Page 3	011	01
Page 4	100	<del>01</del>
Page 5	101	-
Page 6	110	00
Page 7	111	-

Main mem.

00	Page 6
01	Page <del>4</del> 3
10	Page 1
11	Page 0

Secondary memory





example 1 :-

cpu generates logical address (virtual add.)

P	d
1	✓

→ search in  
p.T. for P=1

⇒  $f = 2$  Page hit

gotos 

f	d
---	---

  
physical add.

ex 2 :-  
CPU

generates L.A.



→ goto P.T. &  
search for P=3

} ⇒ Page fault

⇒ OS does page fault service

↓  
Brings faulted page from secondary  
memory to main memory.  
(A page is replaced if needed)  
and update page table

After service, the instruction which  
raised page fault will restart.





## Topic : Demand Paging

- **Demand Paging:**

Bring pages in memory when CPU demands

- **Page Fault:**

When the demanded page is not available in physical memory

- Page fault service time :-

Time needed to service a page fault, after page fault is raised.

- Page fault rate :-

fraction of time page fault occurs.

### Pure demand paging:-

when a process arrives, none of its pages are brought into main memory. and pages are brought into m.m. on demand.

### Demand Paging:-

when a process arrives, a few initial pages are brought to main memory and rest are brought on demand.



## Topic : How to Check Page Hit/Fault

using valid/invalid bit

→ 0 invalid  
→ 1 valid

	v/I	f
000	1	11
001	1	10
010	0	
011	<del>0</del> 1	01
100	<del>1</del> 0	01
101	0	
110	1	00
111	0	

CPU generates L.A. 

p	d
3	

goto p.T. at p=3 & check v/I bit

↓  
v/I bit = 0

↓  
Page fault ⇒ after service of page fault  
v/I bit of page 3 changed to 1  
& v/I bit for replaced page 4 is made 0.





## Topic : Saving Page Swap Time

Dirty or modified bit

	V/I	f
00		f
01		
10		
11	1	f

Page table

0  $\Rightarrow$  only read done in page

1  $\Rightarrow$  write done in page  
(Page has become dirty)

only dirty pages are copied back to secondary memory when replaced, hence write time of non-dirty replaced pages can be saved.



## Topic : Effective Memory Access Time

Assume page fault rate =  $p$

$$E.M.A.T. = (1 - p) * \text{time needed for mem. access when page hit} + p * \text{time needed for mem access when page fault}$$

---

$$E.M.A.T. = (1 - p) * \frac{2t_{mm}}{\downarrow} + p * \left( \frac{t_{mm}}{\downarrow} + \text{p.f. service time} \right)$$

one for P.T.  
one for content

for P.T. check

$$= t_{mm} + (1 - p)t_{mm} + p * \text{p.f. service time}$$

ex:-

$$p = 2\%$$

$$\text{page hit time} = 500 \text{ ns}$$

$$\text{page fault time} = 15000 \text{ ns}$$

$$\begin{aligned} \text{E.M.A.T.} &= 0.98 * 500 + 0.02 * 15000 \\ &= 790 \text{ ns} \end{aligned}$$



ex:-

$$t_{mm} = 200 \text{ ns}$$

$$p = 1\%$$

$$\text{p.f. service time} = 100 \mu\text{sec} = 100000 \text{ ns}$$

$$\begin{aligned} \text{E.M.A.T.} &= 0.99 * (2 * 200) + 0.01 (200 + 100000) \text{ ns} \\ &= 1398 \text{ ns} \end{aligned}$$

# TLB with virtual memory

cpu generates L.A.  
↓  
search in TLB

TLB hit

P.A.



access content from mm

$$= t_{TLB} + t_{mm}$$

TLB miss

search P.T.

page hit

P.A.



access content from  
mm

$$= t_{TLB} + 2t_{mm}$$

fault

Page fault service

$$= t_{TLB} + t_{mm} + \text{p.f. service time}$$

$$E.M.A.T. = H(t_{TLB} + t_{mm})$$

$$+ (1-H) \left[ (1-P)(t_{TLB} + 2t_m) + P * (t_{TLB} + t_{mm} + \underset{\text{service time}}{P.f.}) \right]$$





## Topic : Effective Memory Access Time with TLB

TLB & Virtual memory :

$$\begin{aligned} \text{EMAT} = & H(t_{\text{TLB}} + t_{\text{mm}}) + (1 - H) [(1-p) * (t_{\text{TLB}} + t_{\text{mm}} + t_{\text{mm}}) \\ & + p * (t_{\text{TLB}} + t_{\text{mm}} + p. f. \text{ Service time})] \end{aligned}$$

$$= t_{\text{TLB}} + t_{\text{mm}} + (1 - H) [(1 - p) * t_{\text{mm}} + p * p. f. \text{ Service Time}]$$



## Topic : Question



TLB hit = 90%

TLB access time = 10 nsec

mm access time = 200 n sec

Page fault rate = 1 %

p.f. service time = 100 m sec

E. M. A. T. = \_\_\_\_\_ n sec.

E.M.A.T.

$$= 0.9 * (10 + 200)$$

$$+ 0.1 * \left[ 0.99 * (10 + 2 * 200) + 0.01 * (10 + 200 + 100 * 10^6 \text{ ns}) \right]$$

$$= 100229.8 \text{ ns}$$



## 2 mins Summary

**Topic**

**Virtual Memory**

**Topic**

**Demand Paging**

**Topic**

**Page Fault**





**Happy Learning**

**THANK - YOU**