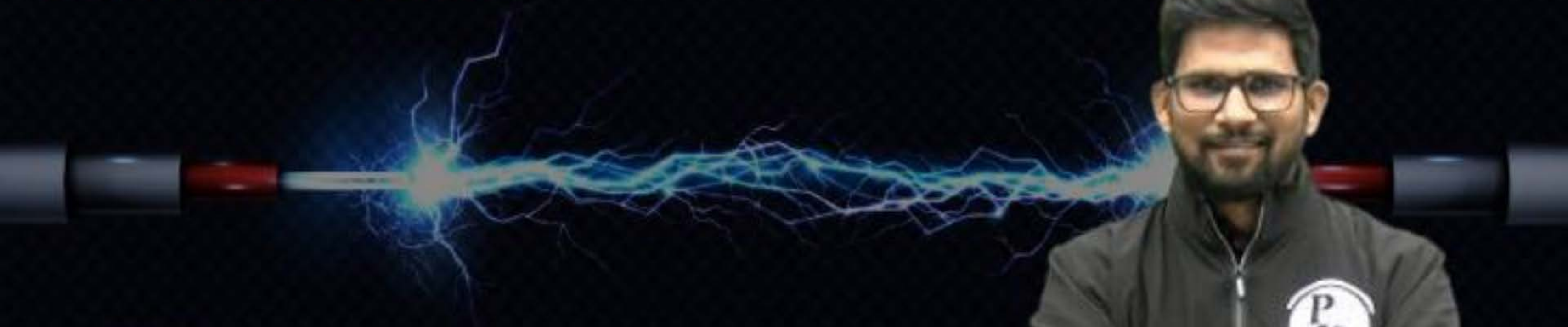


COMPUTER SCIENCE & IT

DIGITAL LOGIC




Lecture No. 08

**BOOLEAN THEOREMS AND
GATES**

By- Chandan Gupta Sir





Recap of Previous Lecture

Universal gates



Topics to be Covered

Universal gate Cont.

Q. $Y = \bar{A}B + B\bar{C}$

$$\bar{Y} = \overline{\bar{A}B \cdot B\bar{C}} = P \cdot Q \quad \left. \begin{array}{l} \downarrow 2 \text{ NAND} \\ \searrow 2 \text{ NAND} \end{array} \right\} 5 \text{ NAND}$$

$$Y = \overline{P \cdot Q} \quad \hookrightarrow 1 \text{ NAND}$$

$$\Rightarrow Y = B(\bar{A} + \bar{C}) = B \cdot \overline{AC} = B \cdot R \quad \left. \begin{array}{l} \xrightarrow{1 \text{ NAND}} \\ \xrightarrow{2 \text{ NAND}} \end{array} \right\} 3 \text{ NAND}$$

$$B \cdot R \longrightarrow 2 \text{ NAND}$$

$$\# y = A \cdot (B + C)$$

$$\left. \begin{array}{l} (B + C) \rightarrow 3 \text{ NAND} \rightarrow P \\ A \cdot P \rightarrow 2 \text{ NAND} \end{array} \right\}$$

$$y = A \cdot B + A \cdot C$$

$$\bar{y} = \overline{AB + AC} = \overline{AB} \cdot \overline{AC} = Q \cdot R$$

\downarrow
1 NAND

\searrow
1 NAND

$$y = \overline{Q \cdot R} \rightarrow 1 \text{ NAND}$$

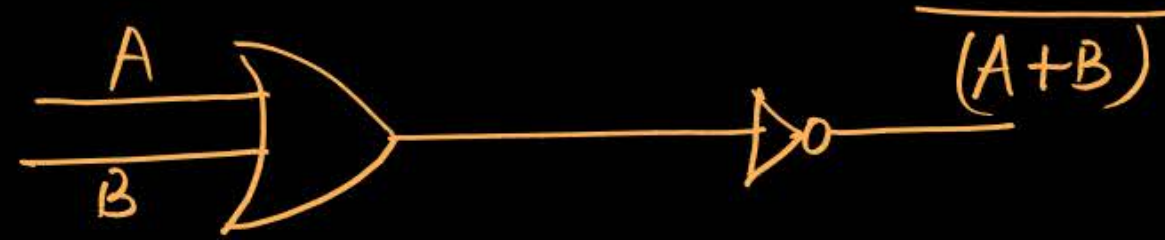
$$f(A, B, C) = \Sigma(1, 2, 4, 7)$$

$$= A \oplus B \oplus C$$

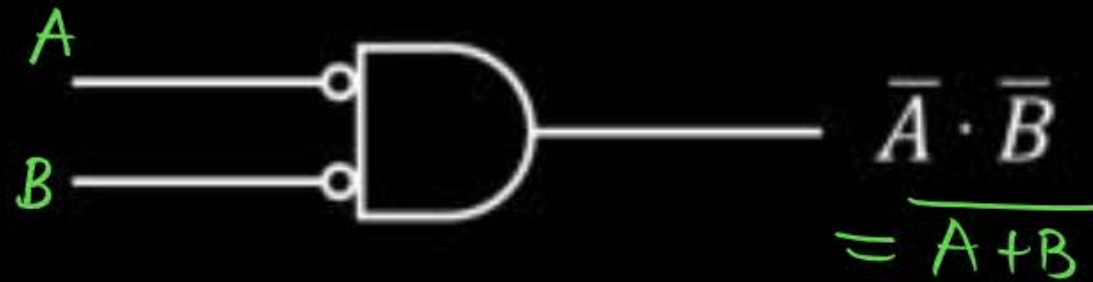
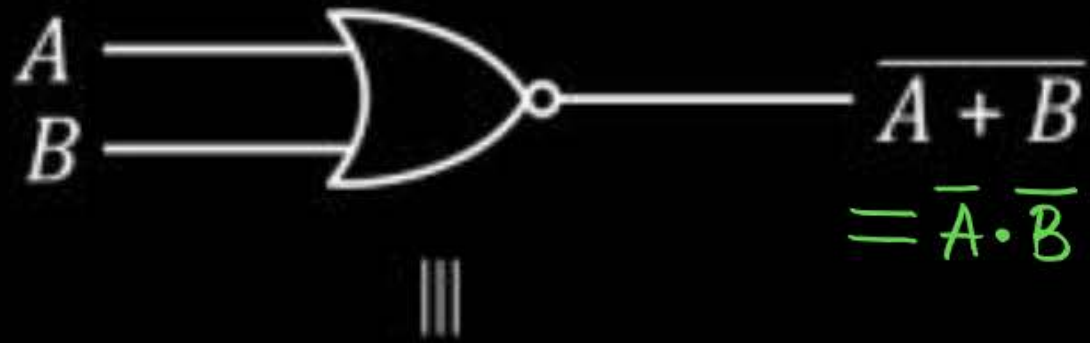
$$\left. \begin{array}{l} A \oplus B \rightarrow 4 \text{ NAND} \rightarrow P \\ P \oplus C \rightarrow 4 \text{ NAND} \rightarrow \end{array} \right\} 8 \text{ NAND}$$

[NOR GATE]

OR + NOT



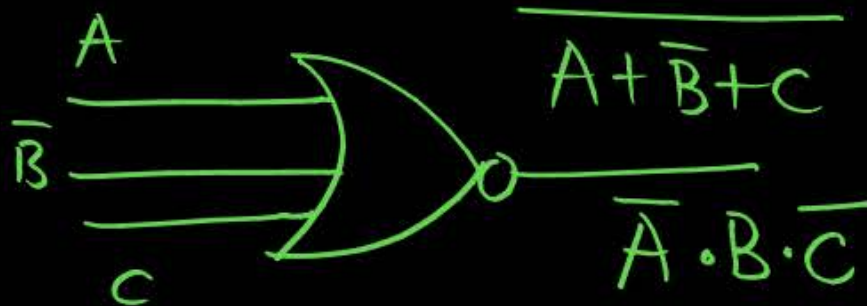
Representation :



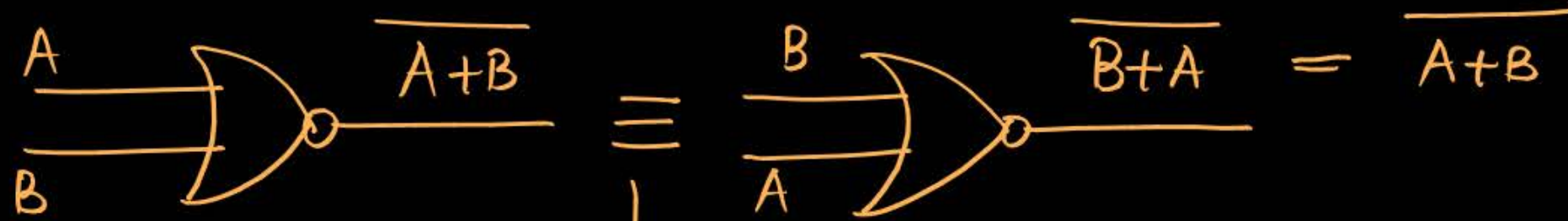
0
1
2
3

A	B	$y = \overline{A+B}$
0	0	1
0	1	0
1	0	0
1	1	0

$$y(A, B) = \sum(0) = \pi(1, 2, 3) = \overline{A \cdot B} = \overline{A+B}$$

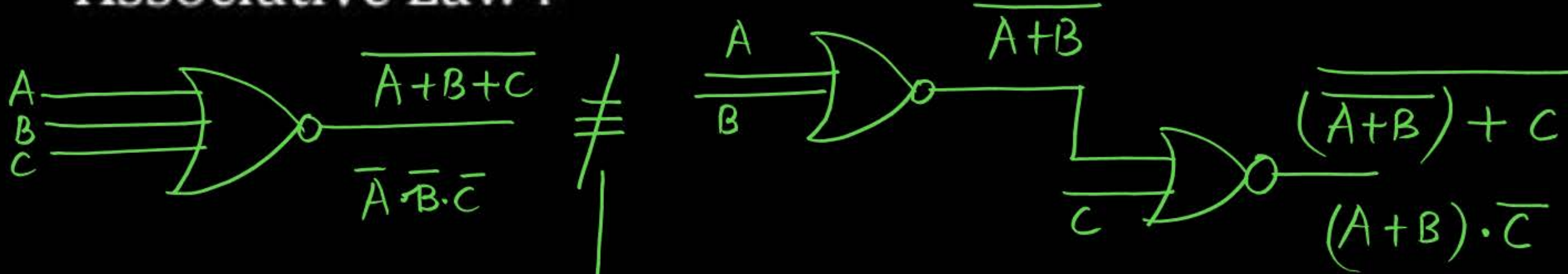


- Commutative Law :



→ It holds commutative law → position of variables is irrelevant

- Associative Law :



→ It does not hold associative law.

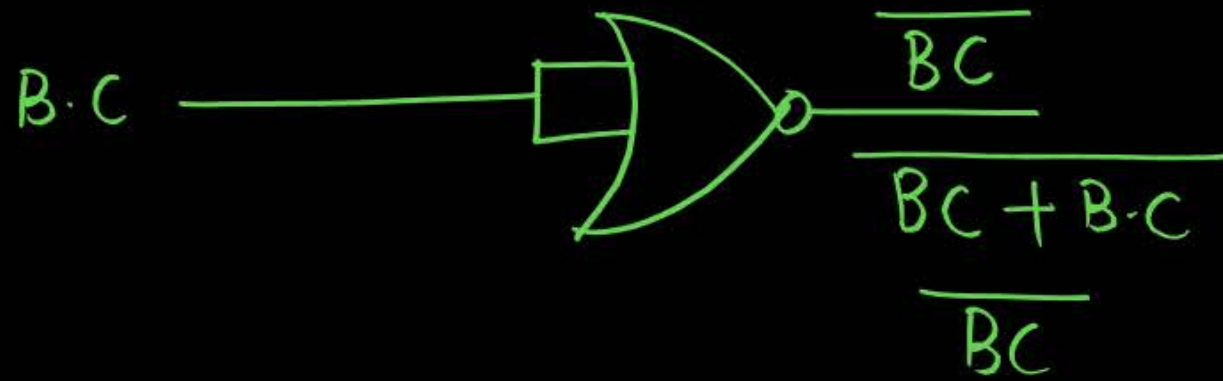
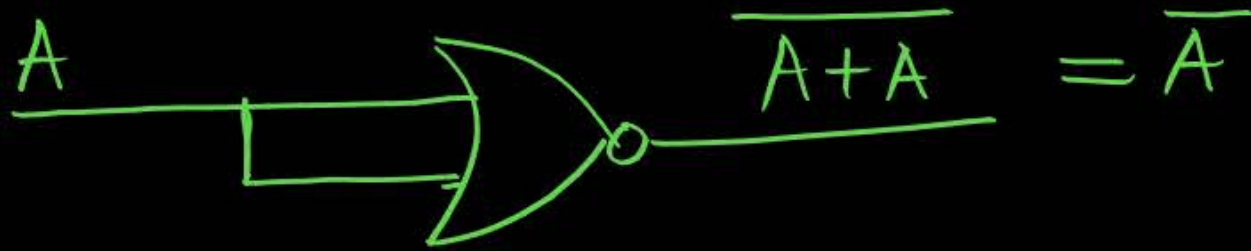
- IMP Points :

- If any one of i/p line is at logic '1' then irrespective of other i/p lines, O/p will be at logic '0'.
- O/p will be '1' only in one case if all the i/p lines will be at logic '0'.

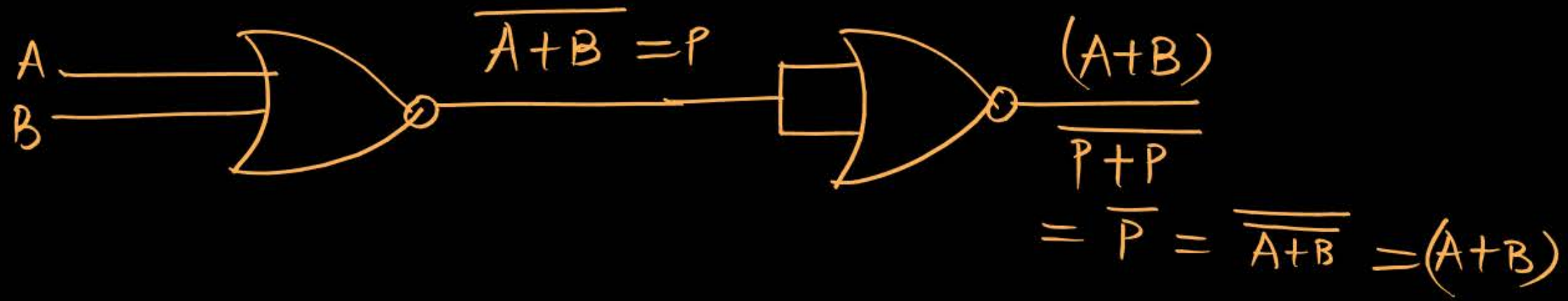
[NOR GATE as Universal Circuit]



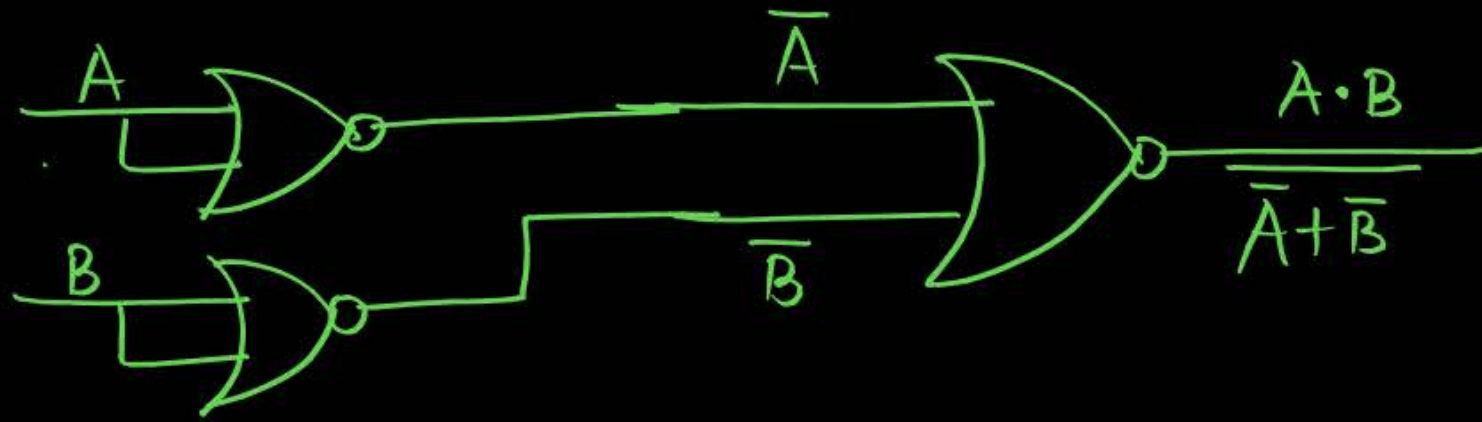
- NOT GATE



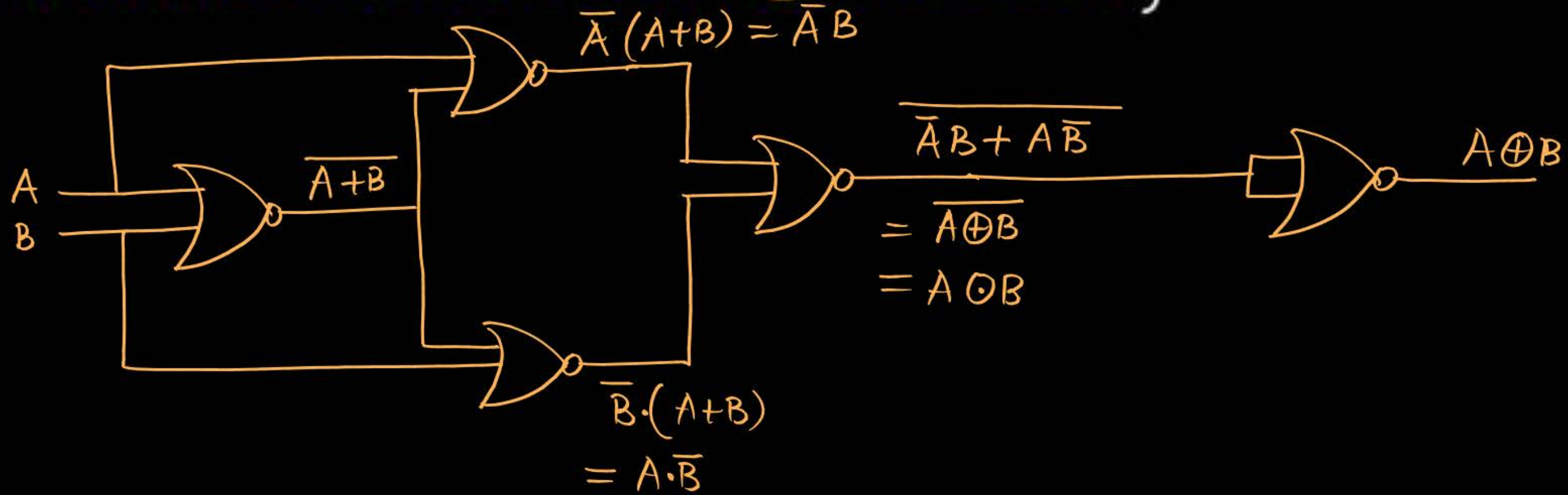
- OR GATE



- AND GATE



[XOR and XNOR GATE using NOR GATE]



2 i/p

2 i/p

GATES	No. of NAND	No. of NOR
NOT	1	1
AND	2	3
OR	3	2
XOR	4	5
XNOR	5	4

#Q. $y = (A+B) \cdot (B+C)$

Minimum no. of ^{2 i/p} NOR gate require to implement this o/p 3.

$$\left. \begin{array}{l} (A+B) \longrightarrow 2 \text{ NOR} \Rightarrow P \\ (B+C) \longrightarrow 2 \text{ NOR} \Rightarrow Q \\ P \cdot Q \longrightarrow 3 \text{ NOR} \rightarrow y \end{array} \right\} 7 \text{ NOR}$$

$$\begin{aligned} \overline{y} &= \overline{(A+B) \cdot (B+C)} = \overline{(A+B)} + \overline{(B+C)} \\ &= \overline{A+B} + \overline{B+C} \\ &= \overline{A} + \overline{B} + \overline{B} + \overline{C} \\ &= \overline{A} + \overline{B} + \overline{C} \end{aligned}$$

$\overline{A} + \overline{B} \xrightarrow{1 \text{ NOR}} R$
 $R + \overline{C} \xrightarrow{1 \text{ NOR}} S$
 $\overline{S} \xrightarrow{1 \text{ NOR}} y$

#Q. $Y = (A + \bar{B}) \cdot (A + \bar{C}) = A + \bar{B} \cdot \bar{C} = A + \overline{(B + C)} = A + R$

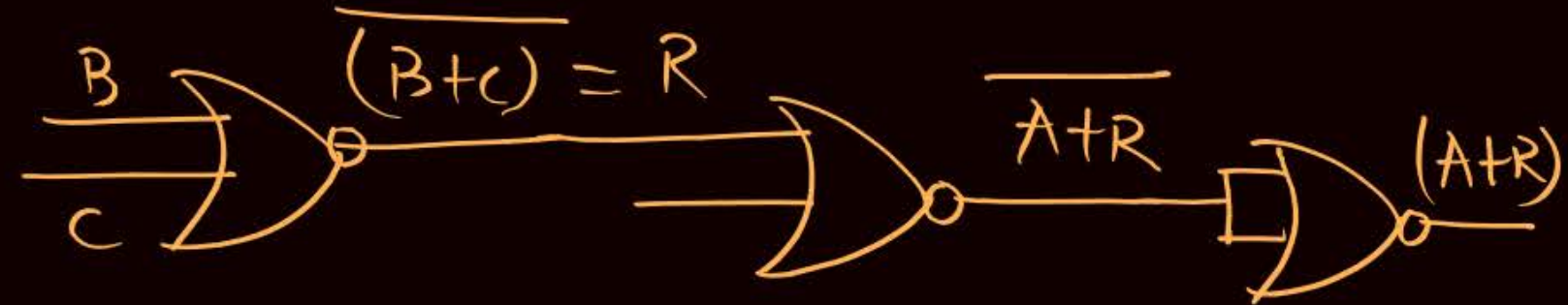
Minimum no. of 2-i/p NOR gate required to implement above function _____.

$$\bar{Y} = \overline{(A + \bar{B})} + \overline{(A + \bar{C})} = P + Q \xrightarrow{2 \text{ NOR}} 2 \text{ NOR}$$

$$Y = \overline{P + Q} \xrightarrow{1 \text{ NOR}} 1 \text{ NOR}$$

• $Y = A + \overline{(B + C)} = A + R \xrightarrow{1 \text{ NOR}}$
 $A + R \rightarrow 2 \text{ NOR}$

3 NOR



Q. $y = A + (B \cdot C) \longrightarrow$

Minimum no. of 2-i/p NOR gate required to implement y 3.

$$y = (A + B) \cdot (A + C)$$

$$\overline{y} = \overline{A+B} + \overline{A+C} = \begin{matrix} P+Q \\ \downarrow \quad \searrow \\ \text{NOR} \quad \text{NOR} \end{matrix}$$

$$y = \overline{P+Q} \rightarrow \text{NOR}$$

Q. $y = \overline{A}\overline{B} + \overline{B}\overline{C}$

Minimum no. of 2-IP NOR gate required to implement y 4.

$$y = \overline{A+B} + \overline{B+C} = P+Q$$

\swarrow 1 NOR \swarrow 1 NOR
P Q

$P+Q \rightarrow 2 \text{ NOR}$

#Q. $y = (\bar{A} + B) \cdot (A + \bar{B})$

Minimum no. of 2-i/p NOR required to implement y 4.

$$\Rightarrow \bar{y} = \overline{(\bar{A} + B)} + \overline{(A + \bar{B})} = P + Q \quad \begin{matrix} \nearrow 2 \text{ NOR} \\ \searrow 2 \text{ NOR} \end{matrix}$$

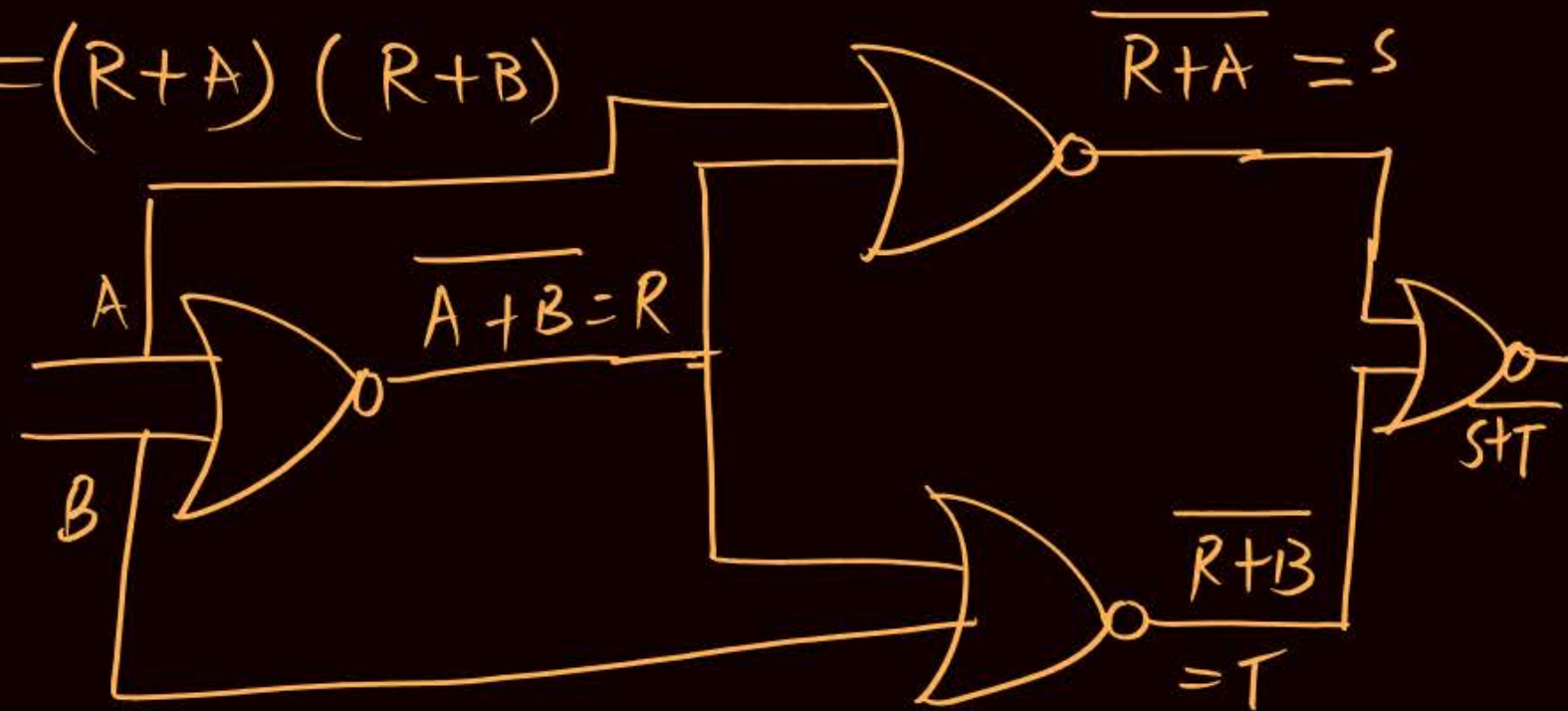
$$y = \overline{P + Q} \rightarrow 1 \text{ NOR}$$

$$y = \underline{\underline{\bar{A}\bar{B}}} + AB = \overline{(A+B)} + AB = R + (AB) = (R+A)(R+B)$$

$\nearrow 1 \text{ NOR}$

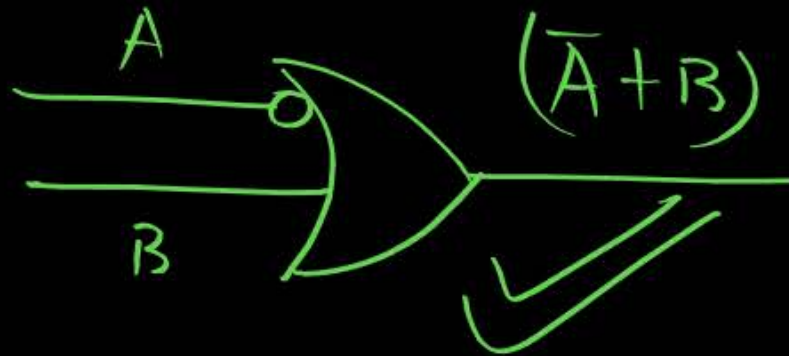
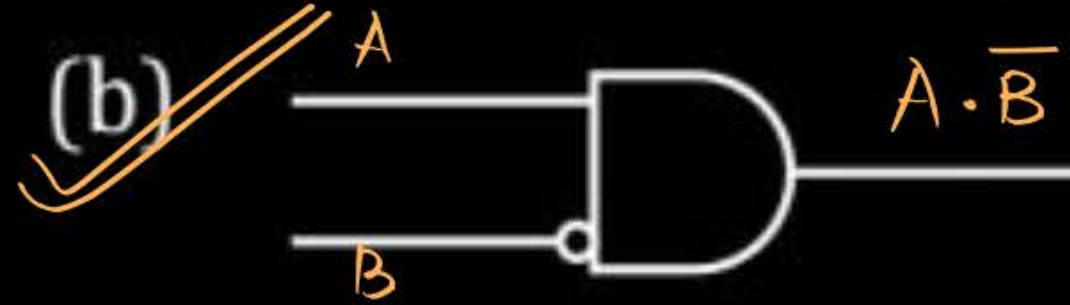
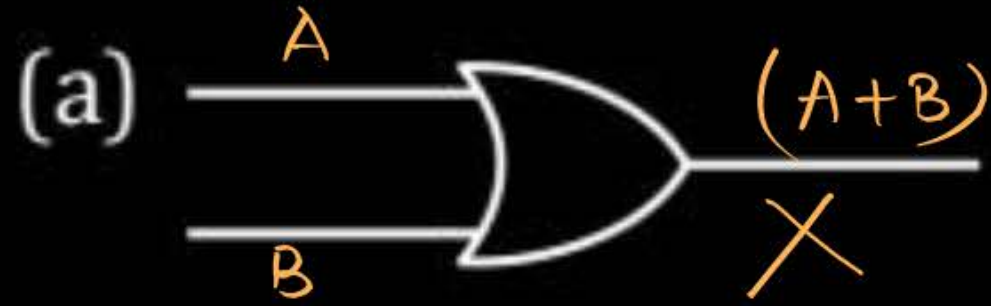
$$\bar{y} = \overline{R+A} + \overline{(R+B)} = S + T \quad \begin{matrix} \nearrow 1 \text{ NOR} \\ \searrow 1 \text{ NOR} \end{matrix}$$

$$y = \overline{S+T} \rightarrow 1 \text{ NOR}$$



[Question]

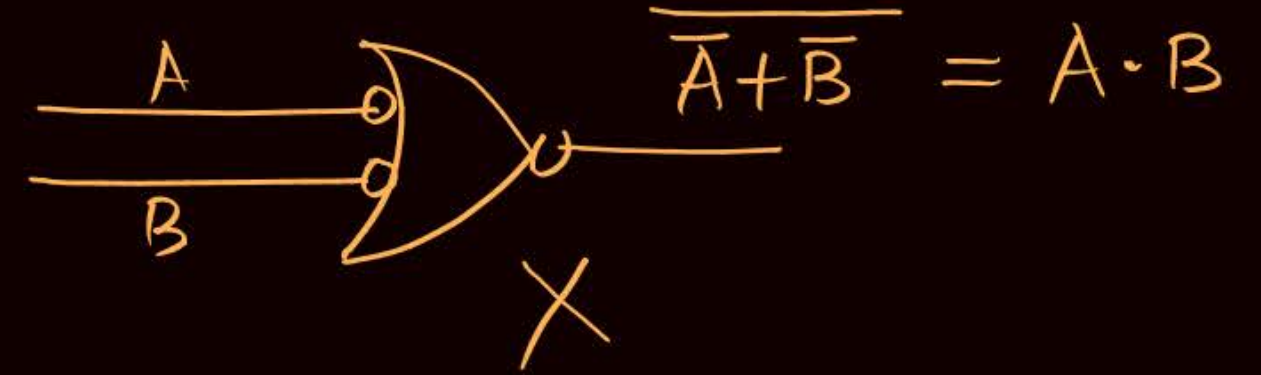
Which of the following circuit can work as universal gate ?

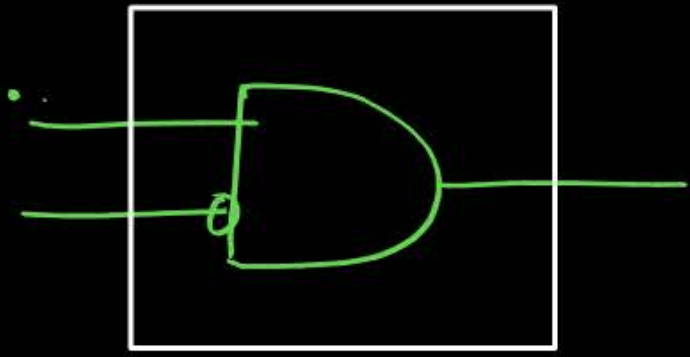


$$\text{AND} + \text{NOT} \longrightarrow \overline{\bar{A} \cdot \bar{B}} = (A + B)$$

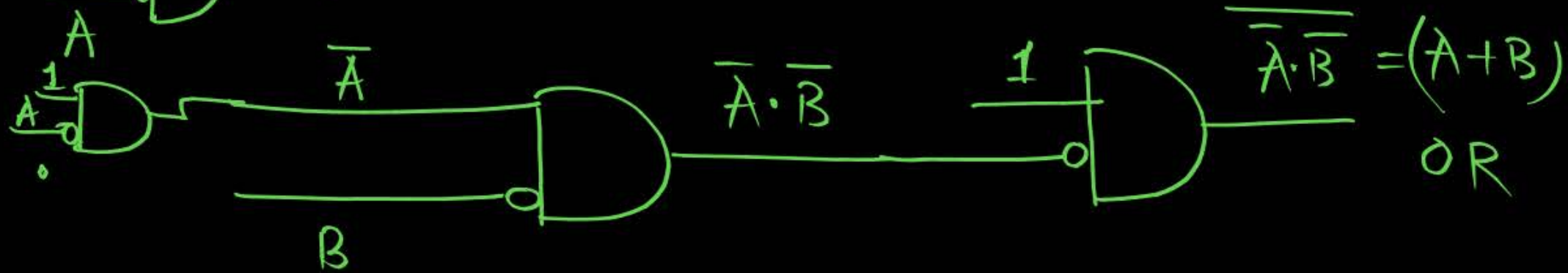
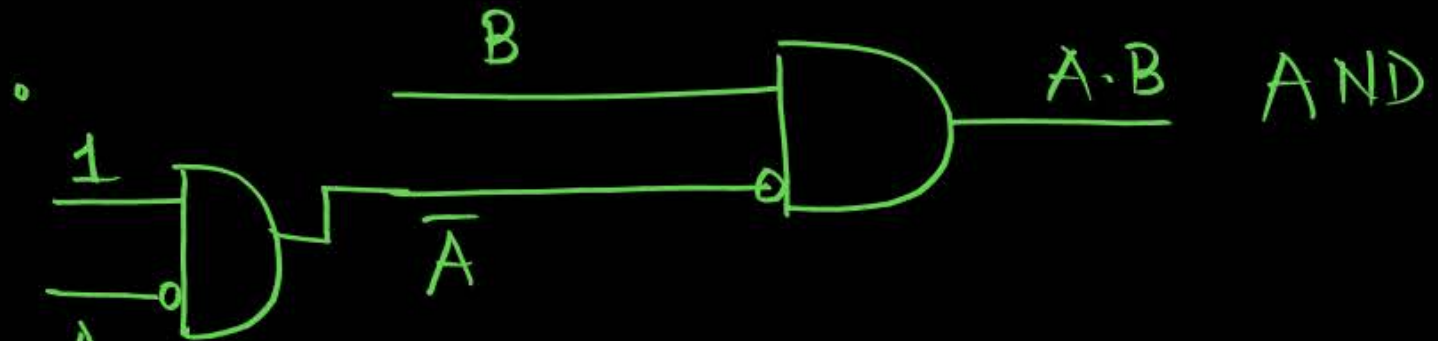
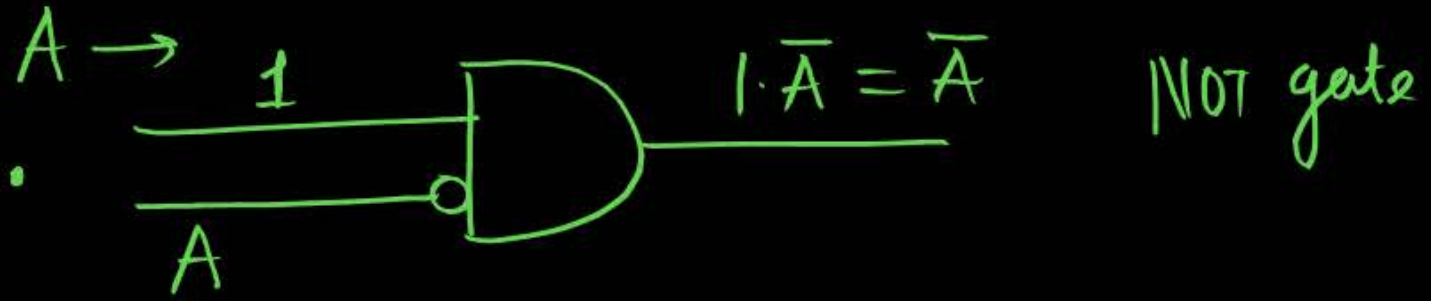
$$\text{OR} + \text{NOT} \longrightarrow \overline{\bar{A} + \bar{B}} = A \cdot B$$

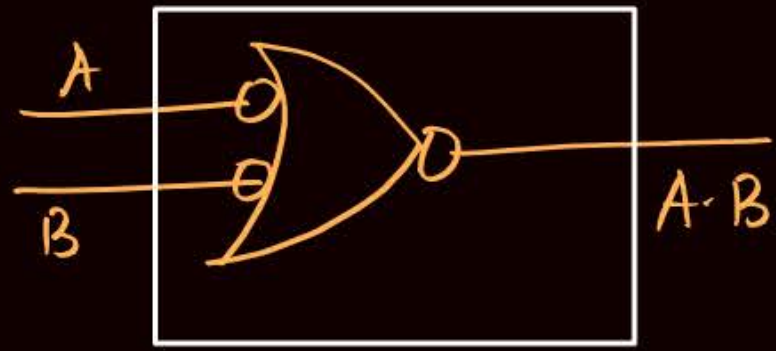
↓
Can work as universal gate.



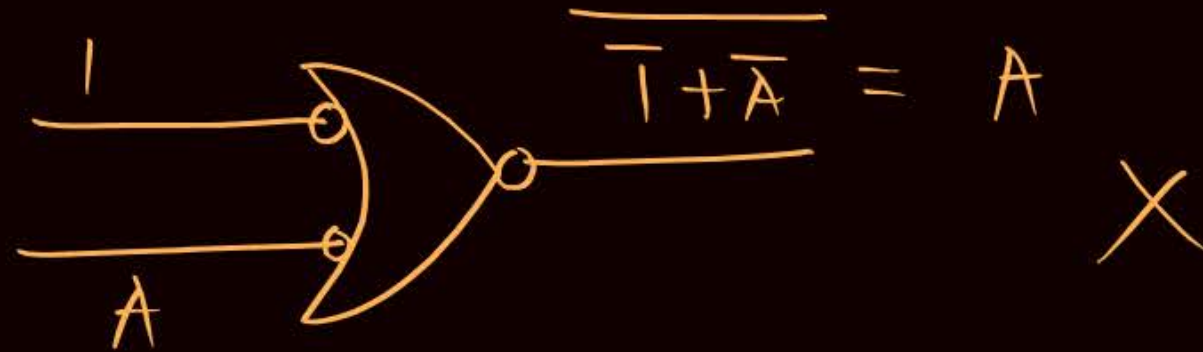


$$\overline{\overline{A} \cdot \overline{B}}$$

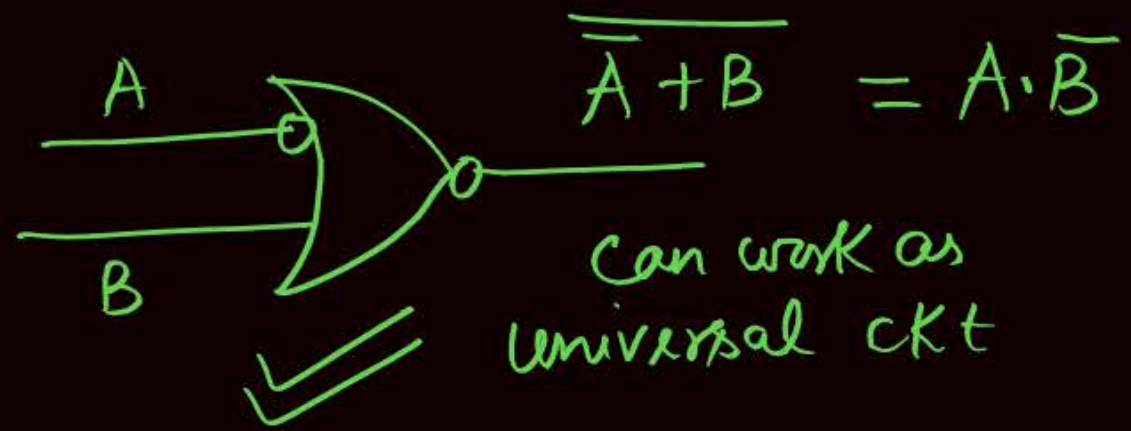




Can not work as universal gate.



NOT operation is not possible using this unit so it will not work as universal gate.



#Q.

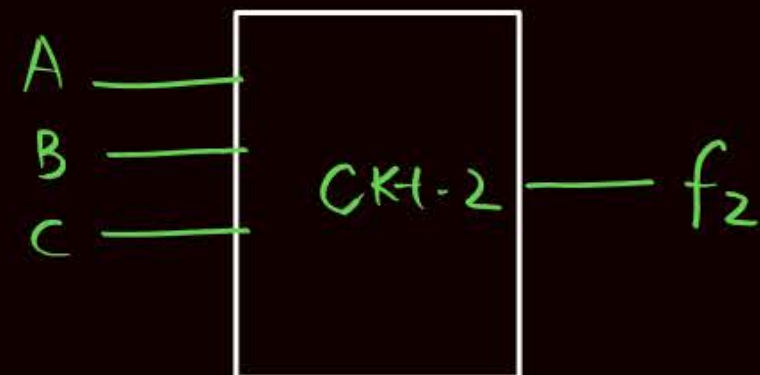
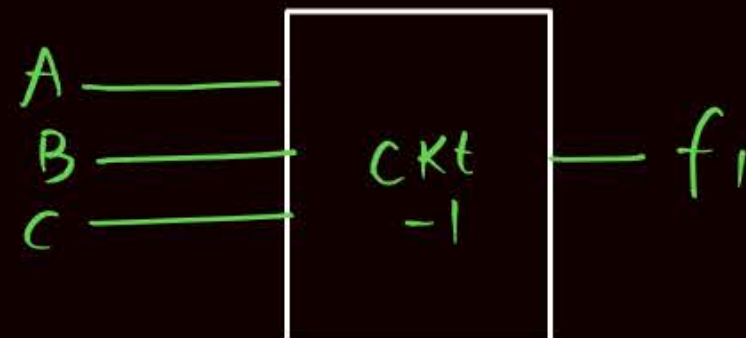
$$f_1(A, B, C) = \sum(1, 2, 3, 6, 7)$$

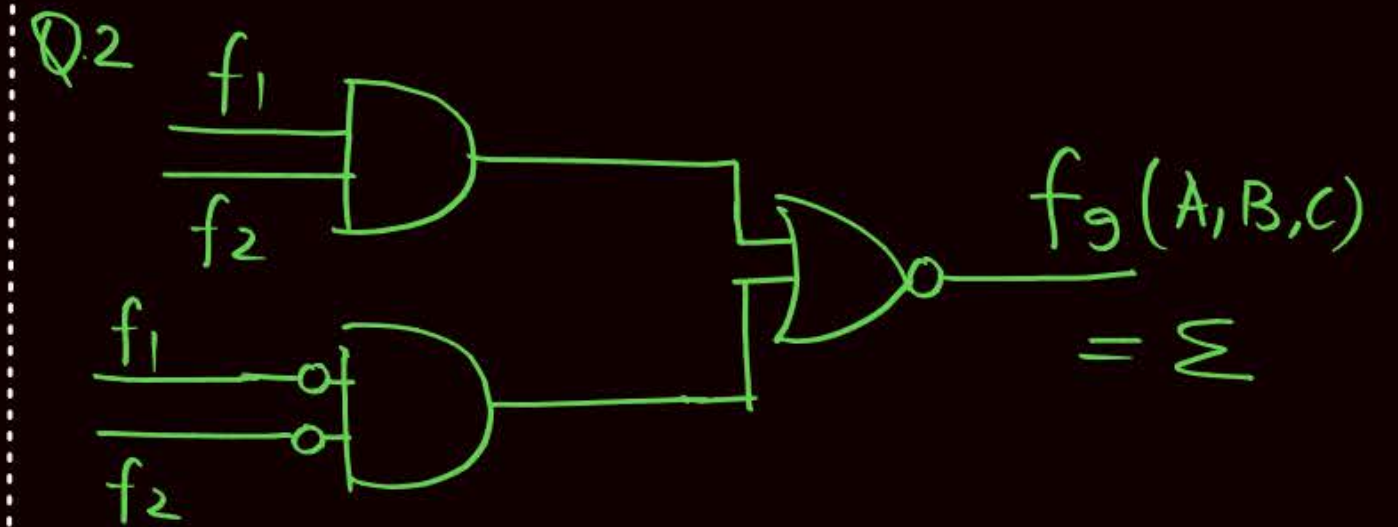
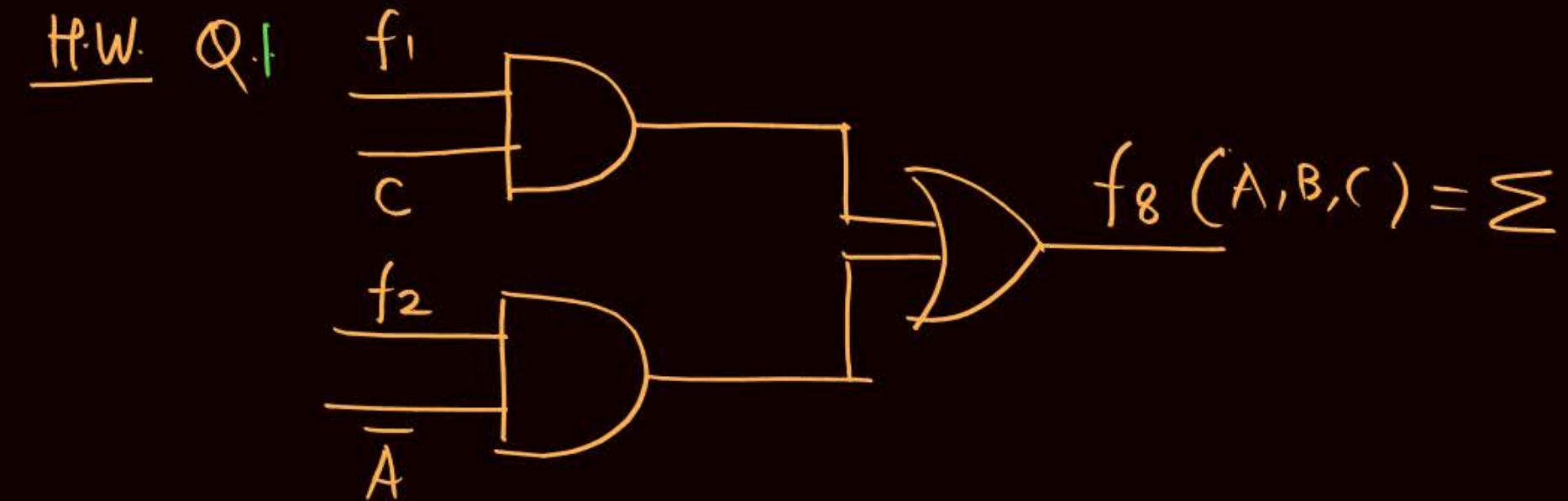
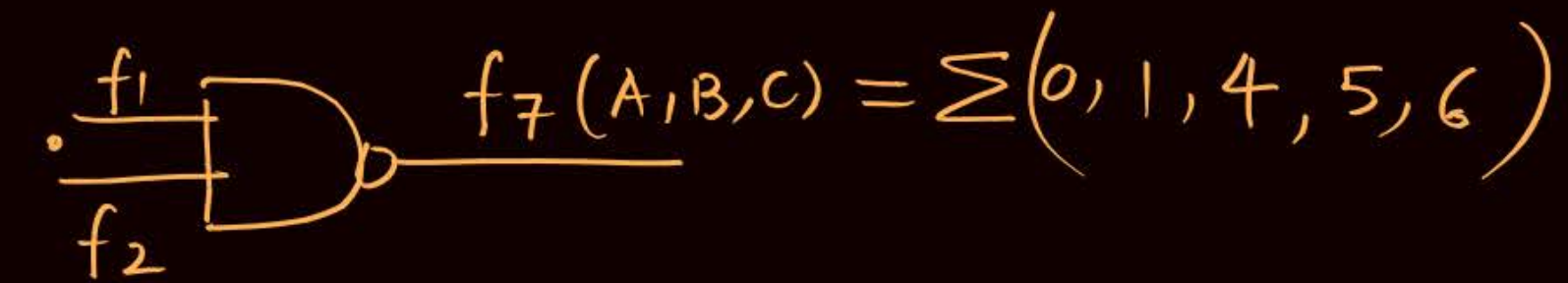
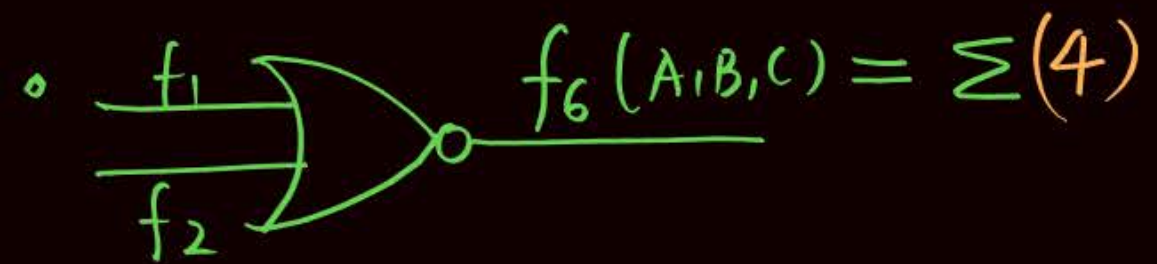
$$f_2(A, B, C) = \sum(0, 2, 3, 5, 7)$$

• f_1 and f_2 are inputs to an AND gate, outputting $f_3(A, B, C) = \sum(2, 3, 7)$

• f_1 and f_2 are inputs to an OR gate, outputting $f_4(A, B, C) = \sum(\underline{0, 1, 5, 6})$

• f_1 and f_2 are inputs to a NOR gate, outputting $f_5(A, B, C) = \sum(\underline{2, 3, 4, 7})$





• # Q. Minimum no. of NOR gate required to implement the functions given below:

a. $y = A\bar{B} + \bar{A}B$

✓ b. $y = (\bar{A} + B)(B + \bar{C})(\bar{A} + D)(\bar{C} + D)$

c. $y = (\bar{A} + \bar{B})(C + D)$



2 Minute Summary

→ Universal gates

Thank you

GW
Soldiers !

