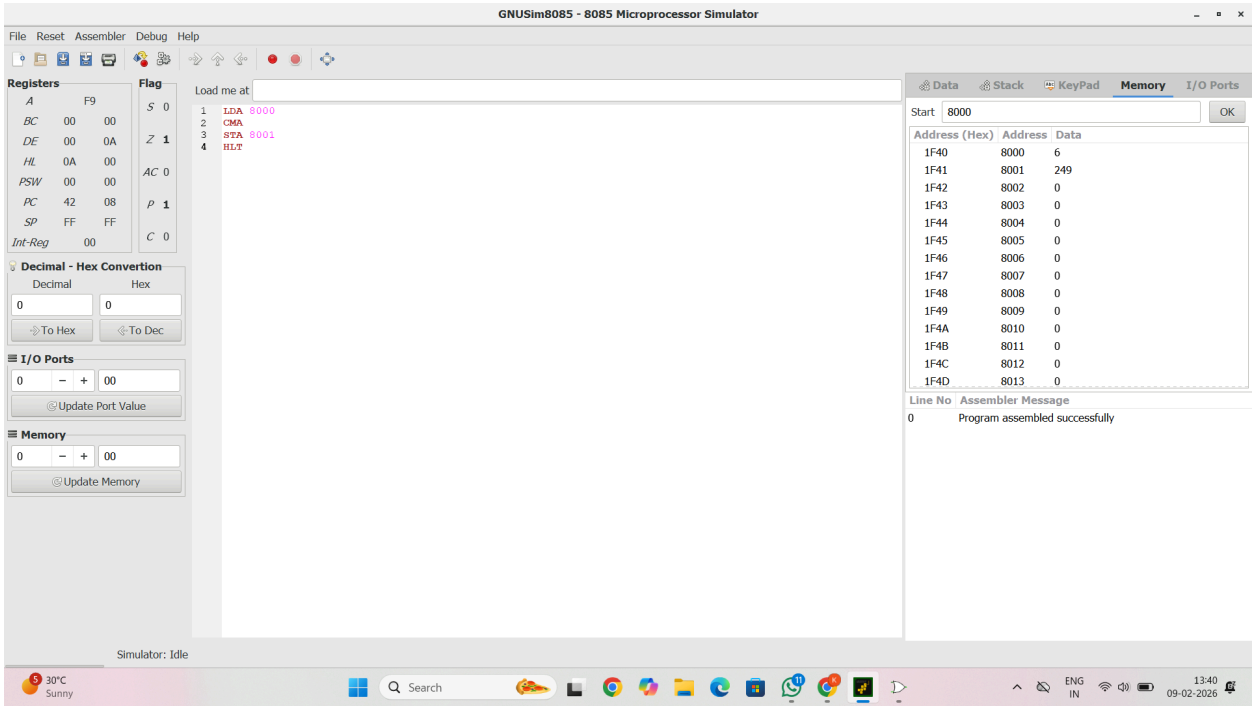
COMPUTER ARCHITECTURE CSA1264

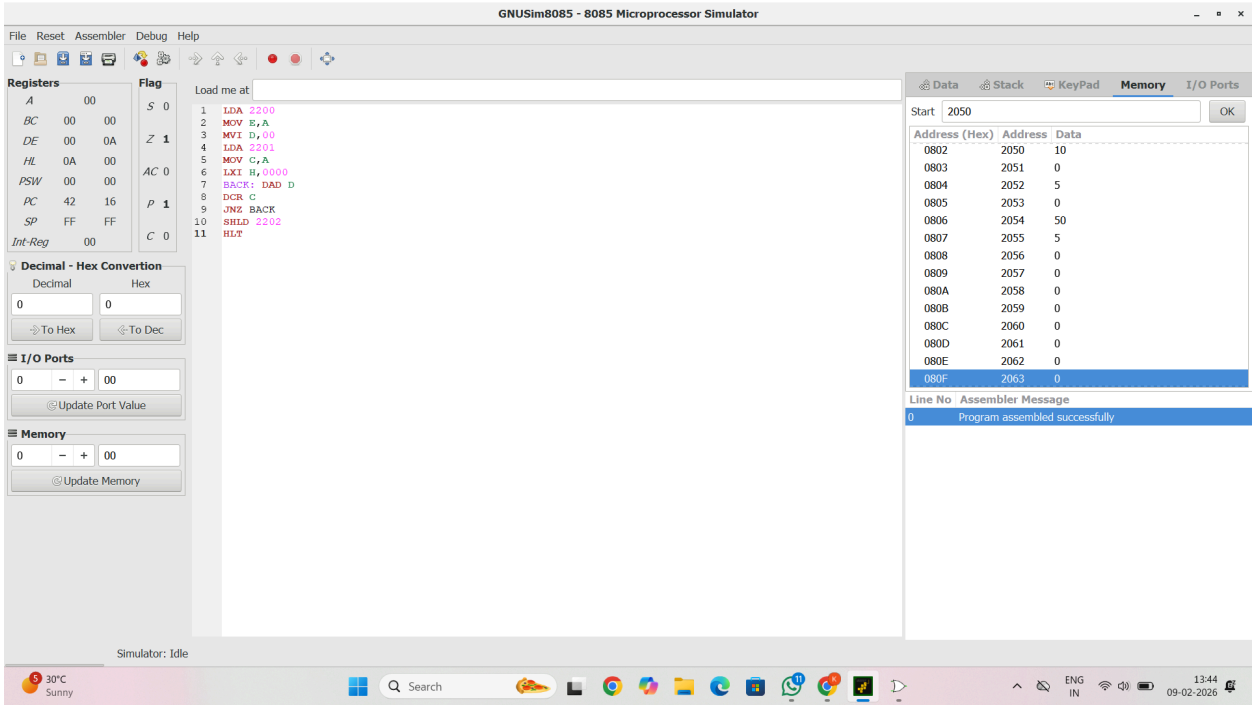
MODEL PRACTICAL EXAMINATION

REG NO: 192511083

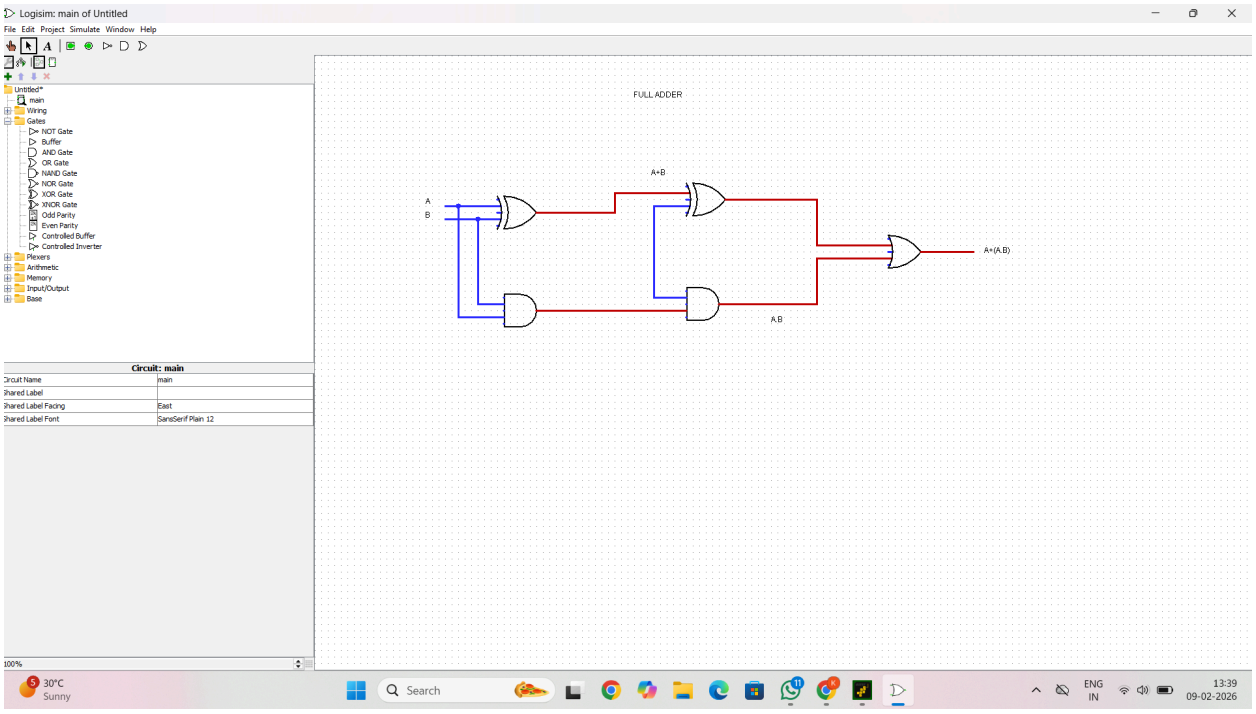
1. 1's COMPLEMENT



2. MULTIPLICATION OF TWO 8-BIT DATA



3. FULL ADDER


Logisim: main of Untitled
File Edit Project Simulate Window Help
Untitled*
main
Wiring
Gates
NOT Gate
Buffer
AND Gate
OR Gate
NAND Gate
NOR Gate
XOR Gate
XNOR Gate
Odd Parity
Even Parity
Controlled Buffer
Controlled Inverter
Plexers
Arithmetic
Memory
Input/Output
Base
Circuit: main
Circuit Name
main
Shared Label
Shared Label Facing
East
Shared Label Font
SansSerif Plain 12
100%
FULL ADDER
A
B
A+B
A.B
A+(A.B)
30°C
Sunny
Search
ENG
IN
13:39
09-02-2026

4. CONVERT BINARY TO OCTAL