X	Digital Electronics and Logic Design (DELD) - Practical Number - 11
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	Realization of Mod-N counter wing Decade Counter (IC9430)
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	Realization of Mod-6 counter wing TC 74.90
	Objective: - Redization of Mod-6 counter using IC 7490.
	Theory: -
	IC-7490 is a decade counter It
	O Ripple Counter Decade Counter) - IC - 7490 is a decade counter, It contain forur master slave flip flop and additional gating to provide a divide by two counter and a three stage binary counter which provide a sign scounter.
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	nrowide A 1 - hu 5 courter
	gowan top go to tool.
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Mod - 6 Counter:
Mod counter are cascaded counter circuits which count to a set modulus value before resting Mod - 6.

CLM Culse QA QB QC.

Initial = 0 0 0 1

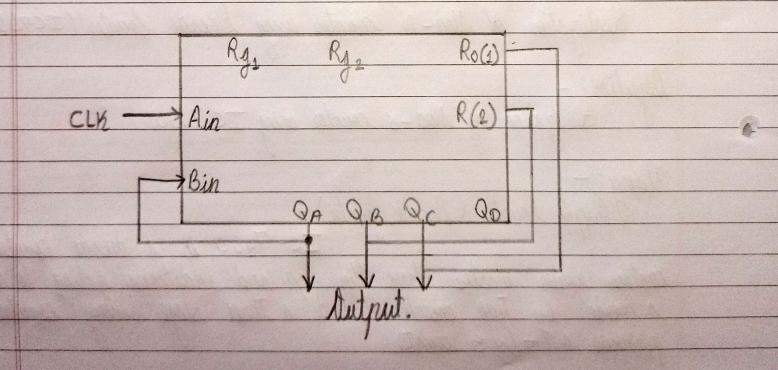
2 0 1 0

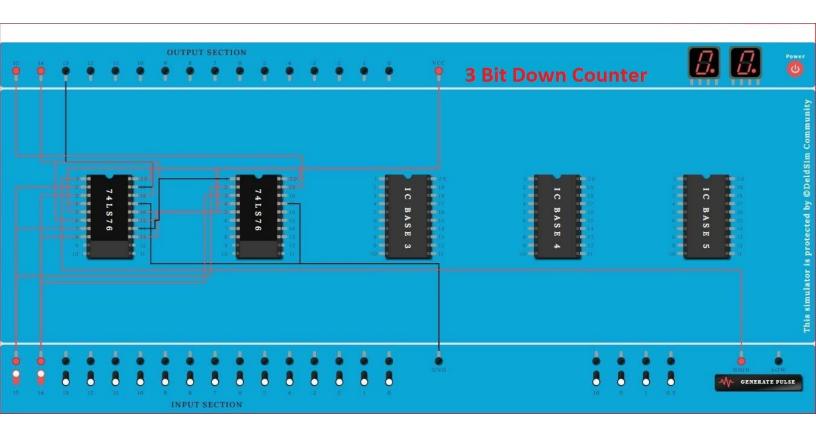
3 0 1 1

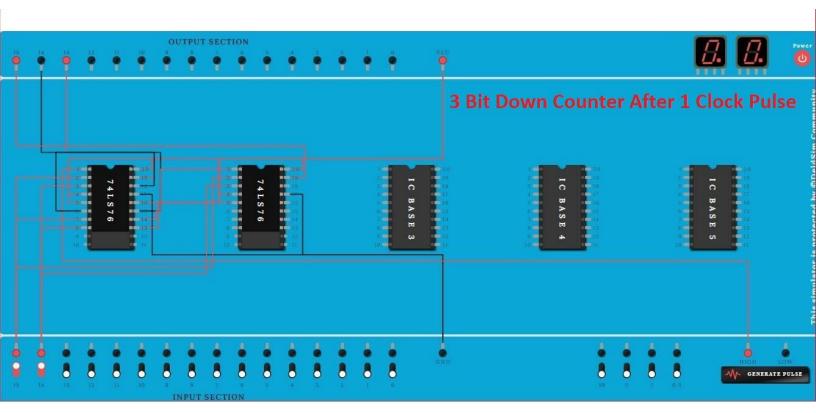
4 1 0 0

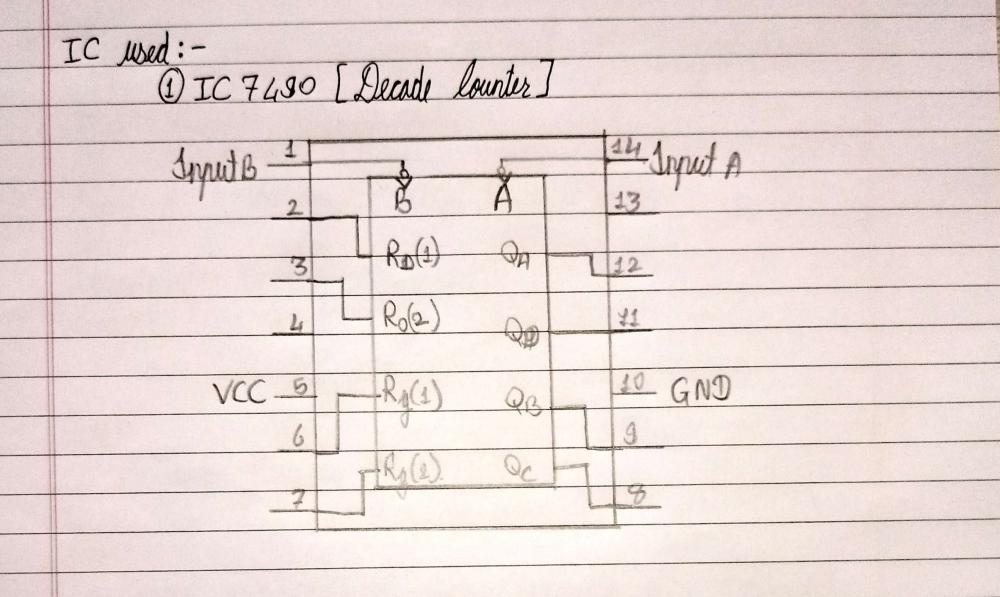
5 1 0 1

6 Reset to Zero









Conclusion: Hence, we have realized a Mod-6 counter using IC 7490.