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31. One that is a digital component is

a. latch

b. encoder

c. flip-flop

d. processor

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Answer: (b). encoder

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32. The third level of design with multiplexer consists of

a. Demultiplexer

b. mux

c. encoder

d. decoder

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Answer: (d).
decoder

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33. ASM stands for

a. algorithmic state machine

b. algorithmic solid machine

c. arithmetic state machine

d. arithmetic solid machine

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Answer: (a).
algorithmic state machine

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34. In ASM design flip-flops are considered to be

a. negative edge triggered

b. negative level triggered

c. positive edge triggered

d. negative level triggered

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Answer: (c).
positive edge triggered

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35. Discrete element of information is

a. data

b. control information

c. metadata

d. operation

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Answer: (a). data

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36. A method used to specify the sequence of algorithm is

a. map

b. data

c. flowchart

d. operation

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Answer: (c). flowchart

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37. State box is a shape of

a. square

b. rectangle

c.   rhombus

d.   pentagon

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Answer: (b).
rectangle

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38.   Conditional box has a shape of

a.   square

b.   rectangle

c.   oval

d.   pentagon

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Answer: (c).
oval

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39.   One that is not a digital component is

a.   decoder

b.   encoder

c.   flip-flop

d.   mux

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Answer: (c).
flip-flop

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40.   With every clock pulse count is

a.   decremented

b.   stopped

c.   incremented

d.   enabled

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Answer: (c).
incremented

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