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11. State box of ASM chart represents

- a. condition
- b. clock
- c. state
- d. pulse

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Answer: (c).
state

12. In state table when input pulses is greater than 5 it is necessary to use

- a. Small maps
- b. medium maps
- c. Large maps
- d. None

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Answer: (d).
None

13. One that is not present in the list of state table is

- a. present state
- b. input
- c. next state
- d. previous state

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Answer: (d).
previous state

14. Another possible method of control logic design is to use

- a. 1 flip-flop
- b. 2 flip-flop
- c. 1 flip-flop per state
- d. None

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Answer: (b).
2 flip-flop

15. Binary information is classified into

- a. data
- b. control information
- c. metadata
- d. both a and b

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Answer: (d).
both a and b

16. Difference in conventional flowchart and ASM chart is

- a. master clock
- b. flow
- c. time relationship
- d. clock

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Answer: (c).
time relationship

17. The logic design consists of

- a. 1 part
- b. 2 parts

- c. 3 parts
- d. 4 parts

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Answer: (b).
2 parts

18. Design ASM with multiplexers, is the method consists of

- a. 1 level
- b. 2 levels
- c. 3 levels
- d. 4 levels

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Answer: (c).
3 levels

19. Sequential circuit is also called

- a. state
- b. encoder
- c. flip-flop
- d. state machine

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Answer: (d).
state machine

20. ASM chart takes entire block as

- a. 1 unit
- b. 2 unit
- c. 3 unit
- d. 4 unit

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Answer: (a).
1 unit

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