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21. ASM chart is very same to

a. state diagram

b. flowchart

c. data box

d. operation

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Answer: (a).
state diagram

22. To continue the count E must be

a. enabled

b. reset

c. stopped

d. cleared

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Answer: (d).
cleared

23. At E=1, register R will be

a. enabled

b. reset

c. stopped

d. cleared

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Answer: (d).
cleared

24. The rounded corners of conditional box differentiate it from

a. state box

b. decision box

c. data box

d. conditional box

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Answer: (a).
state box

25. For going to the next state flip-flop is set to

a. 1

b. 0

c. y

d. don't cares

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Answer: (a).
1

26. Control implementation method is

a. practical

b. impractical

c. enabled

d. cleared

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Answer: (b).
impractical

27. The timing for all flip-flops in digital system is controlled by

a. Memory

b. latches

c. Master clock Generator

d. None

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Answer: (b).
latches

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28. Symbolic notation R←0 represents

a. Clear Register

b. Move register

c. Add contents to Register

d. None

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Answer: (d).
None

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29. The first level of design with multiplexer determines the register's

a. present state

b. input

c. next state

d. output

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Answer: (c).
next state

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30. The number of inputs and outputs in a state table are

a. equal

b. same

c. unequal

d. not present

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Answer: (a).
equal

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