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d) NMOS logic

c, CMCs logic raining

Answer: b

Explanation: Transistor-transistor logic is the most widely employed logic family. It is the most popular logic family.

2. The basic function of TTL gate is which of the following functions?

- a) AND
- b) OR
- c) NOR
- d) NAND

Answer: d

Explanation: The basic function of TTL gate is NAND function. It is the most popular logic family.

- 3. In TTL logic, the input transistor has a number of _____ equal to the desired fan-in of the circuit.
- a) Base
- b) Collect

a) Fm:++a:













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4. Which of the following is the propagation delay of TTL circuits?

- a) 1 s
- b) 1 ms
- c) 1 ns
- d) 1 ps

Answer: c

Explanation: The propagation delay of TTL circuit is 1 ns. It is a main characteristic of TTL circuit.

5. The standard TTL gates are marketed as _____ series.

- a) 80
- b) 82
- c) 74
- d) 08

Answer: c

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Explanation: The standard TTL gates are marketed as 74 series. They can operate up to 70° C.













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- a) Good fan-in
- b) Good fan-out
- c) High speed capability
- d) High propagation delay

Answer: d

Explanation: Schottky TTL circuits have reduced propagation delay than normal TTL circuits. The propagation delay of 10ns is very high for some applications.

7. The logic '0' of ECL is represented as _____V and logic '1' is represented as _____V.

- a) 1, 1.65
- b) 0.9, 1.75
- c) 1.2, 2.35
- d) 1.9, 4.3

Answer: b

Explanation: The logic '0' of ECL is represented as 0.9 V and logic '1' is represented as 1.75 V. ECL stands for emitter-coupled logic.













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Answer: a

Explanation: We can achieve high speed of gate by sing ECL. ECL stands for emitter-coupled logic.

9. Which of the following is not the advantage of MOS gates?

- a) Low power dissipation
- b) Small size
- c) Good immunity to noise
- d) High switching speeds

Answer: d

Explanation: MOS gates do not have high switching speed. They have limited switching capability.

10. CMOS gates are commercially available as which of the following series?

- a) 1000
- b) 2000
- c) 3000
- d) 4000













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a) True

Τl

b) False

View Answer

Answer: a

Explanation: The switching of MOS gates can be improved by using CMOS. CMOS stands for complementary MOS. It is an inverter.

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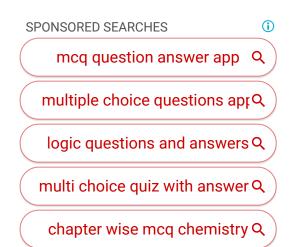
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