1.

a.

asm block

(/)



State box without decision and conditional box is

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| b. | defined block  |  |  |  |
|----|--|--|--|--|
| c. | simple block   |  |  |  |
| d. | both a and b   |  |  |  |
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|    | <b>nswer:</b> (c).<br>mple block   |  |  |  |
| 2. | ASM chart reser  | mbles with   |  |  |
| a. | map  |  |  |  |
| b. | data   |  |  |  |
| c. | flowchart  |  |  |  |
| d. | operation  |  |  |  |
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|    | <b>Answer:</b> (c).<br>flowchart  |   |
|----|---|---|
| 3. | Control sequence s  | ate is indicated by   |
| a. | state box   |   |
| b. | decision box  |   |
| c. | data box  |   |
| d. | conditional box   |   |
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| 4. | Control information   | gives knowledge about the   |
| a. | command signals   |   |
| b. | data  |   |
| c. | metadata  |   |
| d. | operation   |   |
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|    | command signals   |   |

| 5. | If system is performing no function then it is in  |
|----|--|
| a. | clear state  |
| b. | initial state  |
| c. | enable state   |
| d. | reset state  |
| Δ  | View Answer Report Discuss (/Digital-Logic-Design/Algorithmic-State-Machine/discussion/49291) Too Difficult! Search Google  Inswer: (b).                     |
|    | nitial state   |
| 6. | ASM chart has  |
| a. | 4 exits  |
| b. | 3 exits  |
| c. | 2 exits  |
| d. | any number of exits  |
|    | View Answer Report Discuss (/Digital-Logic-Design/Algorithmic-State-Machine/discussion/49292) Too Difficult! Search Google  Inswer: (d).  ny number of exits |
| 7. | ASM chart is composed of   |
|    |  |

2 elements

3 elements

a.

b.

| c. | 4 elements                     |   |
|----|--------------------------------|---|
| d. | 5 elements                     |   |
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|    | <b>nswer:</b> (b).<br>elements |   |

- **8.** In designing ASM with multiplexers, the registers hold
- **a.** present binary state
- **b.** input
- **c.** next binary state
- **d.** output

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**Answer:** (a). present binary state

- **9.** All inputs are synchronized with
- a. master clock
- **b.** clock pulses
- c. counter
- **d.** latch

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**Answer:** (b). clock pulses

- 10. A state table for a controller is a list of present states and inputs and their corresponding
- a. Pervious states and output
- **b.** next states and outputs
- c. current states
- d. None

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Answer: (a).

Pervious states and output

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