

Karemma S Myaginamani

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8197028626

CAREER OBJECTIVE

To continuously enhance my knowledge, skills and experience by getting involved in challenging environment and being innovative, and utilize them for personal and organizational growth to the best of my ability.

EDUCATIONAL QUALIFICATIONS

Degree	Name of the Institution	University/Board	Year of Passing	Percentage/CGPA
B.E in Electronics and Communication Engineering.	SKSVMACET, Lakshmeshwar	VTU	2020	7.35
Pre-University college	M D R PU College. Savanur	Department of Pre-University board	2015	73%
SSLC	M D R S Ganjigatti	Karnataka Secondary Education Examination Board.	2013	84%

TECHNICAL SKILLS

Programming languages- Java, web designing.

Tools Known-Cadence tool, Eclipse, Visual Studio Code.

TECHNICAL SUMMARY

JAVA-Basics of Java:

- Knowledge on Operators, Control Statements, Functions, Arrays and Strings. OOPS:
- Knowledge on Class, Object, Types of Variables, Constructors, Constructor overloading, Blocks, Access specifiers, Encapsulation, Method overloading,
- Method overriding, Constructor Chaining, Typecasting, Inheritance, Abstraction, polymorphism. Java interface, coupling, Libraries

Web design:

- Basics of Web designing.
- HTML, Javascript

CO-CURRICULAR ACTIVITIES

Project1-HIGH PERFORMANCE FIR FILTER FOR FIXED AND RECONFIGURABLE APPLICATION.

In this project we designed FIR filter for fixed and Reconfigurable FIR filter by using Sub blocks flip flops,register unit,pipeline adder unit,inner product unit, coefficient adder unit, 32 bit full adder unit. For these sub blocks we have synthesis,simulate and RTL

Discription of code in Xiling 32.2 Software. We explore the possibility of block FIR filter in transpose form for Area -Delay efficient realization of large order fir filter for both fixed and reconfigurable application.

Project2-CMOS INVERTER FOR HIGH PERFORMANCE.

In this project we did a CMOS Inverter with High performance using Cadence tool. Using Cadence tool we did the schematic,symbol and the analog layout which is a fully customised inverter . Once the Layout is done we did the verification ,that is DRC,LVS and post simulation for the given inverter .And we did it for different drive strength of 1x,2x,4x,16x.

INTERNSHIP

1.Completed my Internship at Eklakshya Innovations Pvt.Ltd on VLSI.

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WORKSHOP

- 1.Workshop on PCB Designing.
- 2.Workshop on Arduino board.

SKILLS

1. Adaptability and Time management.
- 2.Carrying Positive Attitude.

PERSONAL DETAILS

Father's Name : Subhas Myaginamani

Date of birth : 19 -09 - 1997

Permanent address: D/O Subhas Myaginamani

Madli. Post. Dundashi. Tq Shiggaon. Dist. Haveri

Languages known: Kannada, English, Telugu

Hobbies : singing and hearing songs, Playing game during my free time

DECLARATION:

I hereby declare that the information mentioned above is true to the best of my knowledge.

Date

Place: Madli

Karemma S Myaginamani