KAVYA O

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PERSONAL PROFILE

Dedicated and ambitious Electronics and Communication Engineer, with a strong foundation in embedded systems, signal processing and programming languages. Proficient in merging technical skills with strategic thinking to achieve impactful results in the software and electronics industry.

ACADEMIC CREDENTIALS

Federal Institute of Science and Technology, Ernakulam

Bachelor of Technology, Electronics And Communication Engineering, 7.56 CGPA, May 2025

Mambaram Higher Secondary School, Mambaram

HSE, 80%, May 2020

SSLC, 89% 2018

TECHNICAL AREAS OF INTEREST

- **Technical Skills**: Python|C| Verilog programming | 8051 Assembly language programming
- **Others**: Logic Circuit Design | Signal processing

CERTIFICATIONS AND VALUE-ADDED COURSES

- CMOS Digital VLSI Design by NPTEL

ACADEMIC PROJECTS

- Micro project: Robotic Arm Control System
- Minor project: Anti-Theft Flooring System

SEMINARS AND PAPER PRESENTATIONS

Highly Efficient Machine Learning Approach for Color Classification of Olive Fruits

ACHEIVEMENTS

- Minor project submitted for patent - now published

POSITIONS OF RESPONSIBILITY

- Class Representative, Student Council Member
- Volunteer in inter college fest organized by FISAT

PERSONAL TRAITS

- Leadership skills
- Problem solving
- Creativity

CO-CURRICULAR AND EXTRA CURRICULAR ACTIVITIES

- Student member of IEEE College chapters
- Student member of Swan Nature Club

HOBBIES AND INTERESTS

- Crafting
- Drawing
- Dancing

REFERENCES

References are available upon request.