4 Bit Adder Circuit and Truth Table :
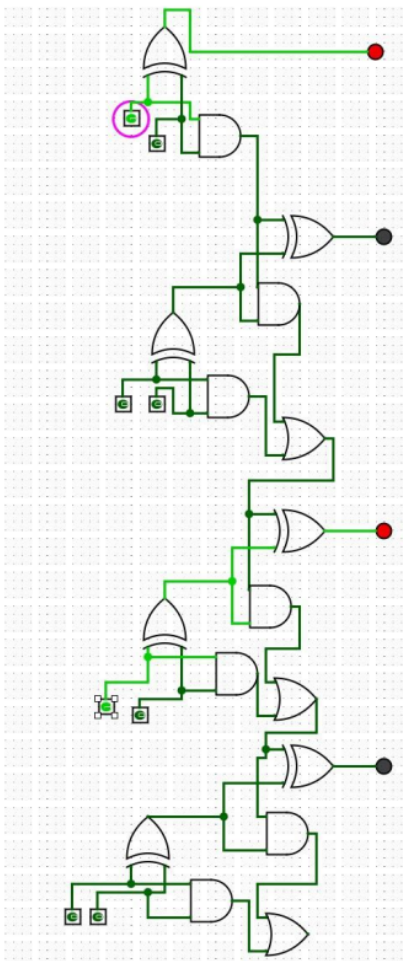
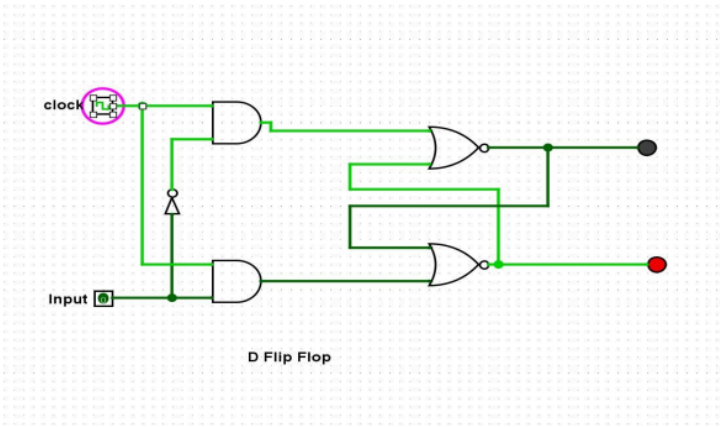| Input A	Input B	Output
0101	0000	0101
0101	0001	0110
0101	0010	0111
0101	0011	1000
0101	0100	1001
0101	0101	1010
0101	0110	1011
0101	0111	1100
0101	1000	1101
0101	1001	1110
0101	1010	1111
0101	1011	0000
0101	1100	0001
0101	1101	0010
0101	1110	0011

R_S Flip Flop Circuit and Truth Table :
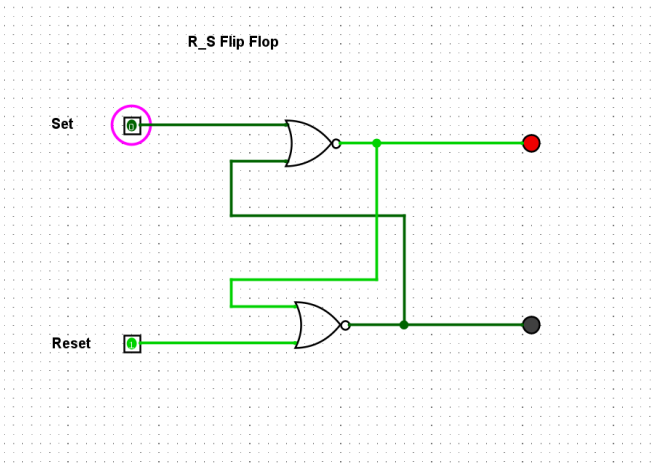
Set	Reset	Q	Q'
1	0	0	1
1	1	0	0
0	1	0	1
0	0	1//0	0//1

D Flip Flop Circuit and Truth Table

D Flip Flop

Clock	Pin	Q	Q'
0	0	Unchanged	Unchanged
0	1	Unchanged	Unchanged
1	1	1	0
1	0	0	1

JK Flip Flop Circuit and Truth Table
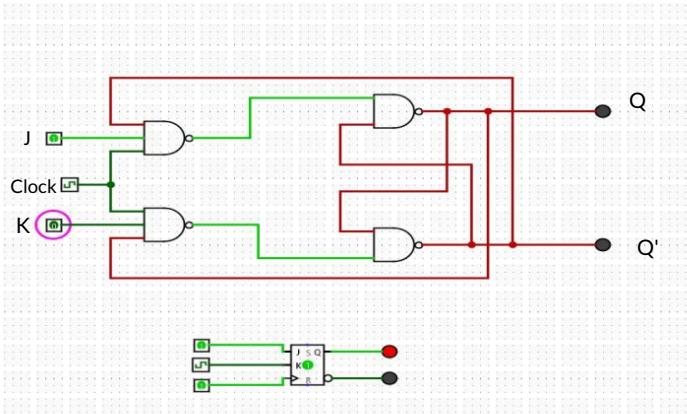
J	K	Q (When clocked)	Q' (when clocked)
0	0	Unchanged	Unchanged
1	0	1	0
0	1	0	1
1	1	0	1