The rule for address mapping between Modbus & Fatek

• Mapping rule:

Modbus Facon

0xxxx Discrete elements of Ynnn,Xnnn,Mnnnn,Snnn,Tnnn,Cnnn

4xxxx Data Registers of Rnnnn ,Dnnnn,Tnnn,Cnnn

or

00xxxx Discrete elements of Ynnn,Xnnn,Mnnnn,Snnn,Tnnn,Cnnn

40xxxx Data Registers of Rnnnn ,Dnnnn,Tnnn,Cnnn

• Available range:

Modbus (5-code)	Facon	Description
$00001 \sim 00256$	Y0∼Y255	(Discrete Output)
$01001 \sim 01256$	$X0 \sim X255$	(Discrete Input)
$02001 \sim 04002$	$M0 \sim M2001$	(Discrete M Relay)
$06001 \sim 07000$	S0~S999	(Discrete S Relay)
$09001 \sim 09256$	T0~T255	(Status of T0 \sim T255)
$09501 \sim 09756$	C0~C255	(Status of $C0 \sim C255$)
$40001 \sim 44168$	R0~R4167	(Holding Register)
45001~45999	R5000~R5998	(Holding Register or ROR)
46001~48999	D0~D2998	(Data Register)
$49001 \sim 49256$	$T0 \sim T255$	(Current Value of $T0 \sim T255$)
$49501 \sim 49700$	C0~C199	(Current Value of C0~C199, 16-bit)
49701~49812	C200~C255	(Current Value of C200 \sim C255 , 32-bit)

or

Modbus (6-code)	Facon	Description	
$000001 \sim 000256$	Y0~Y255	(Discrete Output)	
001001~001256	X0~X255	(Discrete Input)	
$002001 \sim 004002$	$M0 \sim M2001$	(Discrete M Relay)	
$006001 \sim 007000$	S0~S999	(Discrete S Relay)	
$009001 \sim 009256$	$T0 \sim T255$	(Status of $T0 \sim T255$)	
$009501 \sim 009756$	C0~C255	(Status of $C0 \sim C255$)	
400001~404168	R0~R4167	(Holding Register)	
$405001 \sim 405999$	R5000~R5998	(Holding Register or ROR)	
$406001 \sim 408999$	D0~D2998	(Data Register)	
$409001 \sim 409256$	$T0 \sim T255$	(Current Value of $T0 \sim T255$)	
$409501 \sim 409700$	C0~C199	(Current Value of C0~C199, 16-bit)	
409701~409812	C200~C255	(Current Value of C200~C255, 32-bit)	
** 02001~03912	M0~M19	(General purpose M Relay)	

** 02001~03912	$M0 \sim M1911$	(General purpose M Relay)
$03913 \sim 04002$	$M1912 \sim M2001$	(Special M Relay)
$40001 \sim 43840$	R0~R3839	(General purpose R Register)
$43841 \sim 43904$	R3840~R3903	(Analog Input Register)
43905~43968	R3904~R3967	(Analog Output Register)
$43969 \sim 44168$	R3968~R4167	(Special Register)