



OBJECTIVE

I am a final year student in the department of electronics and communication engineering(ECE). I have a strong interest in the core electronics domain, particularly in areas such as soc verification, VLSI design, .I am keen to learn and gain practical experience, constantly seeking new ways to improve my skills.

EXPERIENCE

Maven silicon

Internship

Mini project on AHB to APB bridge RTL design

August 2024 -
September
2024

EDUCATION

Bharati Vidyapeeth (deemed to be university) college of engineering, pune

2021-2025

Btech (ece)

St.joseph's school

Senior secondary

SKILLS

Basic VHDL programming

Basic verilog