

ARM Instructions Worksheet #5

Multiplication

Single/Double-Length, Signed/Unsigned

Prerequisite Reading: Chapter 5

Revised: March 26, 2020

Objectives: To use the web-based simulator ("CPULator") to better understand ..

- 1. The MUL, SMULL, and UMULL instructions
- 2. Single versus double-length products.
- 3. Signed versus unsigned multiplication.

To do offline: Answer the questions that follow the listing below. (Numbers at far left are memory addresses.)

	.syntax .global	unified _start
00000000 _start: 00000004 00000008 0000000C 00000010 00000014 00000018 0000001C	LDR LDR MUL SMULL LDR LDR MUL UMULL	R2,=+3 // *** EXECUTION STARTS HERE *** R3,=-5 R0,R2,R3 R0,R1,R2,R3 R2,=3 R3,=0x80000000 R0,R2,R3 R0,R1,R2,R3
00000020 done:	B .end	done

Note: Use this hex to decimal converter to convert 64-bit products to decimal.

•		
	R2 (8 hex digits)	R2 (as signed decimal)
What is left in R2 by the LDR pseudo-instruction at 00000000016?	00000003	+ 3
	R3 (8 hex digits)	R3 (as signed decimal)
What is left in R3 by the LDR pseudo-instruction at 00000004 ₁₆ ?	FFFFFFB	- 5
	R0 (8 hex digits)	R0 (as signed decimal)
What product is left in R0 by the MUL instruction at 00000008 ₁₆ ?	FFFFFFF	-15
WH		
What is left in R1.R0 by the SMULL R1 (8 hex digits)	R0 (8 hex digits)	R1.R0 (as signed decimal)
instruction at 0000000C ₁₆ ? FFFFFFF	FFFFFFF	-15
Did the single-length signed product produced by the previous MUL	Yes: No:	
	R2 (8 hex digits)	R2 (as unsigned decimal)
What is left in R2 by the LDR pseudo-instruction at 00000010 ₁₆ ?	00000003	3
	R3 (8 hex digits))	R3 (as unsigned decimal)
What is left in R4 by the LDR pseudo-instruction at 00000014 ₁₆ ?	80000000	2 147483648
if not a typo, then 00000000 & 0	R0 (8 hex digits)	R0 (as unsigned decimal)
What product is left in R0 by the MUL instruction at 00000018 ₁₆ ?	80000000	2147483648

What is left in R1.R0 by the UMULL instruction at 0000001C ₁₆ ?	R1 (8 hex digits)	R0 (8 hex digits) %000 0000	R1.R0 (as unsigned decimal)
Did the single-length unsigned produc	et produced by the previous	MUL overflow?	Yes: No:
Getting ready: Now use the simulator to c	collect the following inform	ation and compare to your earlie	r answers.
1. Click <u>here</u> to open a browser for t	the ARM instruction simulat	or with pre-loaded code.	
Note: You can change the number for needed. For 64-bit products, use this h		w between hex, unsigned decimal	and signed decimal as
Step 1: Press F2 exactly 2 times to execute	e the two LDR pseudo-instruc	tions (MOV, MVN) to provide the o	perands
		R2 (8 hex digits)	R2 (as signed decimal)
What is left in R2 by the LDR pseudo-	instruction at 00000000 ₁₆ ?	0000003	3
What is left in R3 by the LDR pseudo-	instruction at 00000004 ₁₆ ?	R3 (8 hex digits)	R3 (as signed decimal)
Step 2: Press F2 exactly once to execute to	he MUL RØ,R2,R3 instructi	on.	
What product is left in R0 by the MUL	instruction at 00000008 ₁₆ ?	R0 (8 hex digits)	R0 (as signed decimal)
Step 3: Press F2 exactly once to execute to	he SMULL R0,R1,R2,R3 ii	estruction.	
What is left in R1.R0 by the SMULL instruction at $0000000C_{16}$?	R1 (8 hex digits)	RO (8 hex digits)	R1.R0 (as signed decimal)
	tttttt	++++++1	
instruction at 0000000C ₁₆ ?	fffffff produced by the previous MU	fffffff L overflow?	
instruction at $0000000C_{16}$? Did the single-length signed product p	produced by the previous MU te the two LDR pseudo-instruction at 00000010 ₁₆ ?	fffffff L overflow? tions (MOV, MOV) to provide the of R2 (8 hex digits) 0000003	
instruction at 0000000C ₁₆ ? Did the single-length signed product p Step 4: Press F2 exactly 2 times to execute	produced by the previous MU the two LDR pseudo-instruction at 00000010 ₁₆ ? Assumerationstruction at 00000014 ₁₆ ?	FFFFFFI L overflow? Stions (MOV, MOV) to provide the overflows (8 hex digits) R2 (8 hex digits) R3 (8 hex digits)) 80000000	Yes: No: V Perands R2 (as unsigned decimal) R3 (as unsigned decimal) 2 4 7 4 8 3 6 4 6
instruction at 0000000C ₁₆ ? Did the single-length signed product part of the single-length signed part of the single-length signed part of the signed p	produced by the previous MU the two LDR pseudo-instruction at 0000001016? Assuming the note a hypo	Ffffffl L overflow? Stions (MOV, MOV) to provide the overflow R2 (8 hex digits) Cooococca R3 (8 hex digits)) Cooococca Coococca Cooococca Coococca Cooococca Coocca Coocca	Yes: No: V Perands R2 (as unsigned decimal) R3 (as unsigned decimal)
instruction at 0000000C ₁₆ ? Did the single-length signed product parts of the single-length signed parts of the signed parts o	produced by the previous MU the two LDR pseudo-instruction at 0000001016? Assuming the note a hypo	Ffffffl L overflow? Stions (MOV, MOV) to provide the overflow R2 (8 hex digits) Cooococca R3 (8 hex digits)) Cooococca Coococca Cooococca Coococca Cooococca Coocca Coocca	Yes: No: V Perands R2 (as unsigned decimal) R3 (as unsigned decimal) 2 4 7 4 8 3 6 4 6
instruction at 0000000C ₁₆ ? Did the single-length signed product part of the single-length signed part of the single-length signed part of the signed p	produced by the previous MU the two LDR pseudo-instruction at 00000010 ₁₆ ? Assumed instruction at 00000014 ₁₆ ? If not a hypo the MUL RO, R2, R3 instruction	Ffffffl L overflow? Stions (MOV, MOV) to provide the overflow R2 (8 hex digits) Cooococca R3 (8 hex digits)) Cooococca Coococca Cooococca Coococca Cooococca Coocca Coocca	Yes: No: V Perands R2 (as unsigned decimal) R3 (as unsigned decimal) 2 4 7 4 8 3 6 4 6
instruction at 0000000C ₁₆ ? Did the single-length signed product p Step 4: Press F2 exactly 2 times to execute What is left in R2 by the LDR pseudo- What is left in R4 by the LDR pseudo- Step 5: Press F2 exactly once to execute to	ffffffffffffproduced by the previous MU the two LDR pseudo-instruction at 00000010 ₁₆ ? Assuming the MUL RO, R2, R3 instruction at 00000018 ₁₆ ?	Ffffffl L overflow? R2 (8 hex digits) R3 (8 hex digits)) R3 (8 hex digits)) R0 (8 hex digits) R0 (8 hex digits)	Yes: No: V Perands R2 (as unsigned decimal) R3 (as unsigned decimal) 2 4 7 4 8 3 6 4 6
instruction at 0000000C ₁₆ ? Did the single-length signed product part of the signed part of the signe	ffffffffffffproduced by the previous MU the two LDR pseudo-instruction at 00000010 ₁₆ ? Assuming the MUL RO, R2, R3 instruction at 00000018 ₁₆ ?	Ffffffl L overflow? R2 (8 hex digits) R3 (8 hex digits)) R3 (8 hex digits)) R0 (8 hex digits) R0 (8 hex digits)	Yes: No: V Perands R2 (as unsigned decimal) R3 (as unsigned decimal) 2 4 7 4 8 3 6 4 6