

ARM Instructions Worksheet #3

Addressing Modes

Prerequisite Reading: Chapter 4

Revised: March 26, 2020

Objectives: To use the web-hased simulator ("CPULator") to better understand the four addressing modes:

1. Immediate Offset Addressing: [R1] and [R1,4]

2. Register Offset Addressing: [R1,R2] and [R1,R2,LSL 2]

3. Post-Indexed Addressing: [R1],4

4. Pre-Indexed Addressing: [R1,4]!

To do offline: Answer the questions that follow the listing below. (Numbers at far left are memory addresses.)

```
unified
                      .syntax
                      .global
                                    _start
                      .skip
                                   0x100
 00000100
           Array32:
                                                 // uint32_t Array[4] ;
                      .word 0xBEEFBEEF
 00000104
                      .word 0xC0DEC0DE
 00000108
                      .word 0xF00DF00D
 0000010C
                      .word 0xFACEFACE
                                                // *** EXECUTION STARTS HERE ***
                            R1,=Array32
 00000110
           start:
                     LDR
                                                // Address provided by R1
 00000114
                     LDR
                            R0,[R1]
 00000118
                     LDR
                            R0,[R1,4]
                                                // Address = R1 + 4
 0000011C
                     LDR
                            R2.=8
                                                // R2 = Offset = 8
00000120
                     LDR
                            R0,[R1,R2]
                                                // Address = R1 + R2
                                                // R2 = Subscript = 3
                     LDR
                            R2,=3
00000124
                            RØ,[R1,R2,LSL 2]
                                               // Address = R1 + 4*R2
00000128
                     LDR
                                               // Address = R1; Post-Increment
                           R0,[R1],4
0000012C
                     LDR
                                               // Address = R1 + 4; Pre-Increment
                           R0,[R1,4]!
00000130
                    LDR
                                               // infinite loop
                           done
00000134 done:
                    .end
```

What hex address is copied into R1 by the LDR instruction at address 00000110₁₆?

What hex <u>data</u> is copied from the address in R1 by the LDR at address 00000114₁₆?

What hex <u>data</u> is copied into R0 by the LDR instruction at address 00000118₁₆?

What hex address did that value come from?

What hex <u>data</u> is copied into R0 by the LDR instruction at address 00000120₁₆?

What hex address did that value come from?

00000100 0xBEEFBEEF 0xCODECODE 00000104 0xFOODFOOD 00000108

What hex <u>data</u> is copied into RØ by the LDR instruction at address 00000128_{16} ?	OXFACEFACE
What hex <u>address</u> did that value come from?	00000100
What hex <u>data</u> is copied into R0 by the LDR instruction at address 0000012C ₁₆ ?	DXBEFFBEEF
What hex <u>address</u> did that value come from?	00000100
What hex <u>address</u> is left in R1 by the LDR instruction at address 0000012C ₁₆ ?	00000104
What hex \underline{data} is copied into R0 by the LDR instruction at address 00000130_{16} ?	OXFOODFOOD
What hex <u>address</u> did that value come from?	00000108
What hex <u>address</u> is left in R1 by the LDR instruction at address 00000130 ₁₆ ?	00000108
1. Click here to open a browser for the ARM instruction simulator with pre-loaded code. 2. Press Ctrl-M to open the memory display window and drag-n-drop it about halfway to 3. In the "Memory" window, enter 0x100 into the search box and press Enter to highlig Step 1: Press F2 exactly 2 times to execute the first two LDR instructions. (The 3 rd LDR should be search box and press Enter to highlighter than the search box and press Enter than the search box and press Enter than the search box and the search box and press Enter than the search box and the sea	the right. the that address for easy reference.
What hex <u>address</u> is copied into R1 by the LDR instruction at address 00000110 ₁₆ ?	00000100
What hex <u>data</u> is copied from the address in R1 by the LDR at address 00000114 ₁₆ ?	OXBEEFBEEF
Step 2: Press F2 exactly once to execute the LDR R0, [R1,#4]	
What hex <u>data</u> is copied into R0 by the LDR instruction at address 00000118 ₁₆ ?	Ox CODÉ CODE
What her auto is copied into no by the 221 minutes of at address coccorrection	OX CODE COOL
What hex <u>address</u> did that value come from?	00000104
,	00000104
What hex <u>address</u> did that value come from?	00000104
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