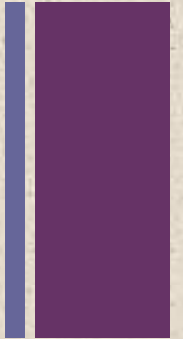




Elements of Bus Design



Type		Bus Width	
Dedicated		Address	
Multiplexed		Data	
Method of Arbitration		Data Transfer Type	
Centralized		Read	
Distributed		Write	
Timing		Read-modify-write	
Synchronous		Read-after-write	
Asynchronous		Block	

Timing of Synchronous Bus Operations

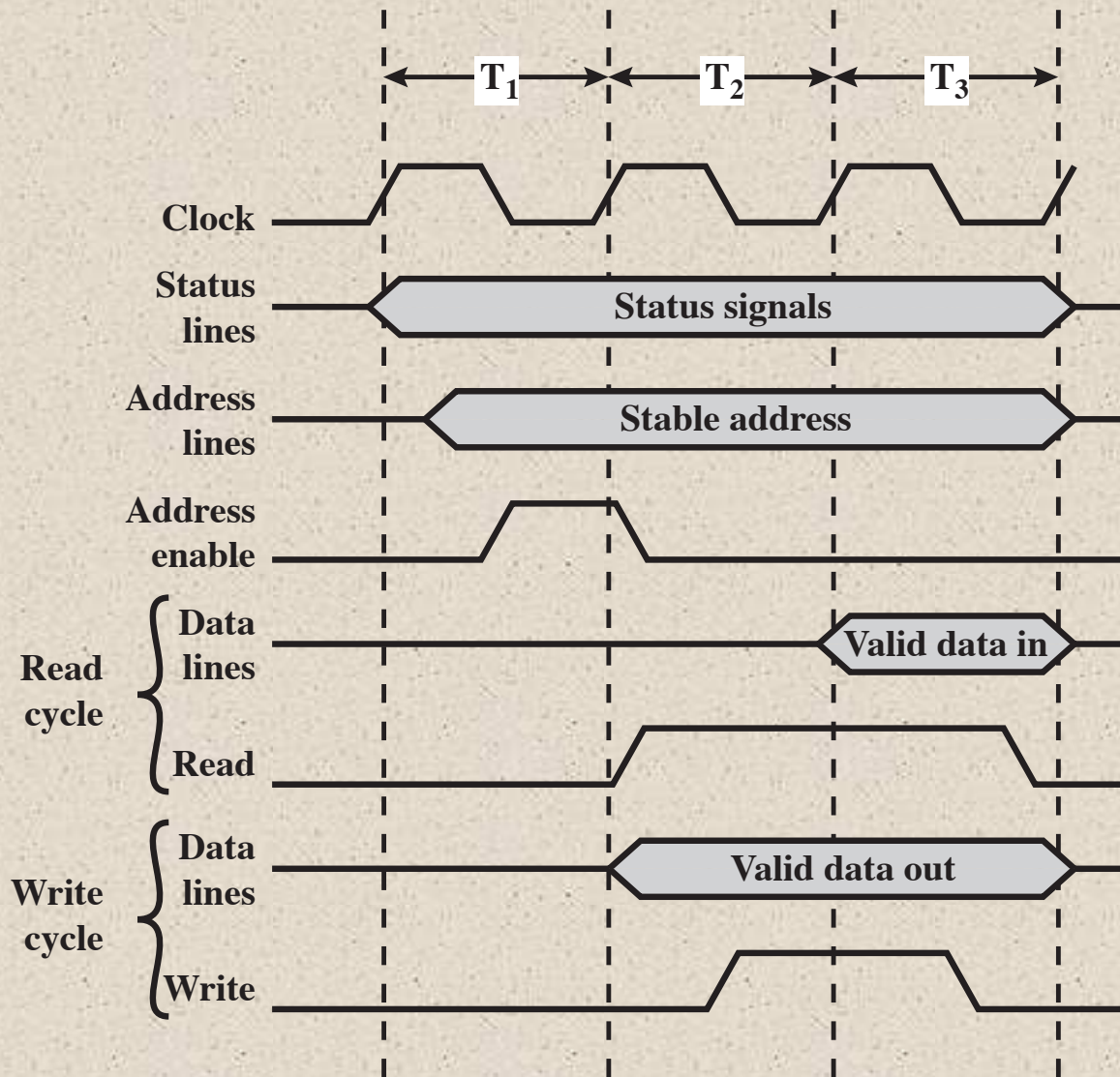
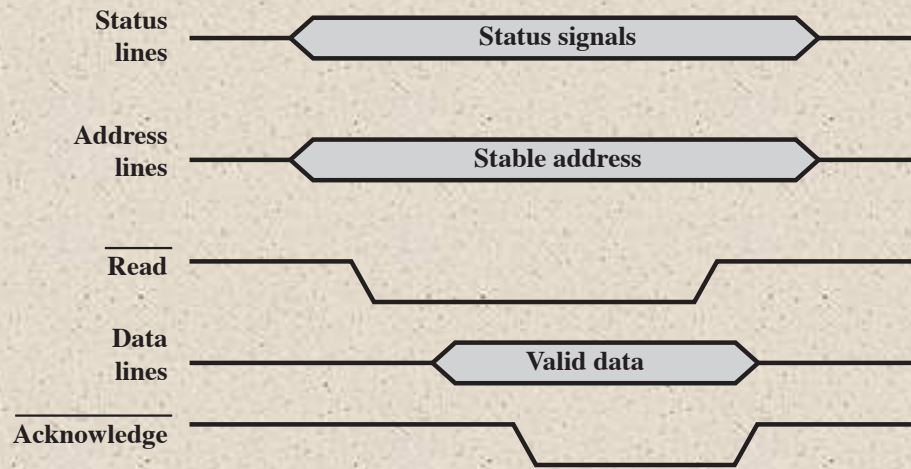
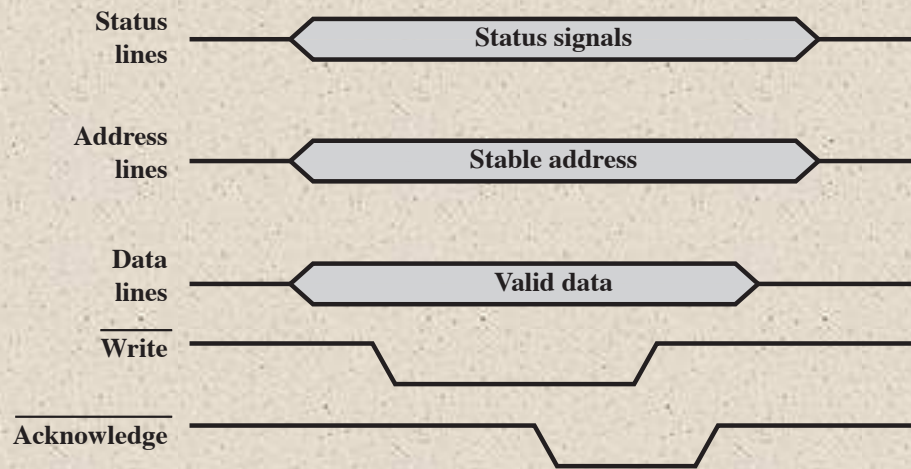


Figure 3.18 Timing of Synchronous Bus Operations



(a) System bus read cycle



(b) System bus write cycle

Timing of Asynchronous Bus Operations

Figure 3.19 Timing of Asynchronous Bus Operations