

Analytic Approach of Improve Multilevel Feedback Queue Scheduling

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Abstract – Multilevel feedback scheduling is a kind of process scheduling algorithm where process with available remaining CPU bursts can be moved or shifted between queues to complete its job. In a multilevel feedback queue, its architecture is divided into multiple queues with a priority level. Shifting between queues happens with the corresponding CPU bursts of the process, if the process takes too much CPU time required in that queue it will be shifted to a lower priority queue. The paper aims to configure to improve the current multilevel feedback queue scheduling where problems in the given standard algorithm will have a corresponding solution. Comparison of the state of the processes running in MLFQ scheduling will be analyzing its performance of the configured CPU scheduling technique to make it much better and applicable in the process scheduling in operating systems. Also, this paper describes the interactive processes.

Keywords— CPU burst time, feedback analysis, starvation, response time, turnaround time, waiting time, aging, starvation, parameters

I. INTRODUCTION

Currently, many CPU scheduling algorithms are existing like FCFS, SJF, SRTF, Priority Scheduling, Round Robin Scheduling, MLQ and MLFQ. The MLFQ is one of the most well-known approaches to feedback scheduling. The fundamental problem that the MLFQ tries to address is optimizing turnaround time, which can be done by running shorter jobs first, but the OS doesn't know how long a job will run for. Additionally, MLFQ wants to make a system feel responsive to users, therefore minimizing response time as well. While algorithms like Round-Robin are great for response time, they have terrible turnaround time. Similarly, SJF minimizing turnaround time, but has poor response time [8].

Given that we won't know how long a process will run, how can the scheduler learn as the system runs, and make the best scheduling decisions [8]?

The MLFQ is a great example of a system that learns from the past to predict the future. Future predicting approaches are common in CS, and it is critical that they be accurate [8].

The MLFQ has components like other CPU scheduling algorithms. Scheduler is the component of the kernel that selects which process to run next [1]. The scheduler (or process scheduler, as it is sometimes called) can be viewed as the code that divides the finite resource of processor time between the runnable processes on a system. In MLFQ [2][3], processes are scheduled according to their remaining CPU burst time and they are shifted down from queue to queue as they have some

remaining CPU burst time [4]. Every queue has unique time slice that gradually increases from upper level queue to lower level queue.

This paper will indicate the necessary rules or actions to consider in some conditions. It will aim to improve the processes involved in the MLFQ scheduling to which the addressing the problems that may encounter may be resolved with certain configuration. Such that the processes running in the operating system may lead to better performance and prevent some issues.

A good scheduling algorithm is the one that can optimize the performance measures. The optimization performance measures are; maximizing CPU utilization, maximizing throughput, minimizing turnaround time, minimizing waiting time, minimizing response time, maximizing scheduler efficiency, and minimizing the context switching.

II. RELATED LITERATURE

There are ways to improve the performance of the processes entering to the system that lead to many approaches. In paper [2], Reducing MLFQ scheduling starvation with feedback and exponential averaging, the approach that is related to what will aim in this paper is that it shows a technique that avoids MLFQ starvation, which will result to low overhead and will not lead to risks and failure on the scheduling action of interactive and high-priority processes. Adding a second level of feedback to redirect a "safe" amount of CPU time to the lowest-priority queue to prevent starvation of processes in that queue.

Another approach is in paper [6], recurrent neural network has been utilized to find both the number of queues and the optimized quantum of each queue. Also, in order to prevent any probable faults in processes' response time computation, a new fault tolerant approach has been presented. The experimental results show that using the IMLFQ algorithm results in better response and waiting time in comparison with other scheduling algorithms. Here this proposed neural network takes inputs of quantum of queues and average response time and getting the required inputs it takes the responsibility of finding relation between the specified quantum changes with average response time. It can find the quantum of a specified queue with the help of optimized quantum of lower queues. Thus, this network fixed changes and specify new quantum which overall optimize the scheduling time. In the paper [6] smoothed competitive analysis is applied to multilevel feedback algorithm. Smoothed analysis is basically mixture of average

case and worst-case analysis to explain the success of algorithms. This paper analyses the performance of multilevel feedback scheduling in terms of time complexity. Any performance enhancing approach can use this approach for performance analysis in terms of time complexity. Another approach in paper [7], multilevel feedback queue scheduling algorithm is implemented in Linux 2.6 kernel and new Linux2.6 scheduler performance compared with the proposed approach. It describes two algorithms elaborately and then for different load of job, which are running in background, this scheduler is applied for calculating the average response time. And to maintain simplicity inverse relationship is maintained between priority of processes and time slice length.

This paper is a real guideline for designer of cognitive scheduling systems. Now to analyze the performance enhancement, the proposed approach is implemented and simulated in Condor [8] which provides high throughput computing environment, that handles job queue mechanism, scheduling policy, priority scheme, resource monitoring and resource management, based upon the policy fixed for execution Condor executes the job when user submits the job.

Another paper [10], efficient implementation of Multilevel Feedback Queue Scheduling, dynamic time quantum is also used which further improves the efficiency of the scheduling. Here the dynamic time quantum of the queues is calculated based on the burst time of the processes. Time quantum of the second queue is the burst time of the $(2n/3)^{th}$ process (where n is the number of processes remaining after the execution in the first queue) and time quantum of the third queue is burst time of the largest remaining process. Thus 66% of the processes get executed in the second queue and remaining processes in the last queue thus preventing the problem of starvation of huge burst time processes.

And in paper [11], involves the design and development of new CPU scheduling algorithm (the Hybrid Scheduling Algorithm using genetic approach). Like any genetic algorithms, it comes with an approach that will find the best variable to which it may result to better outcome which in this paper the variable will be using the fitness function. The idea is to choose an initial random population, calculate the entity fitness using fitness function (based on minimum time), first-rate pairs of best-ranking entity using fitness function to reproduce, breed new generation through crossover and inversion while terminating condition becomes true. Using genetic operators' crossover, fitness function and inversion we get final population.

III. LITERATURE SURVEY

A. Scheduling Algorithms

There are different scheduling algorithms existing today:

a. First-Come, First-Served

First-Come, First-Served (FCFS) [4] is the most basic scheduling algorithm. It simply allocates the CPU to the process that requested it first. All other processes that request the CPU are placed in the ready queue and are served in the order in which they arrive. This algorithm is non-preemptive; once a process is given control of the CPU it will keep it until the process releases it. A process will release

the CPU when it has either finished executing, or if it needs to wait for an I/O event to occur before it can resume its execution.

b. Shortest-Job-First

Shortest-Job-First (SJF) [4] examines the amount of time each process is expected to take during its next CPU burst. It uses this information to form a queue, and then gives control of the CPU to the process that is expected to have the shortest next CPU burst. In theory, this algorithm is very efficient, because it minimizes the average waiting time for each process. However, since the next CPU burst time can be very difficult to accurately determine, this algorithm does not always produce the intended results

c. Shortest-Remaining-Time-First

Shortest-Remaining-Time-First (SRTF) [4] is a preemptive SJF algorithm will preempt the currently executing process, whereas a non-preemptive SJF algorithm will allow the currently running process to finish its CPU burst.

d. Priority scheduling

Priority scheduling [4] algorithms assign each process a priority level, which is represented by a number. Some systems consider low numbers to have high priorities and others designate the high numbers to indicate a higher priority. These priorities can be assigned internally (by the operating system) or externally (by the user). Considering some sort of measurable quantity, such as how many resources each process requires, usually sets internal priority levels. External priority levels reflect a user-specified order of importance for each process. The process with the highest priority is initially given control of the CPU. If the algorithm is preemptive, then the currently running process will be preempted once a process in the ready queue has a higher priority. However, if the algorithm is non-preemptive, then once the process begins running all other processes will have to wait until it releases the CPU, regardless of what their priority levels may be. The major concern with priority scheduling is the concept of starvation. This occurs when a process has such a low priority that it is never given control of the CPU. A solution to this is aging, which gradually increases the priority of a process over time.

e. Round-Robin scheduling

The Round-Robin scheduling [4] algorithm (RR) defines a time quantum, which is the amount of time each process in its queue is allocated the CPU. This algorithm is preemptive and fills its queue using the FCFS method. After a process has exceeded its time limit, it is moved to the end of the queue and the next process in the queue is given control of the CPU. Setting a correct time quantum is critical to the performance of the RR algorithm. If the time quantum is too small, then few processes will complete the first time they are run and much time will be spent performing context switches. Alternatively, if the time quantum is too large, then

most processes will finish and the CPU will sit idle for the remainder of the quantum. In both of these cases, the CPU is not being used efficiently. Generally, it is desirable for around 80 percent of the processes to complete before the time quantum is reached.

f. Multilevel queue scheduling

A multilevel queue [4] scheduling algorithm separates the ready queue into a number of different queues. Each of these queues uses their own scheduling algorithm. For instance, there may be separate queues created for CPU-bound and I/O-bound processes. The multilevel queue algorithm is responsible for allocating the CPU to each of these different queues. Any scheduling algorithm can be used to achieve this goal; however, fixed-priority preemptive scheduling is commonly used. If the CPU-bound queue were assigned the higher priority, then the CPU would not be given to the I/O-bound queue until all of the processes in the CPU-bound queue had completed. A multilevel queue is useful when the processes can be easily grouped by importance.

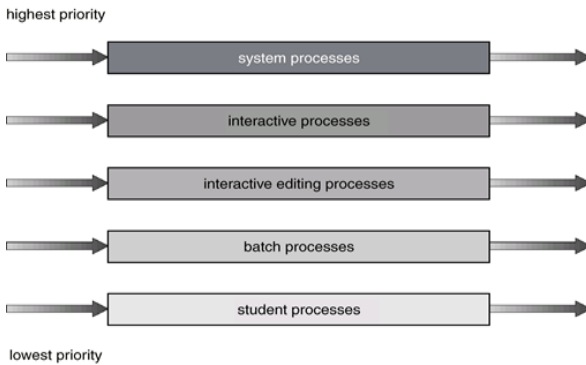


Figure 1: Multilevel queue scheduling

g. Multilevel feedback queue scheduling

In a multilevel queue [4] scheduling processes are permanently assigned to a queue on entry the system. Processes do not move between queues. This setup has the advantages of low scheduling overhead, but the disadvantages of being inflexible. Multilevel feedback queue scheduling, however, allows a process to move between queues. The idea is to separate processes with different CPU-burst characteristics. If a process uses too much CPU time, it will be moved to a lower-priority queue. This scheme leaves I/O bound and interactive processes in the higher-priority queues. Similarly, a process that waits too long in a lower-priority queue may be moved to a higher-priority queue. This form of aging prevents starvation.

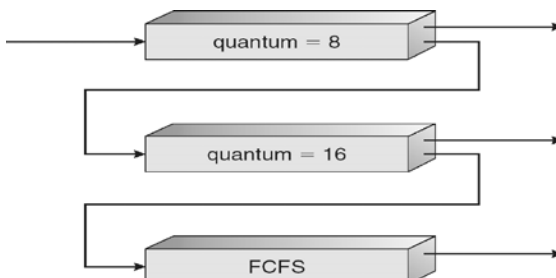


Figure 2: Multilevel feedback queues

A multilevel-feedback-queue scheduler is defined by the following parameters:

- The number of queues.
- The scheduling algorithms for each queue.
- The method used to determine when to promote a process.
- The method used to determine when to demote a process.
- The method used to determine which queue a process will enter when that process needs service.

B. Interactive Processes

There are two types of processes, interactive processes and non-interactive processes. The process type is judged according to the cumulative CPU time. If the cumulative CPU time used exceeds the threshold value, then the process is classified as a non-interactive process, otherwise the process is classified as an interactive process.

Interactive processes are the processes that frequently have long periods of idle time and have relatively little cumulative CPU time that was used between the periods of sleep. CPU time is allocated to the interactive processes with high priority than the non-interactive processes by assigning a high level priority to the interactive type processes [13].

IV. PROBLEM STATEMENT

MLFQ like other scheduling algorithms, problems arise to which processes that are scheduled cause some issues. Starvation is one of the problems that may encounter if some processes are at a low priority queue and takes up too long to run its process that it will never receive CPU time. Some programs change behavior over time that it is difficult for any scheduling algorithm to predict the future behavior of the program.

It is assumed that if low priority processes are boosted and moved to higher level queue for some time then this will prevent the issue of starvation.

V. PROPOSED SOLUTION

The proposed solution is that there will be actions to avoid problems like starvation. Aging is a technique to avoid starvation by just gradually increase the priority of a process that waits too long. There will be two remedial action that may take to consider its priority. One is that if a process takes too long to wait in a low priority queue it may be moved to the higher priority queue or to the topmost queue. Second, it can also be that the CPU scheduler allocates CPU time for each queue so that processes in the lower level queue can be noticed that it is not purely-based on priority that there will be a chance that a process can be run in given amount of time in a queue. Each queue has a corresponding time slice to which the scheduler acts like a round robin scheduling algorithm but can be of different time allocation on each queue.

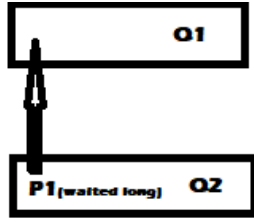


Figure 3: First Approach



Figure 4: Second Approach

VI. CONFIGURATION

The simulation has two configurations with two different **priority policy** namely **higher before lower configuration** and **fixed time slot configuration**. **Priority policy** determines how queue prioritization is implemented in the system. In the **higher before lower** priority policy the CPU scheduler chooses a process from the queue with the highest priority. If there are more than one process that are ready from the highest priority queue, the order of execution is decided according to the associated scheduling algorithm. This means that if there are 2 processes ready in the selected queue and if its associated scheduling algorithm is round robin with a time quantum of 4, the CPU will be multiplexed between these two processes. While in **fixed time slot**, we allocate CPU time across the different queues proportional to its priority. E.g., suppose there are 3 queues Q1, Q2, and Q3 in the system with relative priorities 3, 2, 1, respectively. Then we can allocate CPU time across the different queues as such, 3 CPU times for Q1, followed by 2 CPU times for Q2, and 1 CPU time for Q3. The scheduler then cycles back to Q1 after it has allocated CPU time for Q3.

The two configurations allow an arbitrary number of queues and in each scheduling queue is associated with a scheduling algorithm that determines the process to run among a number of process in ready state.

In the two configuration there is a **migration policy** that determines how a process moves from one queue to another queue. The migration policy can be classified as **promotion**, **demotion**, and **retention**. **Promotion** happens when a process is migrated from lower priority queue to higher priority queue. **Demotion** happens when a process is migrated from higher priority queue to a lower priority queue. **Retention** happens when a process is retained in its current queue.

VII. EXPIREMENT

There are three samples that are shown in Table 1, 2, and 3. In each sample, there are three cases; such as, a MLFQ with 3 queues, with 4 queues, and with 6 queues. These three cases performed the two configuration that was mentioned above, the MLFQ using the priority policy of higher before lower configuration and using the priority policy of fixed time slot

configuration. In the MLFQ with 3 queues, the CPU scheduling algorithm of each queues are 1.) RR with a time quantum of 3, 2.)RR with a time quantum of 4, and in this experiment, the last queue of each of the cases are the different CPU Scheduling. In the case of the MLFQ with 4 queues, the CPU scheduling algorithm of each queues are 1.) RR with time quantum of 3, 2.) RR with a time quantum of 4, and 3.) RR with a time quantum of 5. While in the case of the MLFQ with 6 queues, the CPU scheduling algorithm of each queues are 1.) RR with time quantum of 3, 2.) RR with a time quantum of 4, 3.) RR with a time quantum of 5, 4.) RR with a time quantum of 6, and lastly 5.) RR with a time quantum of 7.

The average response time, average turnaround time, and average waiting time were computed in each experiments, and will be shown after the test samples.

Process ID	Arrival Time	Burst Time	Priority
1	6	29	7
2	0	30	9
3	6	15	10
4	9	40	9
5	10	30	9
6	1	44	4
7	3	39	1
8	2	27	2
9	7	16	9
10	9	36	2
11	2	24	8
12	9	10	2
13	4	9	4
14	8	7	6
15	8	43	5
16	9	13	10
17	10	49	3
18	7	10	7
19	6	36	3
20	7	25	8

Table 1

Test Case 1 of Table 1: 3 queues (RR quantum = 3, RR quantum = 4)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	22.35	324.95	298.35
SJF	22.35	264.95	238.35
SRTF	22.35	264.95	238.85
NP-Priority	22.35	350.95	324.35
Priority	22.35	350.95	324.35

Table 1.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	65.1	312.75	286.15
SJF	65.1	257.05	230.45
SRTF	65.1	257.05	230.45
NP-Priority	65.1	348.95	322.35
Priority	65.1	348.95	322.35

Table 1.2: Fixed time slot configuration

Test Case 2 of Table 1: 4 queues (RR quantum = 3, RR quantum = 4, RR quantum = 5)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	22.35	333.7	307.1
SJF	22.35	301.7	275.1
SRTF	22.35	301.7	275.1
NP-Priority	22.35	364.4	337.8
Priority	22.35	364.4	337.8

Table 2.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	87.9	318.9	292.3
SJF	87.9	291.7	265.1
SRTF	87.9	291.7	265.1
NP-Priority	87.9	348.25	321.65
Priority	87.9	348.25	321.65

Table 2.2: Fixed time slot configuration

Test Case 3 of Table 1: 4 queues (RR quantum = 3, RR quantum = 4, RR quantum = 5, RR quantum = 6, RR quantum = 7)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	22.35	362.3	335.7
SJF	22.35	354.05	327.45
SRTF	22.35	354.05	327.45
NP-Priority	22.35	367.7	341.1
Priority	22.35	367.7	341.1

Table 3.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	131.9	304.4	277.8
SJF	131.9	297.8	271.2
SRTF	131.9	297.8	271.2
NP-Priority	131.9	343.25	316.65
Priority	131.9	343.25	316.65

Table 3.2: Fixed time slot configuration

Process ID	Arrival Time	Burst Time	Priority
1	10	28	8
2	2	1	9
3	3	29	9
4	10	28	9
5	2	4	7
6	10	25	5
7	9	4	6
8	3	41	2
9	9	46	1

10	10	37	6
11	8	19	4
12	5	20	9
13	1	40	7
14	9	49	8
15	7	19	3
16	3	6	9
17	3	10	4
18	0	41	3
19	10	42	7
20	3	8	8

Table 2

Test Case 1 of Table 2: 3 queues (RR quantum = 3, RR quantum = 4)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	20.95	259.75	234.9
SJF	20.95	221.15	196.3
SRTF	20.95	221.15	196.3
NP-Priority	20.95	275.2	260.35
Priority	20.95	275.2	260.35

Table 1.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	65.4	260.35	235.5
SJF	65.4	227.6	202.75
SRTF	65.4	227.6	202.75
NP-Priority	65.4	267.3	242.45
Priority	65.4	267.3	242.45

Table 1.2: Fixed time slot configuration

Test Case 2 of Table 2: 4 queues (RR quantum = 3, RR quantum = 4, RR quantum = 5)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	20.95	274.3	249.45
SJF	20.95	245.75	220.9
SRTF	20.95	245.75	220.9
NP-Priority	20.95	278.9	254.05
Priority	20.95	278.9	254.05

Table 2.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	88.2	265.95	241.1
SJF	88.2	245.35	220.5
SRTF	88.2	245.35	220.5
NP-Priority	88.2	277.5	252.65
Priority	88.2	277.5	252.65

Table 2.2: Fixed time slot configuration

Test Case 3 of Table 2: 4 queues (RR quantum = 3, RR quantum = 4, RR quantum = 5, RR quantum = 6, RR quantum = 7)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	20.95	308.0	283.15
SJF	20.95	295.55	270.7
SRTF	20.95	295.55	270.7
NP-Priority	20.95	312.2	312.2
Priority	20.95	287.35	287.35

Table 3.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	130.1	264.15	239.3
SJF	130.1	250.45	225.6
SRTF	130.1	250.45	225.6
NP-Priority	130.1	263.65	238.8
Priority	130.1	263.65	238.8

Table 3.2: Fixed time slot configuration

Process ID	Arrival Time	Burst Time	Priority
1	3	12	9
2	3	9	7
3	10	9	5
4	2	8	1
5	5	35	8
6	7	44	6
7	10	47	9
8	6	37	9
9	8	30	7
10	2	18	4
11	8	35	1
12	6	44	4
13	0	33	5
14	5	30	5
15	0	31	4
16	7	45	6
17	5	5	6
18	1	23	9
19	6	46	7
20	5	18	8

Table 3

Test Case 1 of Table 3: 3 queues (RR quantum = 3, RR quantum = 4)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	23.55	315.7	287.75
SJF	23.55	274.05	246.1
SRTF	23.55	274.05	246.1
NP-Priority	23.55	338.65	310.7
Priority	23.55	338.65	310.7

Table 1.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	66.3	303.1	275.15
SJF	66.3	261.35	233.4
SRTF	66.3	261.35	233.4
NP-Priority	66.3	303.95	276.0
Priority	66.3	303.95	276.0

Table 2.2: Fixed time slot configuration

Test Case 2 of Table 3: 4 queues (RR quantum = 3, RR quantum = 4, RR quantum = 5)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	23.55	319.7	291.75
SJF	23.55	307.2	279.25
SRTF	23.55	307.2	279.25
NP-Priority	23.55	334.45	306.5
Priority	23.55	334.45	306.5

Table 2.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	89.1	302.4	274.45
SJF	89.1	280.0	252.05
SRTF	89.1	280.0	252.05
NP-Priority	89.1	324.65	296.7
Priority	89.1	324.65	296.7

Table 2.2: Fixed time slot configuration

Test Case 3 of Table 3: 4 queues (RR quantum = 3, RR quantum = 4, RR quantum = 5, RR quantum = 6, RR quantum = 7)

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	23.55	363.65	335.7
SJF	23.55	356.8	328.85
SRTF	23.55	356.8	328.85
NP-Priority	23.55	366.4	338.45
Priority	23.55	366.4	338.45

Table 3.1: Higher before lower configuration

	Average Response Time	Average Turnaround Time	Average Waiting Time
FCFS	125.8	289.2	261.25
SJF	125.8	280.6	252.65
SRTF	125.8	280.6	252.65
NP-Priority	125.8	298.45	270.5
Priority	125.8	298.45	270.5

Table 3.2: Fixed time slot configuration

VIII. COMPUTAIONAL RESULTS

In a CPU Scheduling Algorithm, the criteria for having best scheduling algorithm is to minimize the turnaround time, response time and waiting time.

The average response time of all three samples shows that in the MLFQ using the priority policy of fixed time slot has higher average response time than the MLFQ using the priority policy of higher before lower configuration. As the number of queues increase in the fixed time slot configuration, the average response time also increase. While in the higher before lower configuration, the results are constant in all cases.

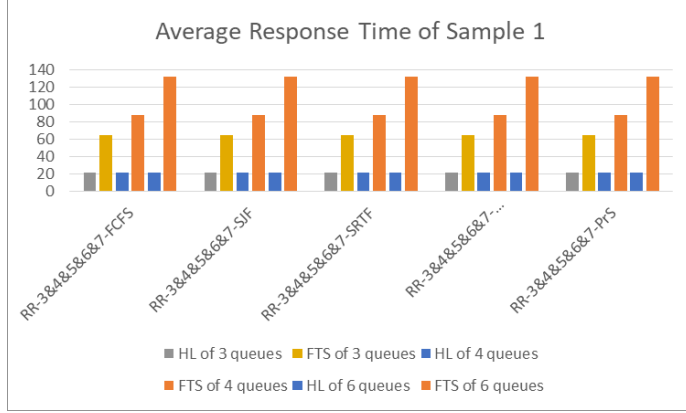


Figure 5

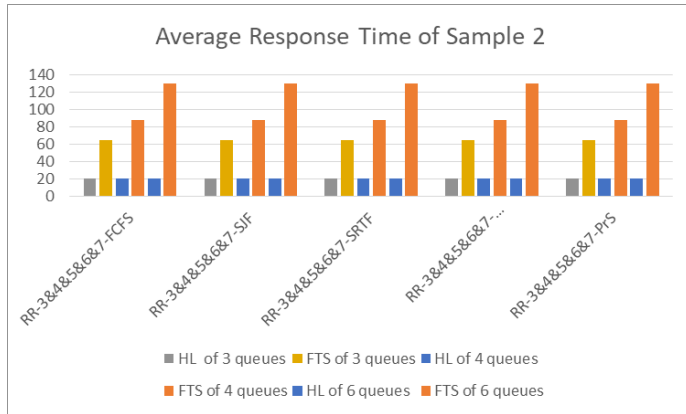


Figure 6

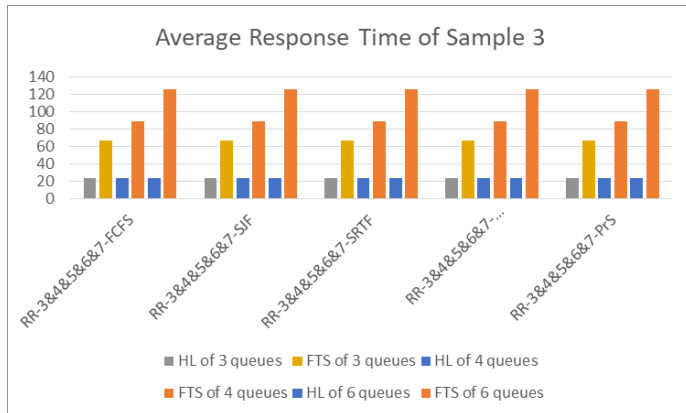


Figure 7

In the case of average turnaround time, in all N level queue of MLFQ, the results shows in all three test samples that the MLFQ using the priority policy of higher before lower

configuration has the higher turnaround time average than using the priority policy of fixed time slot configuration. But the differences of average turnaround time between the two priority policies in some of the cases is not that great. There are cases such as in sample 1 where the MLFQ is a three level queue (first queue has a scheduling algorithm RR with a quantum of 3, the second queue is RR with a quantum of 4, and the last queue is non-preemptive priority and priority scheduling algorithm) have equal average turnaround time for both priority policies.

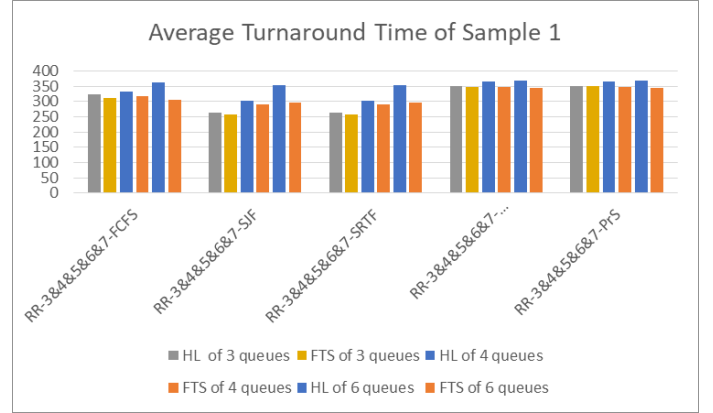


Figure 8

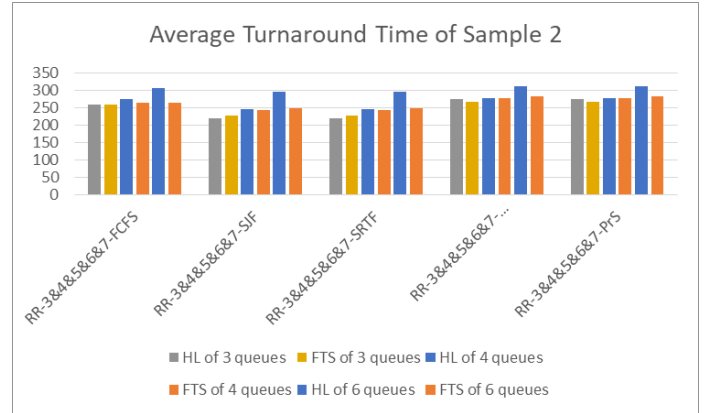


Figure 9

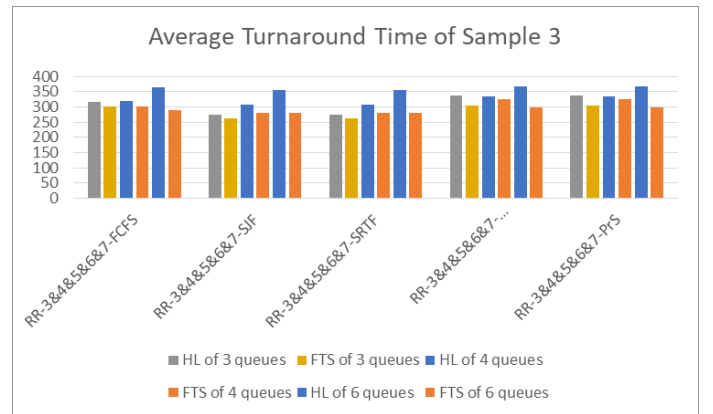


Figure 10

Just like in the case of the average of turnaround time, the average waiting time in all N level queue of MLFQ, the results shows in all three test samples that the MLFQ using the priority policy of higher before lower has the higher turnaround time average that using the priority policy of fixed time slot configuration. But the differences of average turnaround time between the two priority policies in some of the cases is not that

great. Just like in the average turnaround time, there are cases who have equal average waiting time in both priority policies.

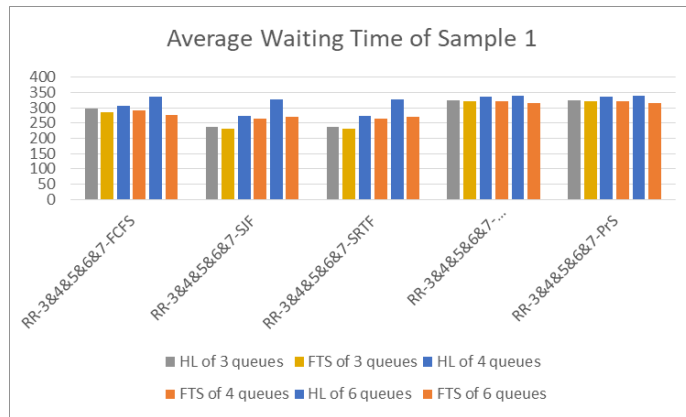


Figure 11

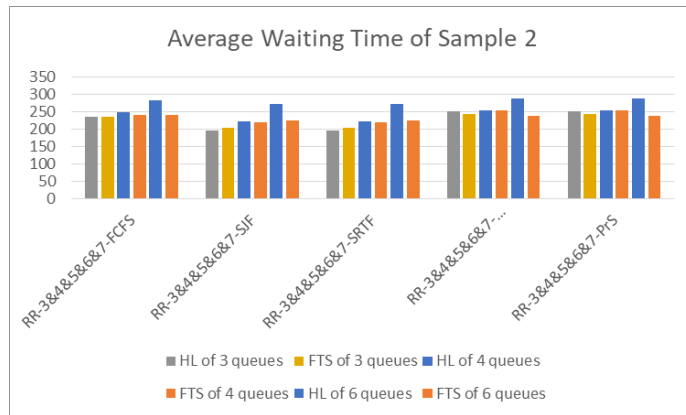


Figure 12

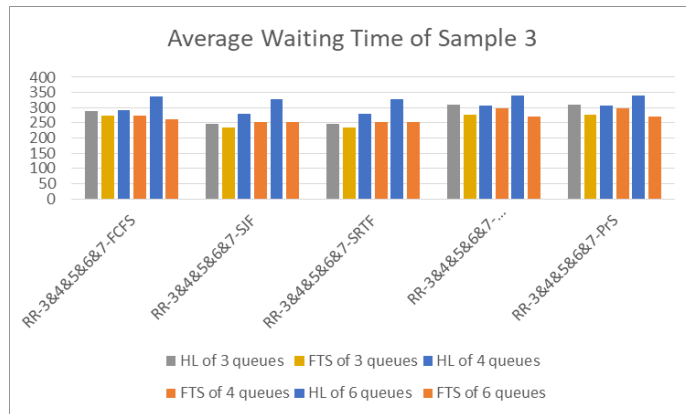


Figure 13

IX. CONCLUSION

The hypothesis is true depending on the configuration that is better for that test data.

The current configuration of MLFQ with promoting and demoting processes and the option of two priority policies shows a great role in contemporary scheduling algorithm to be better in terms of scheduling processes in the CPU. As the samples shown in the computational results shows some of the good characteristics by the given priority policy where the Higher before lower configuration has the better results of average response time and the fixed time slots configuration having the better results of average waiting time or average turnaround time.

It shows that it has decreased the problem of starvation that low priority processes can get faster response to the scheduler and smaller waiting and turnaround time.

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