Lab Questions:

1. The board's peripherals are all part of the Zynq PL (programmable logic), which is controlled by the ARM Dual Cortex-A9 MPCore System (CPU).
2. The two lines of code that load the FPGA bitstream onto the PL are:
   ```
   from pynq.overlays.base import BaseOverlay
   import pynq.lib.rgbled as rgbled
   ```
3. The main difference between these two methods is that to check for inputs, you will have to constantly hard-code a check for a button input every few lines, whereas the asyncio method lets you write out the general logic of the code, with a small section afterwards the code jumps to whenever an interrupt (button input in this case) happens. The asyncio method is much easier to read and program as a result.
4. The main difference between these cells is that the cells starting with "`%%microblaze base.PMODB`" are written in C, whereas the other cells are written in Python.
5. We need to reload the base overlay because the second part of the notebook deals with PmodA instead of PmodB.