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ASURT Task 02  
Power electronics  
Session

## Switches Types

Diode

Mosfet

BJT

IGBT

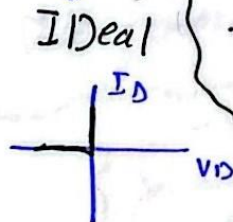
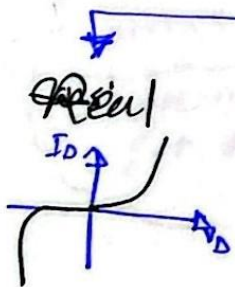
others

• Diode

Symbol



Characteristics

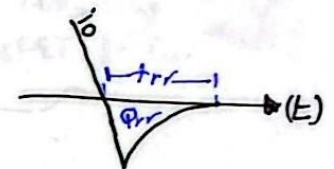


Construction



2 materials called are put together to form a PN Junction which separates between them a depletion Region

Diode Reverse Recovery



The current  $t_{rr}$  Required to sweep out the excess carriers and allow the diode to block the (-ve) polarity from occurs

	General Purpose	Fast recovery	shot key
switching (F)	Low	Highest	High
Ratings	Highest	Medium	Low
V drop	Low	medium	Lowest
APP	Low (F) APPS	High (F) apps	Low (V) out

# Types of Transistors

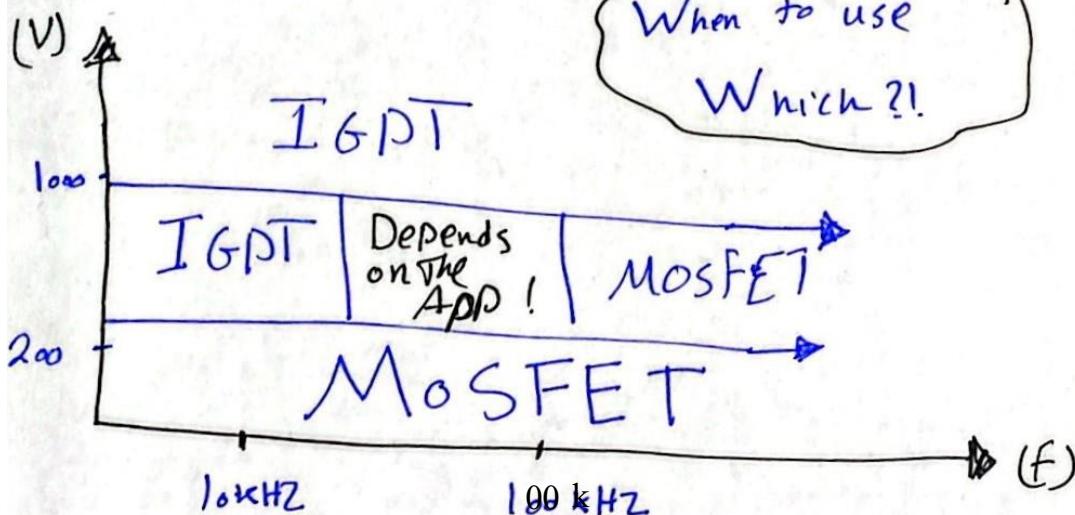
	* MOSFET *	* IGBT	BJT
Control Method	Voltage Controlled	Voltage Controlled	Current Controlled
Power Capability	Low	Medium	Medium
Switching Speed	High	Medium	Medium
Temp Coefficient	Positive	Depends on design	Negative
Inherent body Diode	Exists	Absent	Absent

## Temperature coefficient

It means how the transistor resistance get effected by the temperature

The most preferred transistors types in today's industry are MOSFET & IGBT

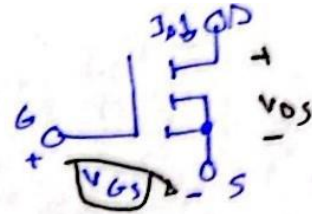
When to use  
Which?!





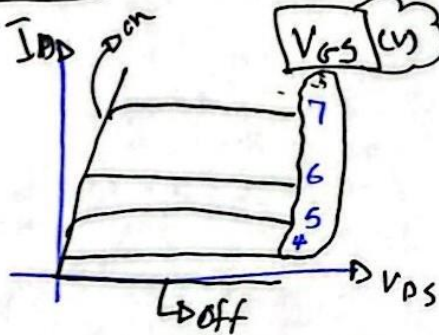
# What is MOSFET

Symbol

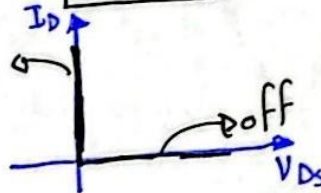


## Characteristics

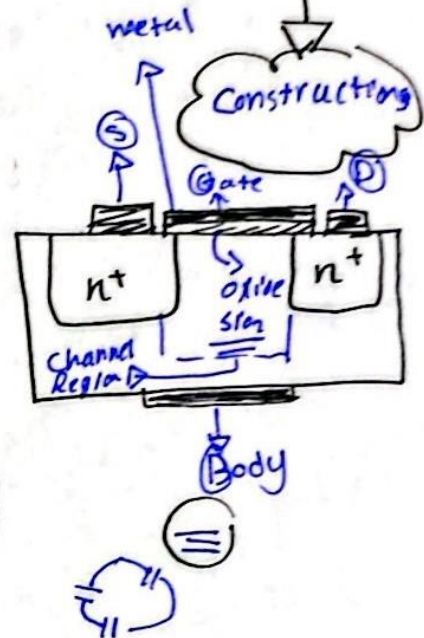
Real



Ideal



## Constructing



## Conduction Losses

\* During ON Time MOSFET acts like a resistor

$$P = I_{D(rms)}^2 R_{DS(on)}$$

## MOSFET Losses

### Diode Losses

\* The conduction losses of the anti-parallel Diode can be estimated using Diode DC approx

$$P = U_{D0} \cdot I_{F(av)} \approx I_{F(av)}^2 R_{D(on)}$$

where  $I_{F(av)}$  is the average forward current.

### Switching Losses

$$W_c = \frac{1}{2} V_D I_D (t_{r(on)} + t_{c(off)})$$

$$P_s = \frac{W_c}{T_s} = W_c f_s = \frac{1}{2} V_D I_D (t_{r(on)} + t_{c(off)}) f_s$$

To reduce the switching losses reduce  $f$  or  $T_{on}$  &  $t_{off}$

