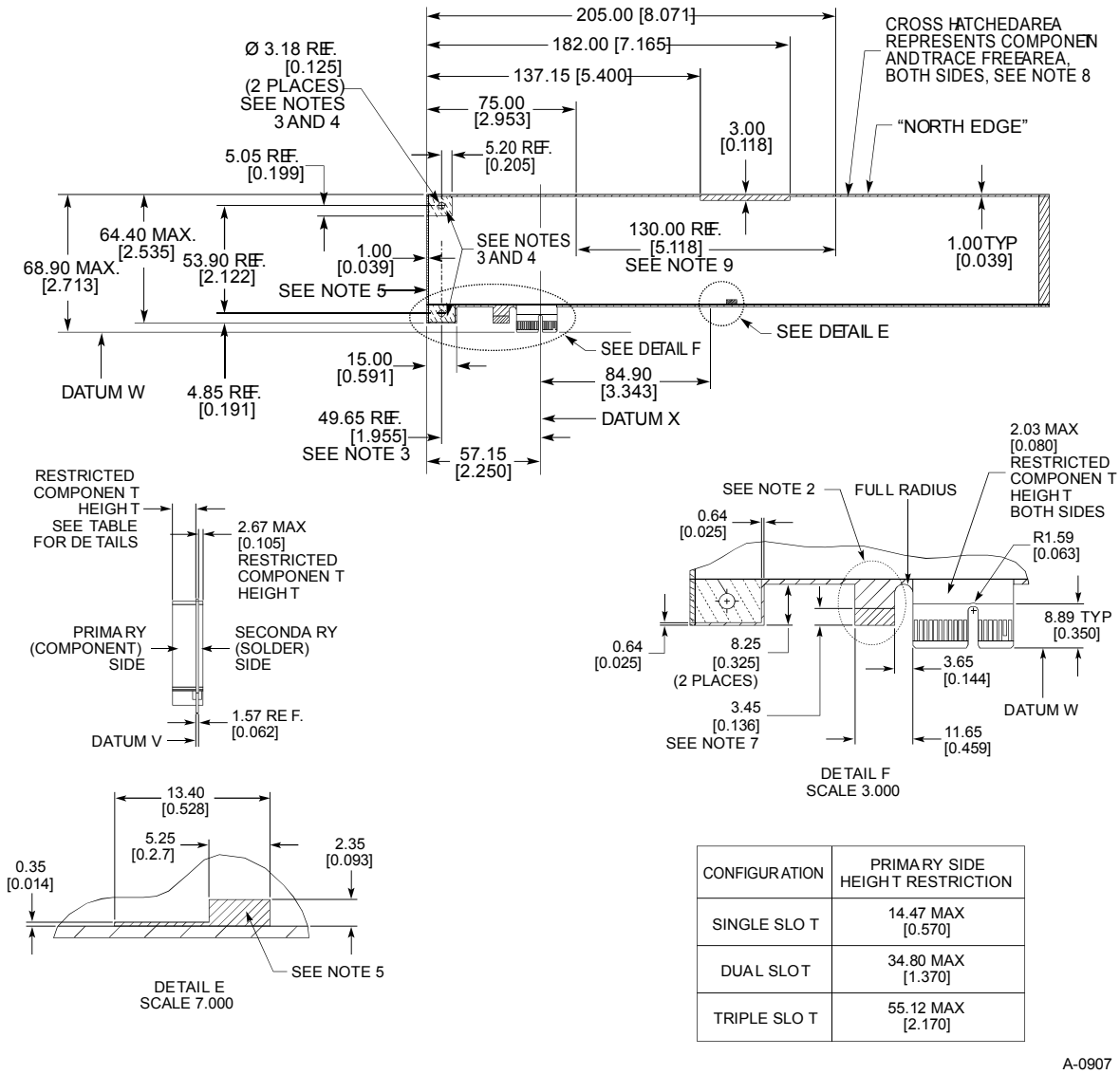


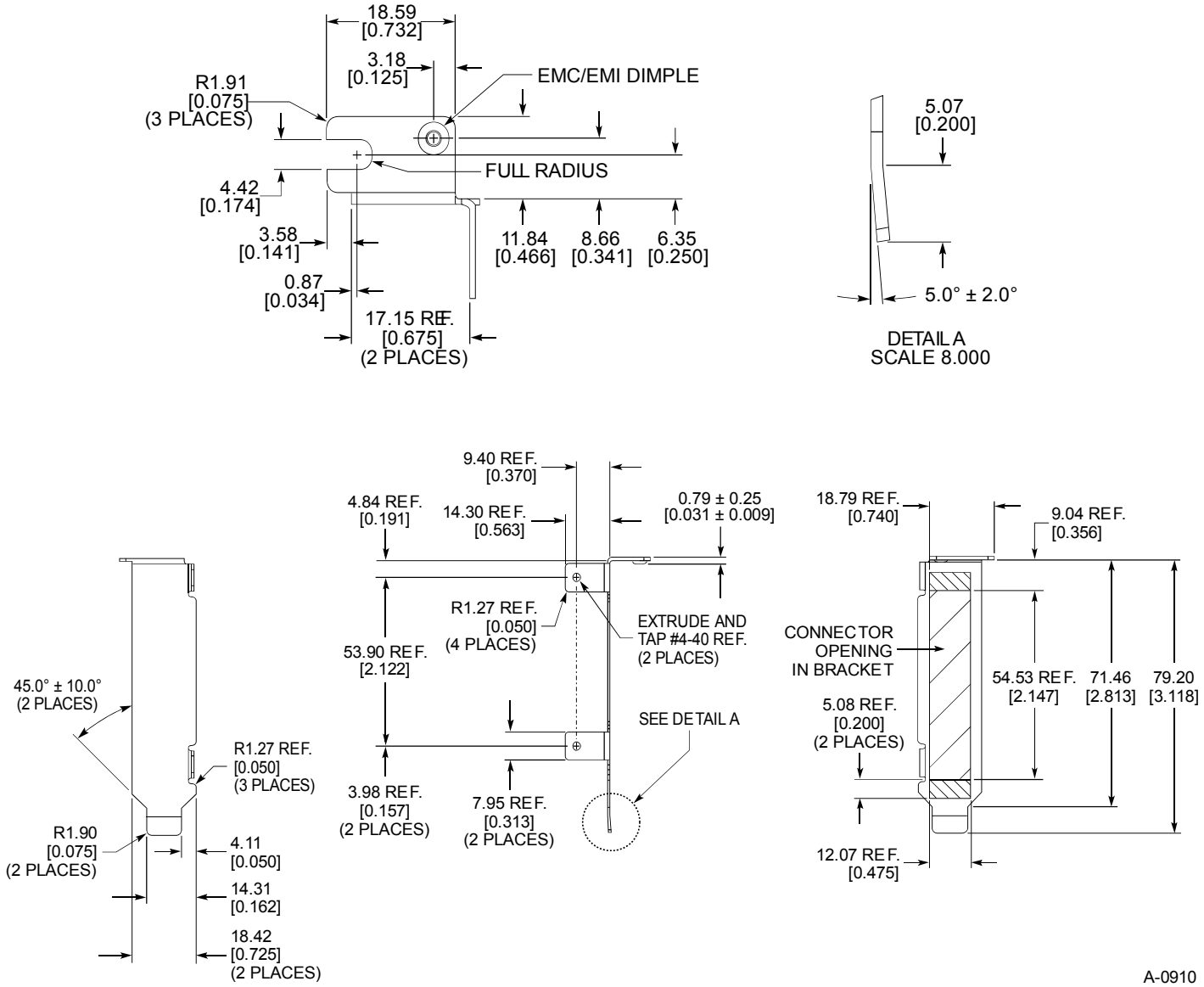
CONFIGURATION	PRIMARY SIDE HEIGHT RESTRICTION
SINGLE SLOT	14.47 MAX [0.570]
DUAL SLOT	34.80 MAX [1.370]
TRIPLE SLOT	55.12 MAX [2.170]

A-0907

NOTES:

1. TOLERANCE UNLESS OTHERWISE SPECIFIED: ± 0.13 [0.005]
2. THIS FEATURE IS TO PREVENT THE PCI EXPRESS CARD FROM BEING PLUGGED INTO PCI CONNECTORS.
3. THESE MOUNTING HOLES AND THE KEEP-OUT AROUND THEM ARE REQUIRED ONLY WHEN THE I/O BRACKET IS MOUNTED TO THE CARD DIRECTLY. FURTHERMORE, THESE ARE CHASSIS KEEPOUT AREAS, ARE INCLUDED IN THE ADD-IN CARD VOLUME, AND MUST ABIDE BY THE RESTRICTED COMPONENT HEIGHT, BOTH SIDES.
4. THESE MOUNTING HOLES MAY BE LOCATED DIFFERENTLY THAN AS SHOWN. ADDITIONALLY, THE SIZE OF THE KEEP-OUTS AROUND THESE HOLES ARE AT THE DISCRETION OF THE ADD-IN CARD PROVIDER.
5. CROSS HATCHED AREA REPRESENTS AN EXPOSED SIGNAL/POWER COMPONENT, FEATURE, AND TRACE FREE AREA, SECONDARY SIDE (SOLDER SIDE) ONLY. HOWEVER, NON-CONDUCTIVE FEATURES (PLASTIC LOCATION PINS, ETC.), GROUNDED FEATURES (BOARDLOCKS, GROUND PINS AND VIAS, ETC.) AND NON-EXPOSED TRACES ARE ALLOWED TO RESIDE IN THIS AREA.
6. CROSS HATCHED AREA REPRESENTS COMPONENT FREE AREA, BOTH SIDES.
7. NO COPPER OR SOLDER MASK IS ALLOWED IN CROSS HATCHED AREA, BOTH SIDES.
8. UNLESS OTHERWISE SPECIFIED AND ALONG "NORTH EDGE" ONLY, EXTERNALLY-MATED CONNECTORS, INCLUDING BUT NOT LIMITED TO POWER CONNECTORS, CARD-TO-CARD EDGE OR BRIDGE CONNECTORS, S/PDIF CONNECTORS, ETC. MAY ENCROACH INTO THIS AREA. INTRUSION BY SELF-CONTAINED COMPONENTS AND EXPOSED TRACES ARE PROHIBITED.
9. NO COMPONENTS OR SURFACE TRACES ALLOWED IN THIS AREA. IN ADDITION, ITEMS INCLUDING BUT NOT LIMITED TO HEATSINKS, DAUGHTER CARDS, OR MATING CONNECTORS (IN WHOLE OR IN PART) ARE NOT ALLOWED TO EXTEND INTO NOR ABOVE THE CARD EDGE IN THIS AREA. THIS KEEPOUT APPLIES TO BOTH SIDES OF THE CARD. THIS KEEPOUT ESTABLISHES A CONSISTENT REGION FOR SUPPLEMENTAL MECHANICAL SUPPORT OF THE ADD-IN CARD BY THE SYSTEM.

Figure 9-6: Low Profile PCI Express Add-in Card without the I/O Bracket



NOTES:

1. MATERIAL: 0.86 ± 0.08 [0.034 ± 0.003] THICK (20 GA)
2. TOLERANCE UNLESS OTHERWISE SPECIFIED: ± 0.25 [± 0.010]

Figure 9-9: Low Profile I/O Bracket