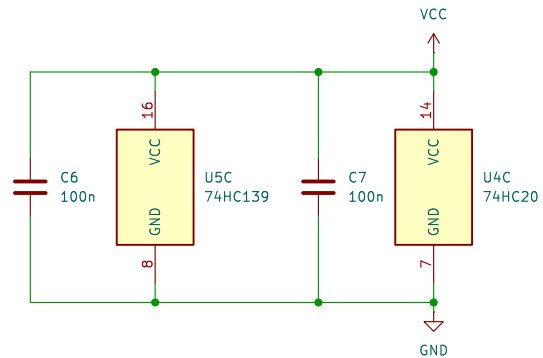


Memory Map
 0000 NVRAM
 F000 CPU registers
 F040-F0FF internal RAM
 F100-F3FF NVRAM
 F400 external I/O
 F800 paging latch
 FC00-FFFF NVRAM



Sheet: /Decode-Latch/
 File: Decode-Latch.kicad_sch

Title: Mini11/8E Board V1.1

Size: A4

Date:

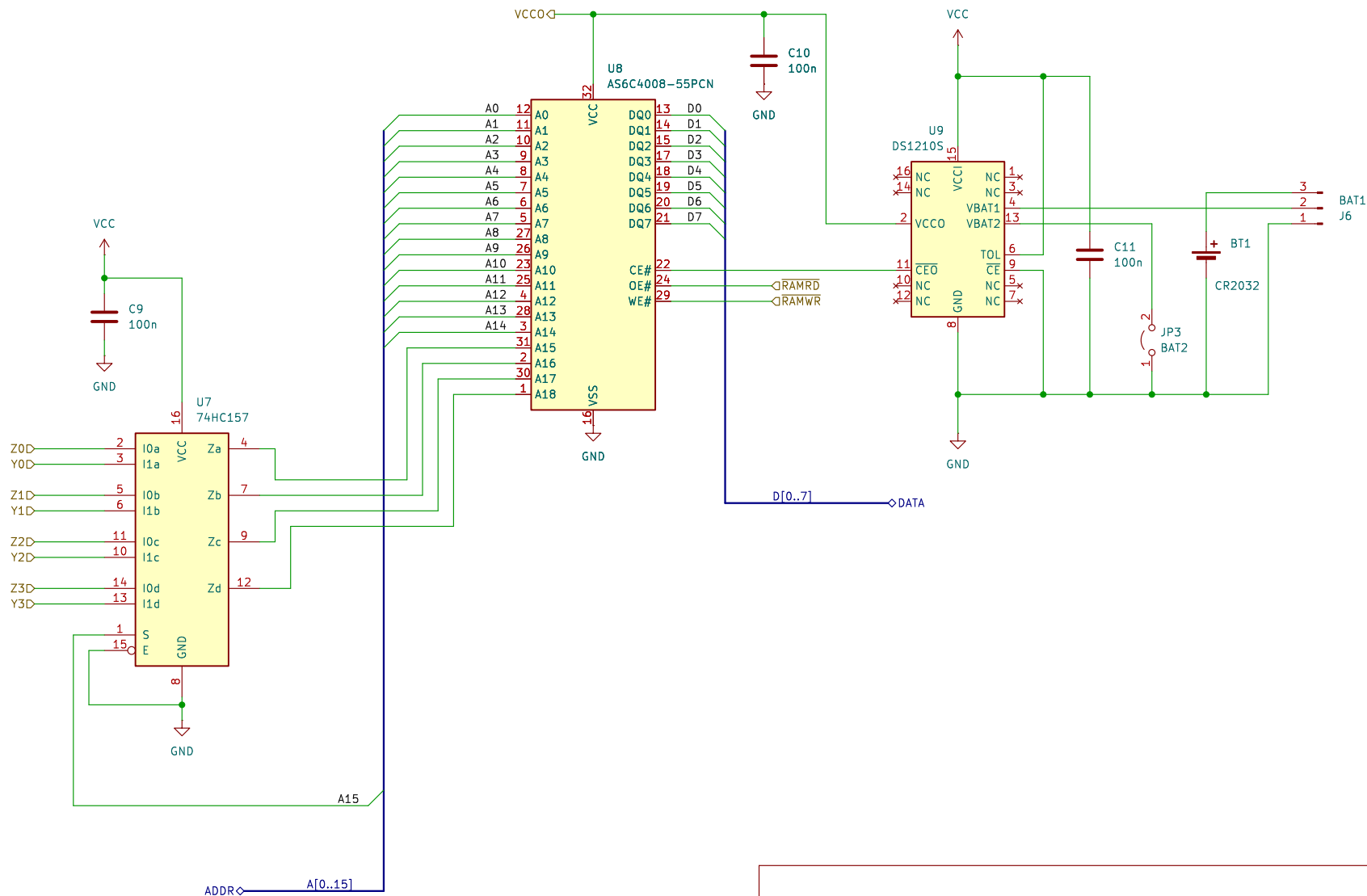
KiCad E.D.A. 8.0.5

Rev:

Id: 3/5



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Sheet: /Memory/

File: Memory.kicad_sch

Title: Mini11/8E Board V1.1

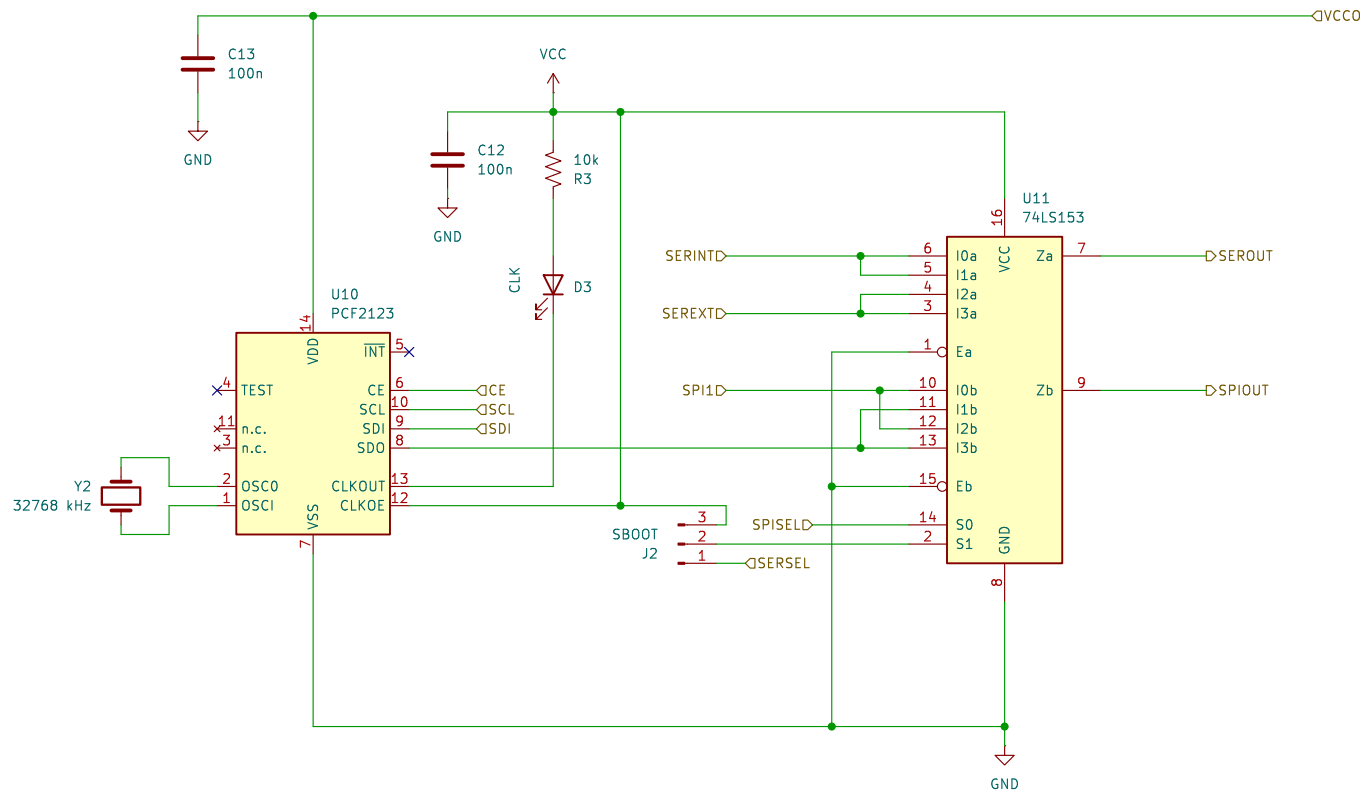
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Date:

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Id: 4/5



Sheet: /Mux-RTC/
File: Mux-RTC_sch.kicad_sch

Title: Mini11/8E Board V1.1

Size: A4

Date:

KiCad E.D.A. 8.0.5

Rev:

Id: 5/5