

kw

Sheet: /

File: stplus.kicad\_sch

**Title: Super Searle Tranter Board V2.0**

Size: A4

Date: 2023-10-05

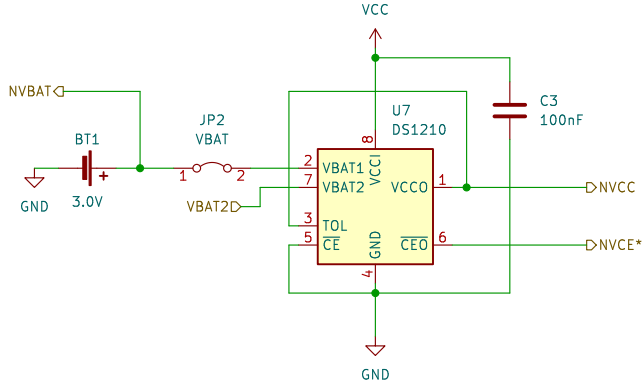
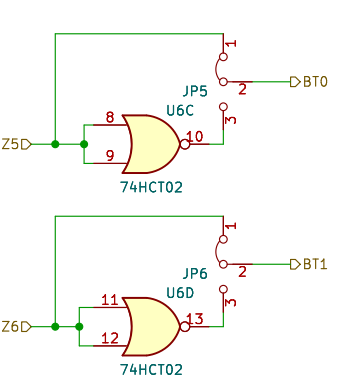
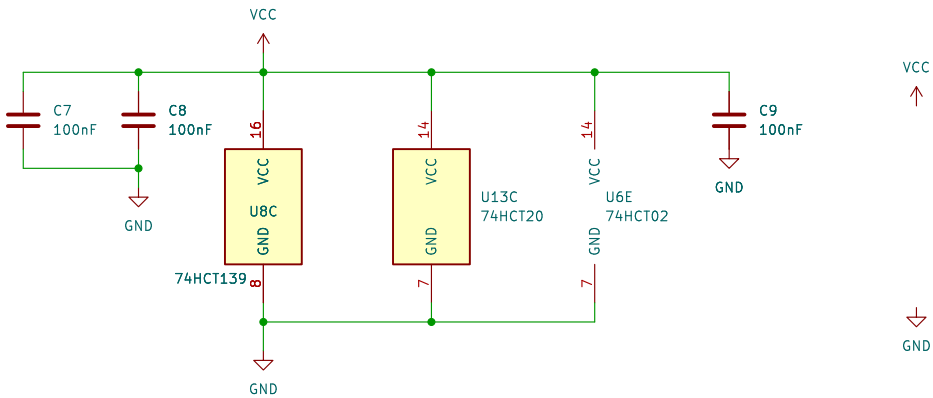
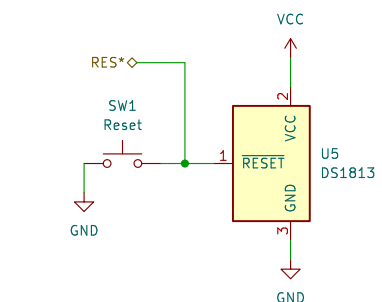
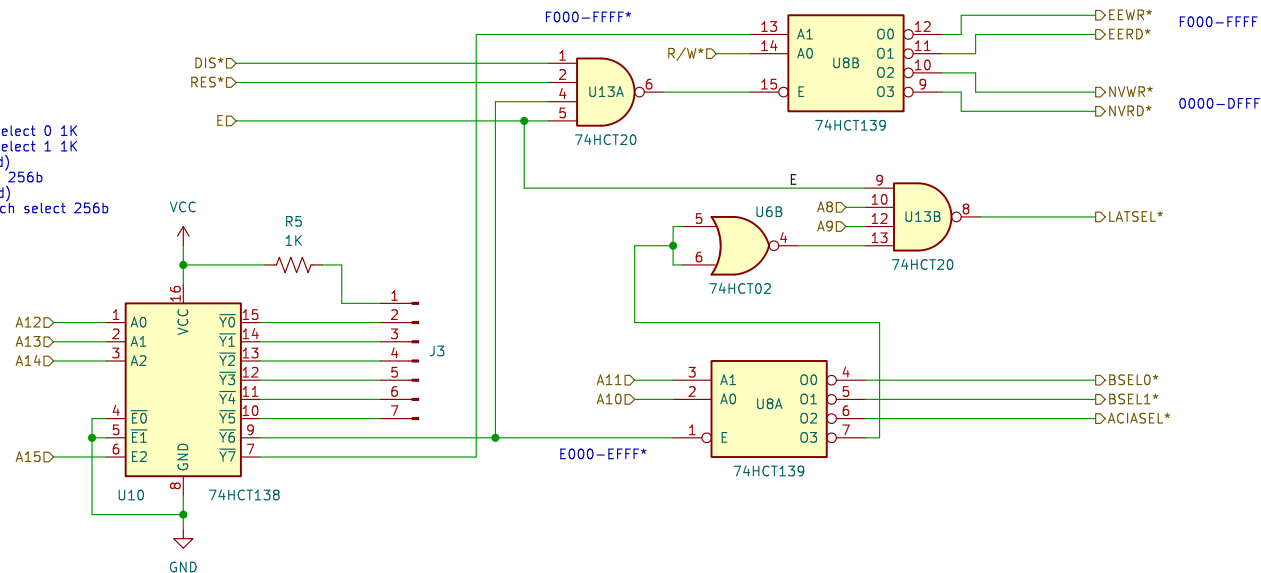
KiCad E.D.A. kicad 7.0.7

Rev: 1

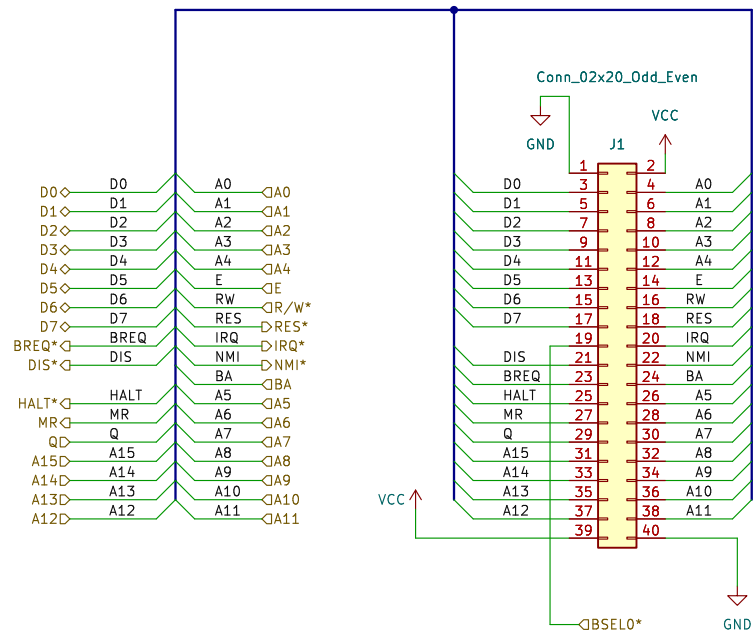
Id: 1/3

# 2.0 Memory Map:

0000-DFFF NVRAM 56k  
E000-E3FF main bus select 0 1K  
E400-E7FF main bus select 1 1K  
E800-EAFF (unassigned)  
EB00-EBFF ACIA select 256b  
EC00-EEFF (unassigned)  
EF00-EFFF memory latch select 256b  
F000-FFFF EEPROM 4K



kw		
Sheet: /decode/		
File: decode.kicad_sch		
<b>Title: Super Searle Tranter Board V2.0</b>		
Size: A4	Date: 2023-10-05	Rev: 1
KiCad E.D.A. kicad 7.0.7		Id: 2/3



kw		
Sheet: /bus/		
File: bus_sch.kicad_sch		
Title: Super Searle Tranter Board V2.0		
Size: A4	Date: 2023-10-05	Rev: 1
KiCad E.D.A. kicad 7.0.7		Id: 3/3