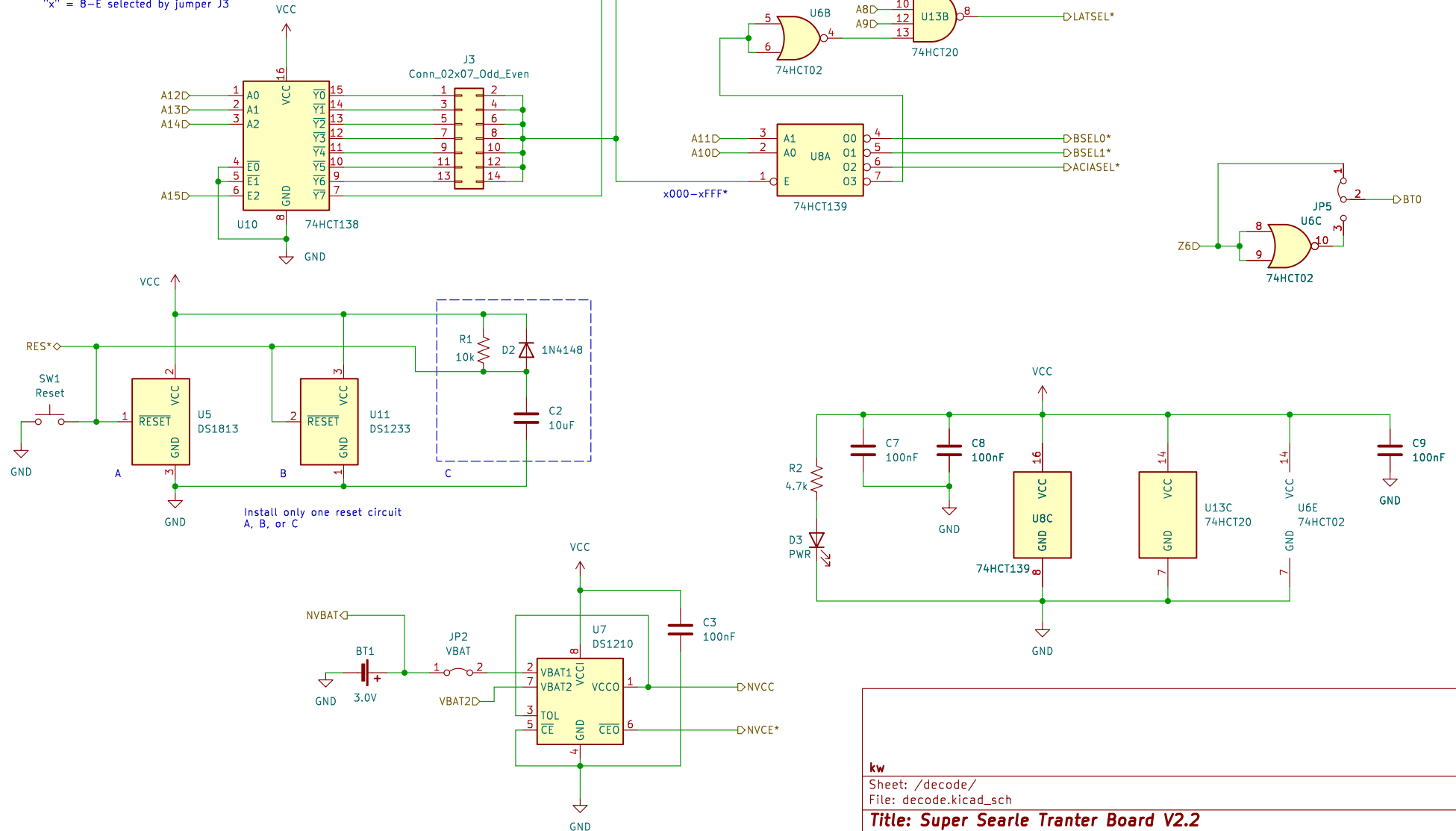


kw	
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Title: Super Searle Tranter Board V2.2	
Size: A4	Date: 2023-10-05
KiCad E.D.A. 8.0.0	Rev: 1
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2.0 Memory Map:

0000-DFFF NVRAM 56k
 x000-x3FF main bus select 0 1K
 x400-x7FF main bus select 1 1K
 x800-xAFF (unassigned)
 xB00-xBFF ACIA select 256b
 xC00-xEFF (unassigned)
 xF00-xFFF memory latch select 256b
 F000-FFFF EEPROM 4K

"x" = 8-E selected by jumper J3



kw

Sheet: /decode/
 File: decode.kicad_sch

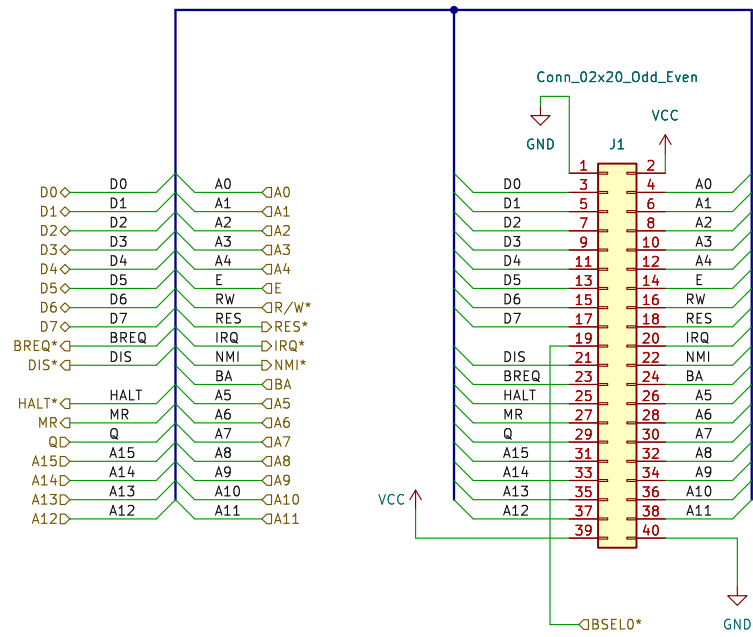
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Size: A4 Date: 2023-10-05

KiCad E.D.A. 8.0.0

Rev: 1

Id: 2/3



kw		
Sheet: /bus/		
File: bus_sch.kicad_sch		
Title: Super Searle Tranter Board V2.2		
Size: A4	Date: 2023-10-05	Rev: 1
KiCad E.D.A. 8.0.0	Id: 3/3	