

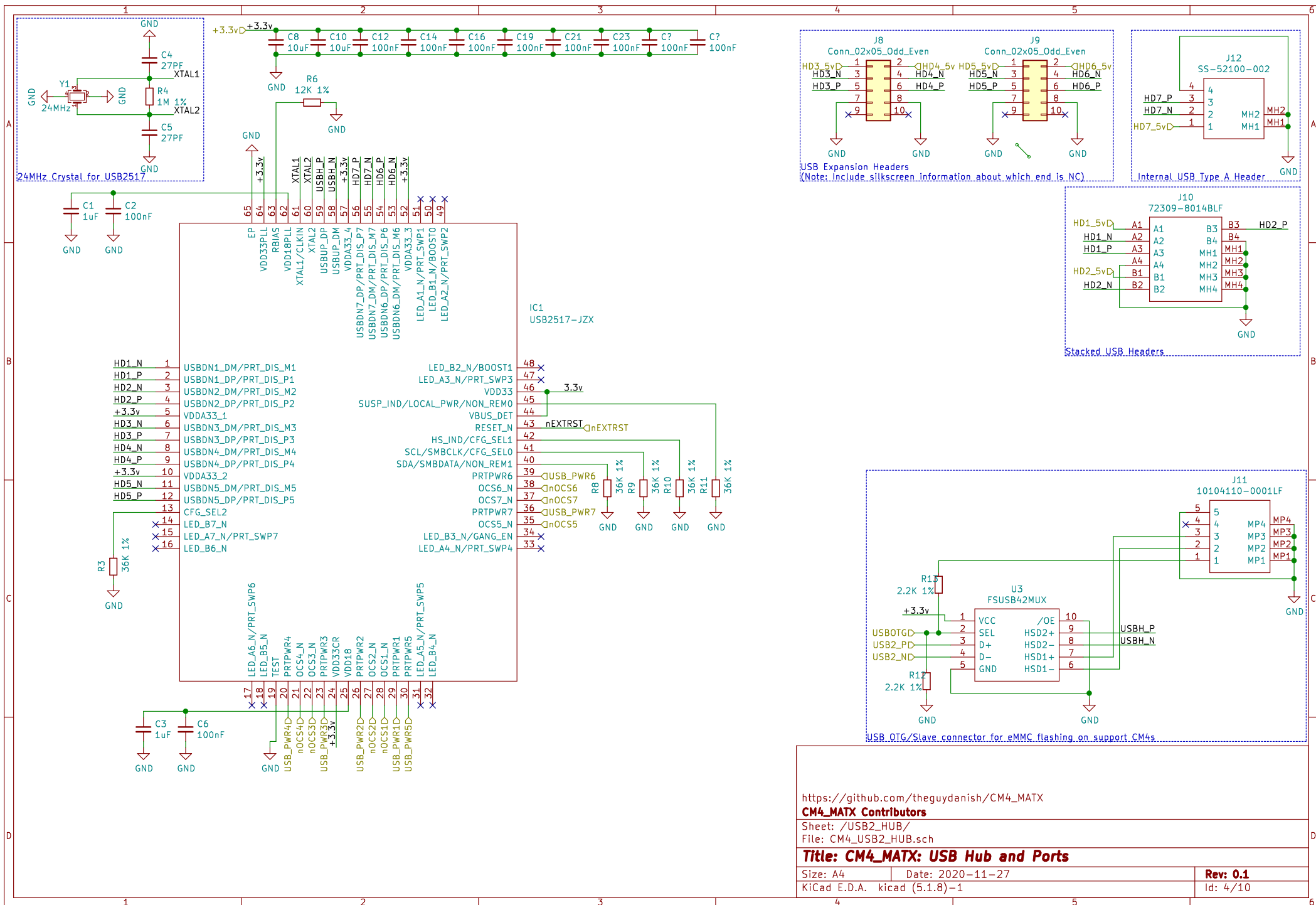
[https://github.com/theguydanish/CM4\\_MATX](https://github.com/theguydanish/CM4_MATX)

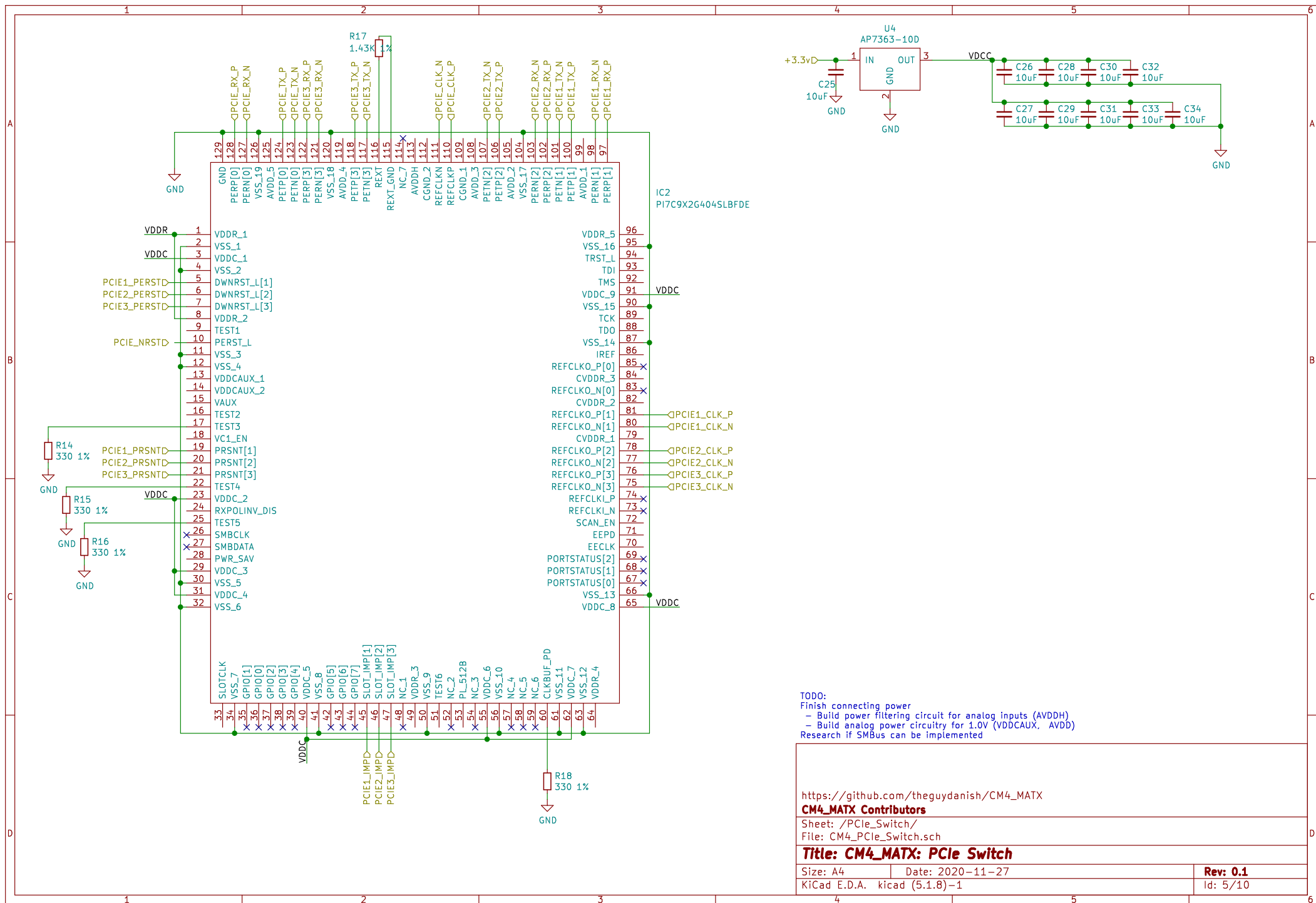
**CM4\_MATX Contributors**

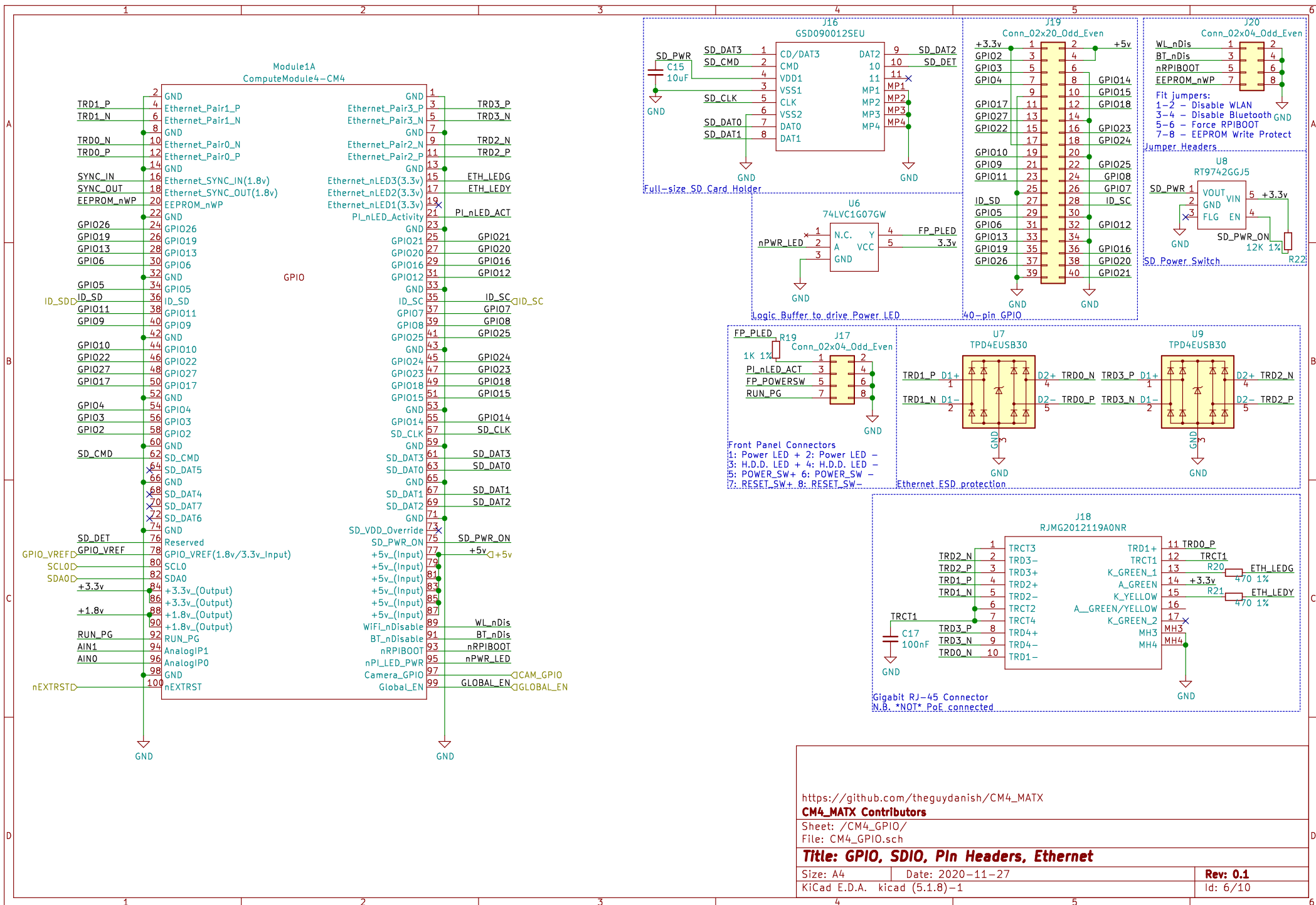
Sheet: /ATX\_PSU/  
File: CM4\_ATX\_PSU.sch

**Title: CM4\_MATX: ATX PSU Connection**

Size: A4	Date: 2020-11-27	Rev: 0.1
KiCad E.D.A. kicad (5.1.8)-1		Id: 3/10







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#### CM4\_MATX Contributors

Sheet: /CM4\_GPIO/

File: CM4\_GPIO.sch

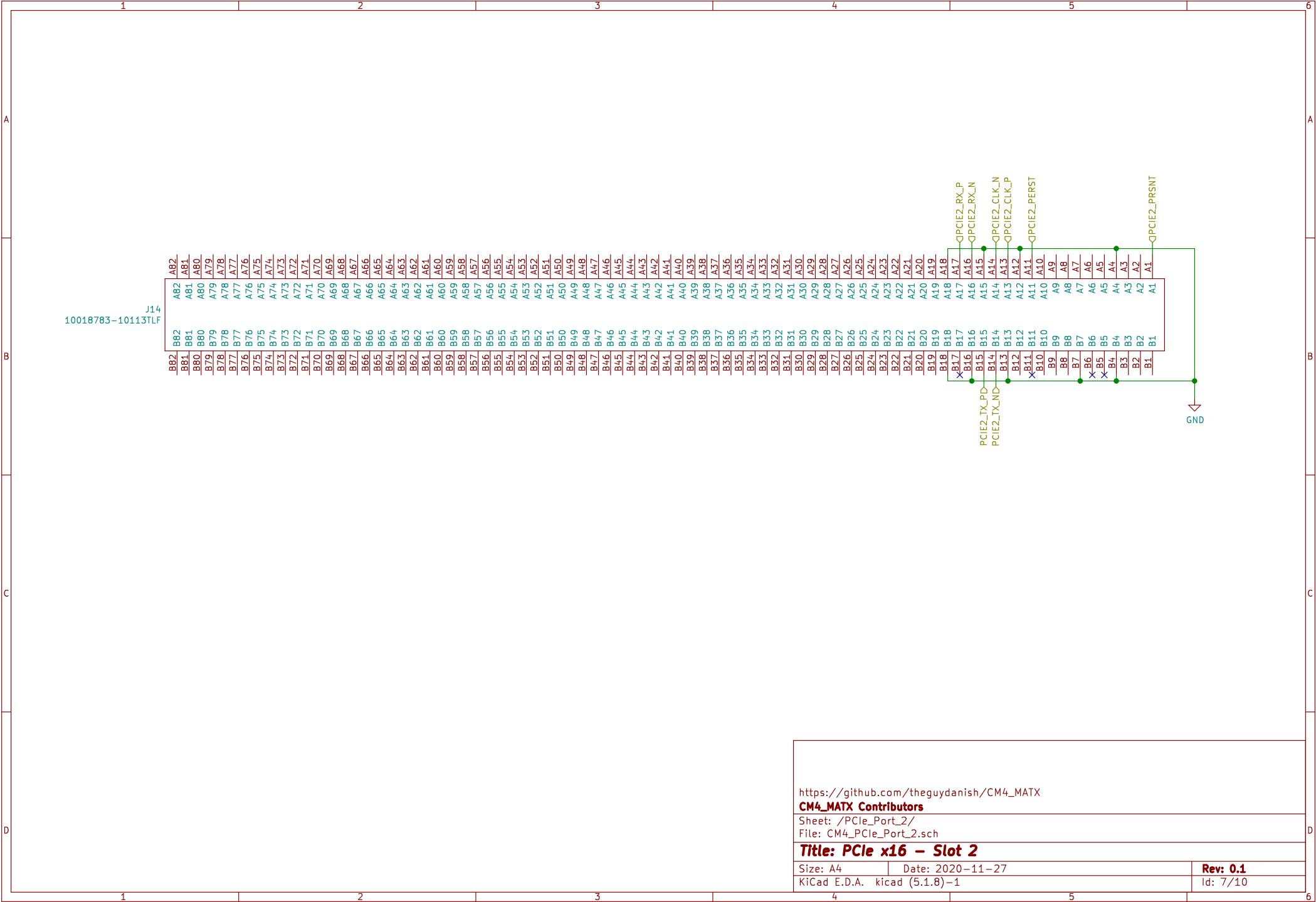
#### Title: GPIO, SDIO, PIn Headers, Ethernet

Size: A4 Date: 2020-11-27

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Rev: 0.1

Id: 6/10



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CM4\_MATX Contributors

Sheet: /PCIe\_Port\_2/

File: CM4\_PCIe\_Port\_2.sch

**Title: PCIe x16 – Slot 2**

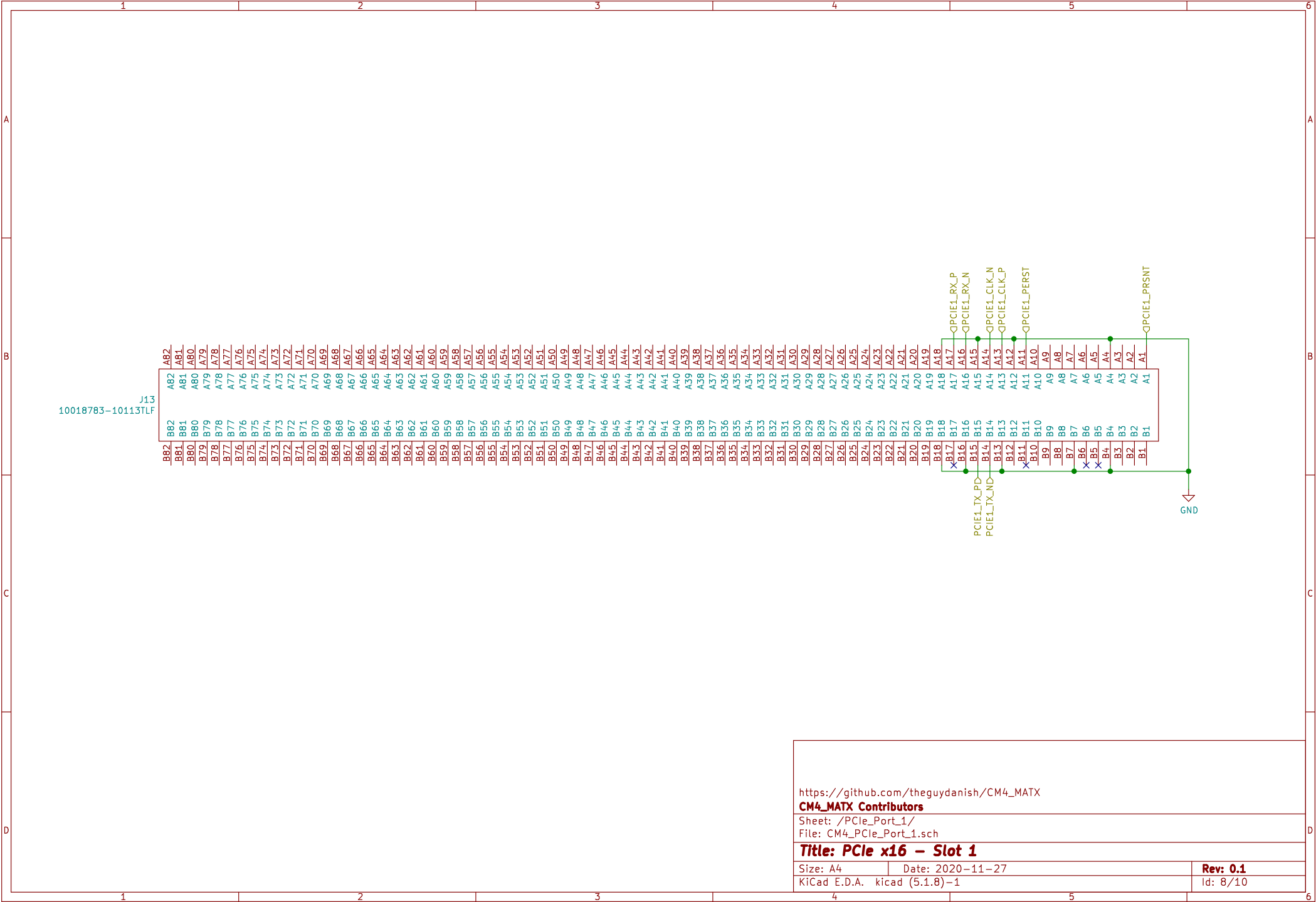
Size: A4

Date: 2020-11-27

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 7/10



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**CM4\_MATX Contributors**

Sheet: /PCle\_Port\_1/

File: CM4\_PCl\_e\_Port\_1.sch

**Title: PCle x16 – Slot 1**

Size: A4

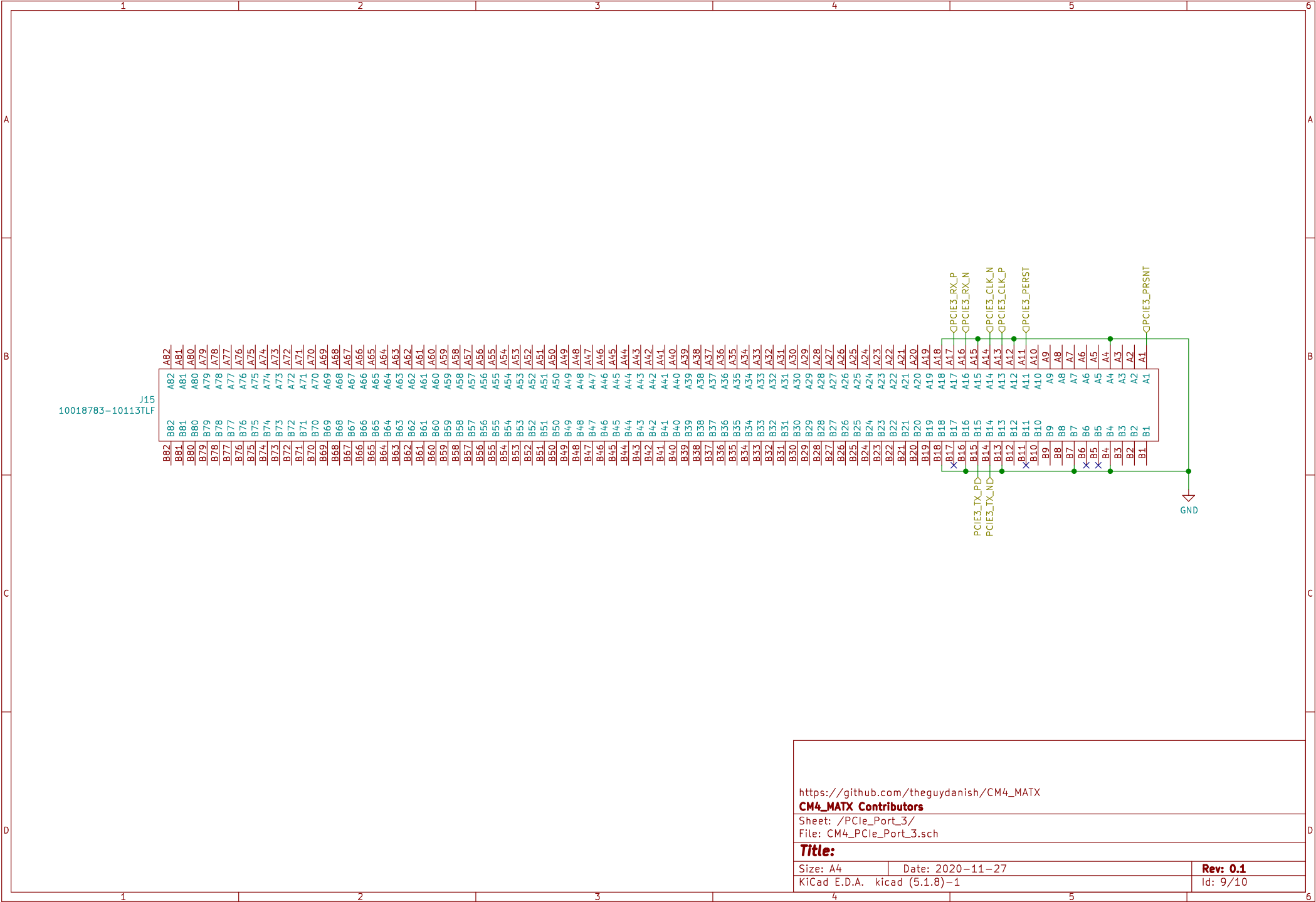
Date: 2020-11-27

Rev: 0.1

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Id: 8/10





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**CM4\_MATX Contributors**

Sheet: /PCle\_Port\_3/

File: CM4\_PCLE\_Port\_3.sch

**Title:**

Size: A4

Date: 2020-11-27

KiCad E.D.A. kicad (5.1.8)-1

**Rev: 0.1**

Id: 9/10

