

KENNETH LEE III

CURRENT ADDRESS

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PROFESSIONAL SUMMARY

Computer Engineer aspiring to contribute to a professional engineering team requiring strong technical aptitude, communications skills, and an exemplary work ethic. Key skills include:

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|--------------|-----------|--------------------|--------------------|--------------|
| • SolidWorks | • Cadence | • FDM Printing | • Arduino | • C++ |
| • Onshape | • EasyEDA | • Laser Cutting | • Raspberry Pi | • Java |
| • Quartus | • LTspice | • Google Suite | • Ultimaker Cura | • JavaScript |
| • Wireshark | • MATLAB | • Microsoft Office | • PLC Ladder Logic | • VHDL |

EDUCATION

BS Computer Engineering | Cedarville University; GPA: 2.9

Cedarville, OH | 8/20 - 5/25

Minor in Bible

EMPLOYMENT EXPERIENCE

Master Power Transmission | Intern

Columbus, IN | 5/22 - 8/22

- Helped to design a setup for automatically spray-painting large gearboxes using a seven-axis robot arm.
- Programmed the articulated robot to paint hundreds of models of gearboxes.
- Learned project management skills including presenting project plans and design updates to management.

Hitachi Astemo | Intern

Findlay, OH | 6/20 - 1/22 & 5/23 - 8/23

- Installed over 40,000 feet of CAT6 cable with another intern, including RJ45 termination and server connections.
- Setup and used FDM printers while writing user guides for full-time engineers to reference for FDM printing.
- Assembled, programmed, installed, and tested a job-specific pneumatic press.

Schoolhouse Electronics | Summer Associate

Bluffton, OH | 5/24 - 8/24

- Learned a variety of construction techniques for the purpose of hanging smart TVs and projectors in school classrooms.

Walmart | Part-Time Associate

Findlay, OH | 9/21 - 5/25

Steak 'n Shake | Service Trainer, Operations Supervisor

Findlay, OH | 6/18 - 1/21

ACADEMIC EXPERIENCE

ASEE Robotics Competition Team | Worked as part of a small team to develop a robot design strategy. Electronic circuit design and troubleshooting, including RF component integration; code development and testing.

Advanced Digital Logic Design | In-depth learning about FPGA and ASIC while building and programming an FPGA robot.

Advanced Computer Architecture | Extensive study of computer ISA, pipelining, performance metrics, and compilers.

CMOS VLSI Design | Project work included designing from scratch a 16x8 SRAM with control circuits in Cadence.

Machine Learning | Project work included working with a team to design and train a neural network to play chess.

Senior Design Project | Design and build the payload for the NASA Student Launch competition team. Role of CFO for the team with responsibilities including custom PCB design, CAD design, system testing, and technical documentation.

INVOLVEMENT

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| • Intramural Soccer | • Intramural Dodgeball | • Dorm Christian Life Coordinator |
| • Discipleship Group | • Chess Club Vice President | • FCC Radio License [Technician] |