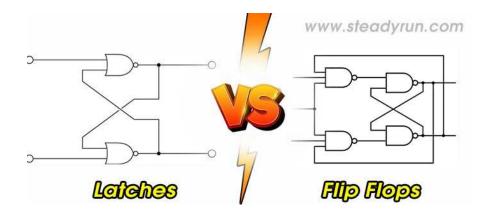
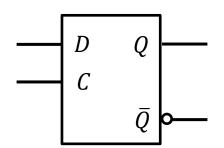
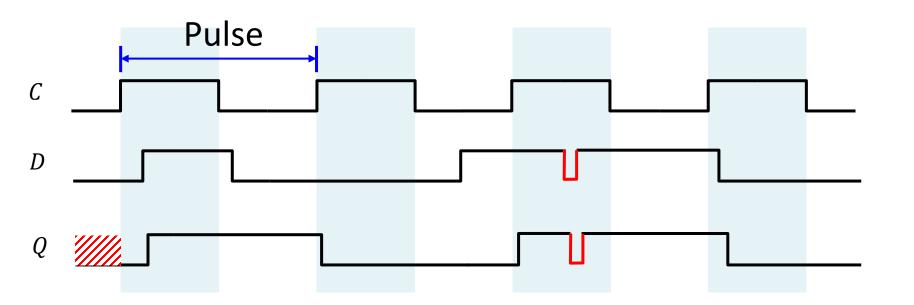
EE2000 Logic Circuit Design

Lecture 7 – Latch and Flip-Flop Circuits



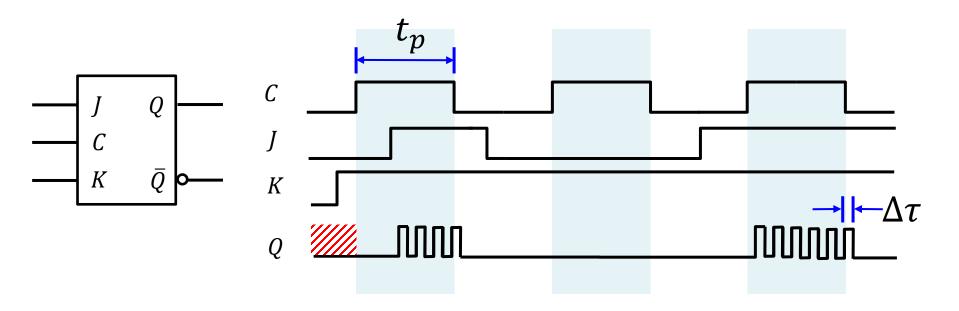
Transparency & Glitch





- FF is activated when C is HIGH (Pulse or Level triggered)
- Transparency: Input passes through directly to output
- Glitch: Undesired signal

Race Around Condition



- When both J and K are HIGH, the output toggles continuously (racing) and becomes uncertain
- Unless propagation delay of the gates larger than the pulse width $(\Delta au > t_p)$

$$T = x + C$$
 $S = A$ $R = A'$
 $J = K = B$

$$x = A$$

$$x, A, B, C = 0$$

$$A = A' = 1 \text{ (RESET STATE)}$$

$$B = A = 0 \text{ (HOLD STATE)}$$

$$C$$

$$T = x + C$$
 $S = A$ $R = A'$
 $J = K = B$

$$x = A$$

$$x, A, B, C = 0$$

$$A = A = A'$$

$$T = x + C = 0 \text{ (HOLD STATE)}$$

$$S = A = 0, R = A' = 1 \text{ (RESET STATE)}$$

$$J = K = B = 0 \text{ (HOLD STATE)}$$

$$T = x + C$$
 $S = A$ $R = A'$
 $J = K = B$

$$x = 1; A, B, C = 0$$

$$T = x + C = 1 \text{ (TOGGLE STATE)}$$

$$S = A = 0, R = A' = 1 \text{ (RESET STATE)}$$

$$J = K = B = 0 \text{ (HOLD STATE)}$$

$$T = x + C$$
 $S = A$ $R = A'$
 $J = K = B$

$$x = 1; A, B, C = 0$$

$$T = x + C = 1 \text{ (TOGGLE STATE)}$$

$$S = A = 0, R = A' = 1 \text{ (RESET STATE)}$$

$$J = K = B = 0 \text{ (HOLD STATE)}$$

$$T = x + C$$
 $S = A$ $R = A'$
 $J = K = B$

$$x \longrightarrow T$$

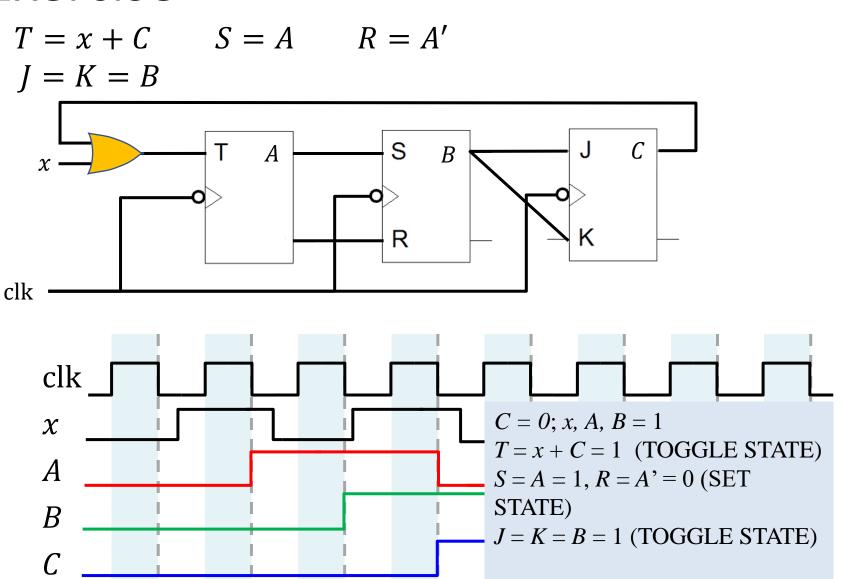
$$A = I; x, B, C = 0$$

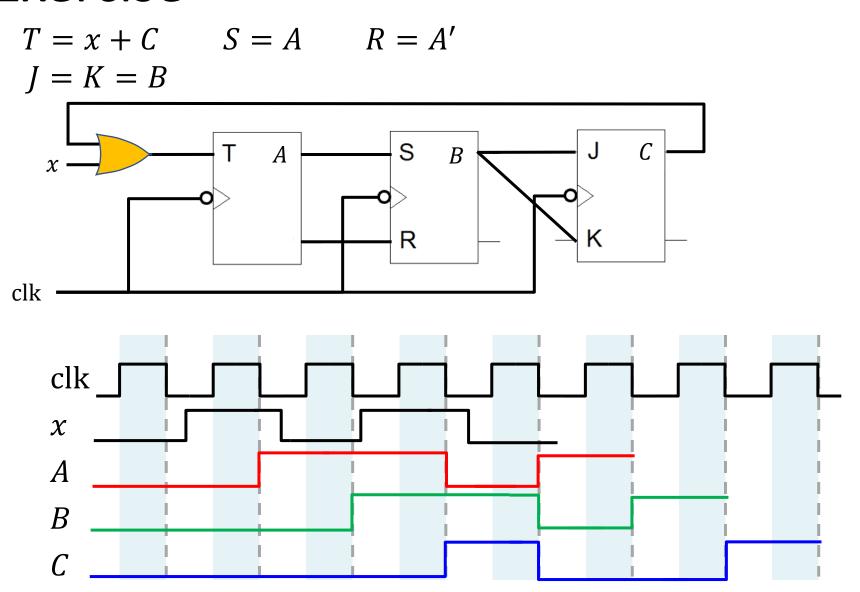
$$T = x + C = 0 \text{ (HOLD STATE)}$$

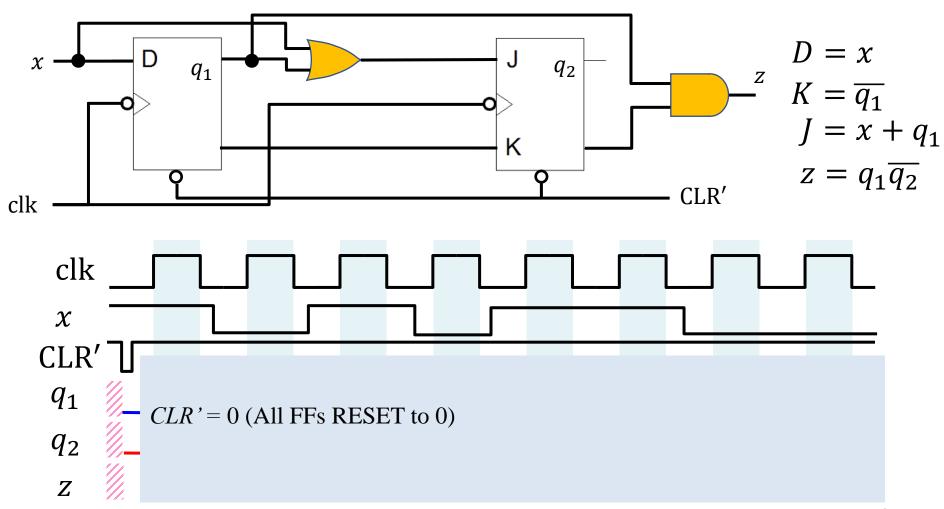
$$S = A = 1, R = A' = 0 \text{ (SET STATE)}$$

$$J = K = B = 0 \text{ (HOLD STATE)}$$

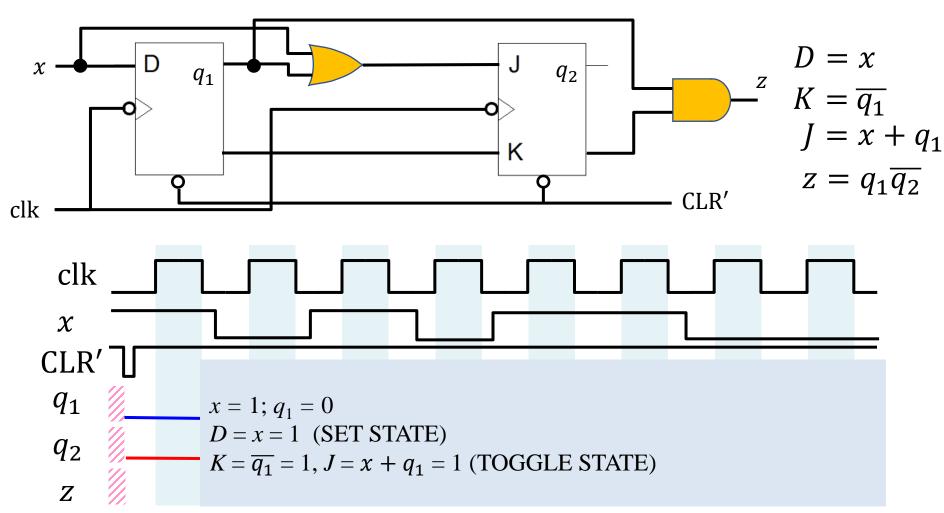
$$T = x + C$$
 $S = A$ $R = A'$
 $J = K = B$
 Clk
 Clk

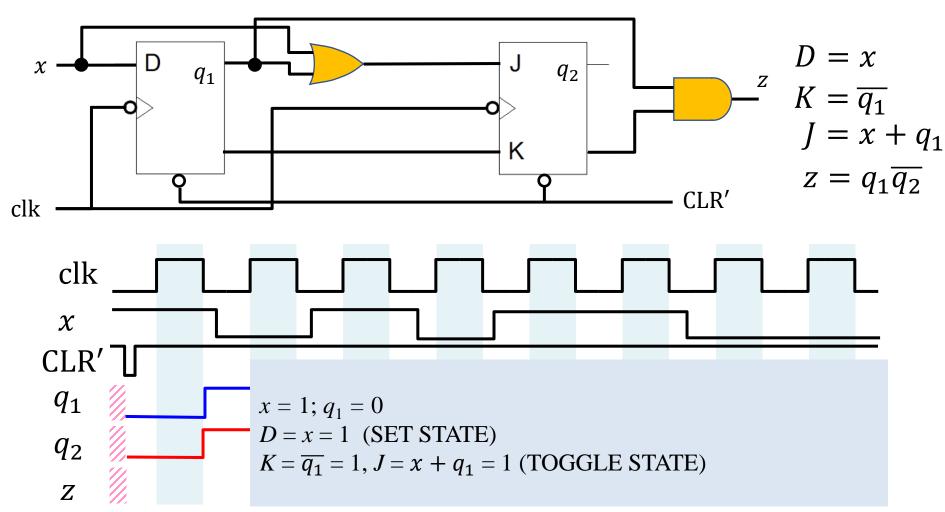


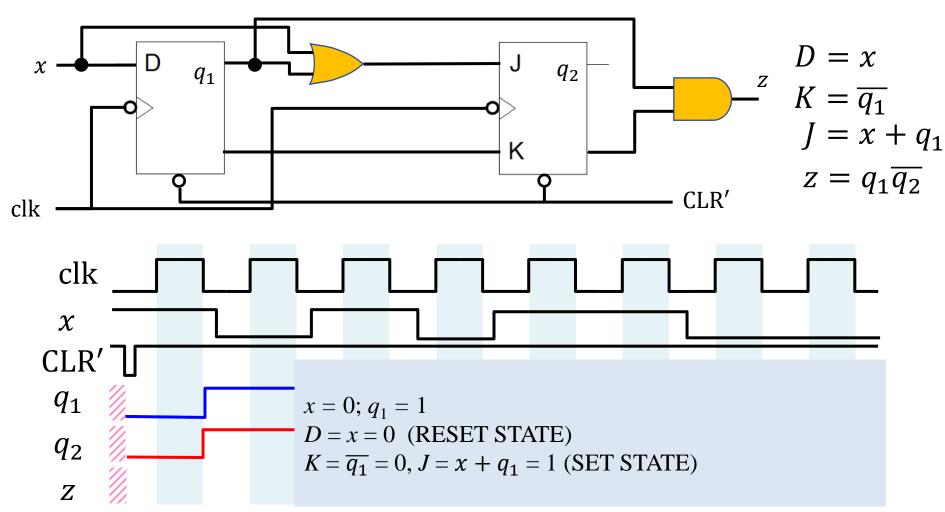


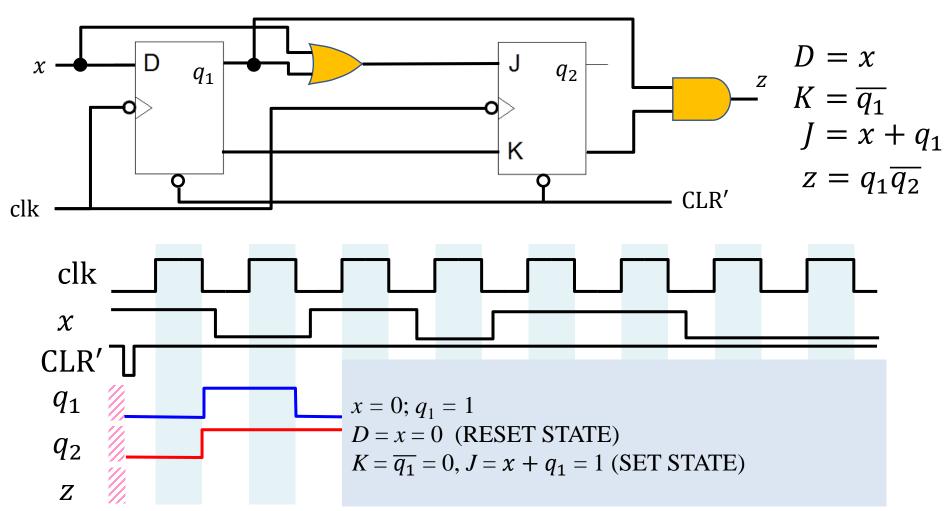


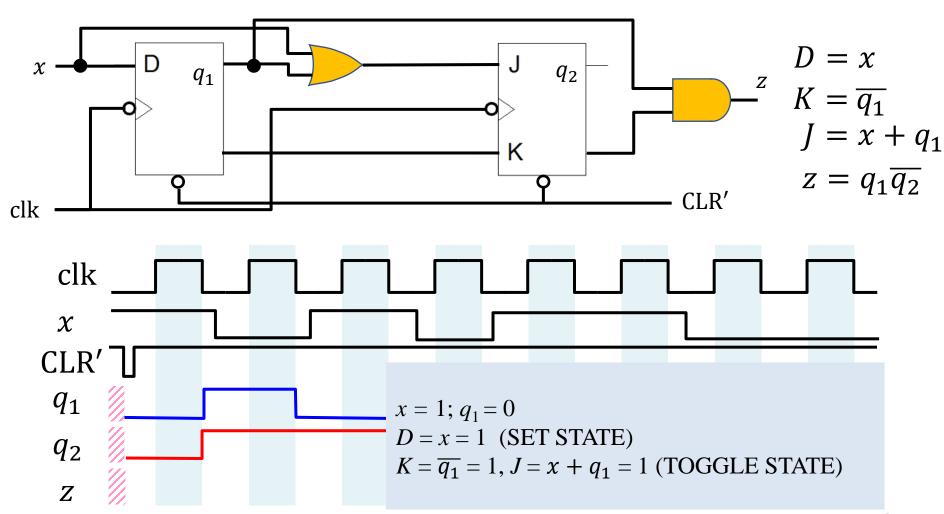
Ignore z first because z is a combinational circuit

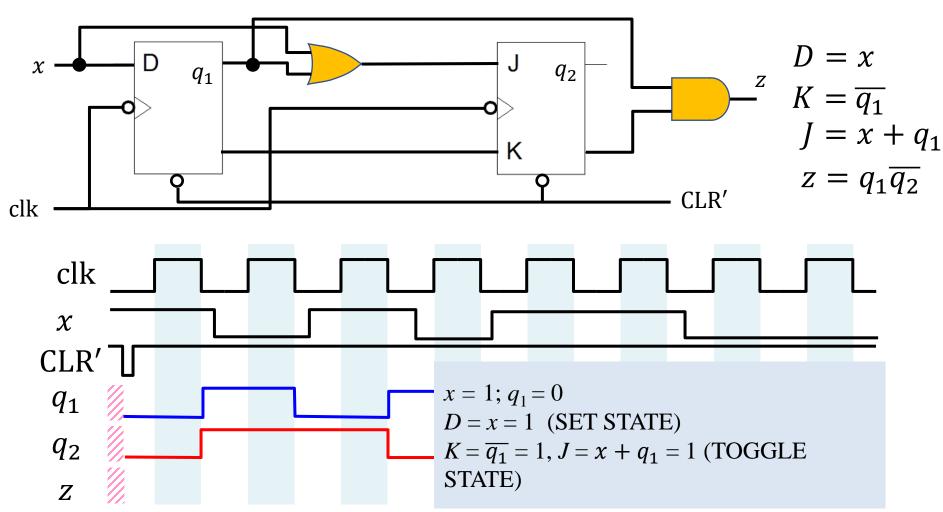


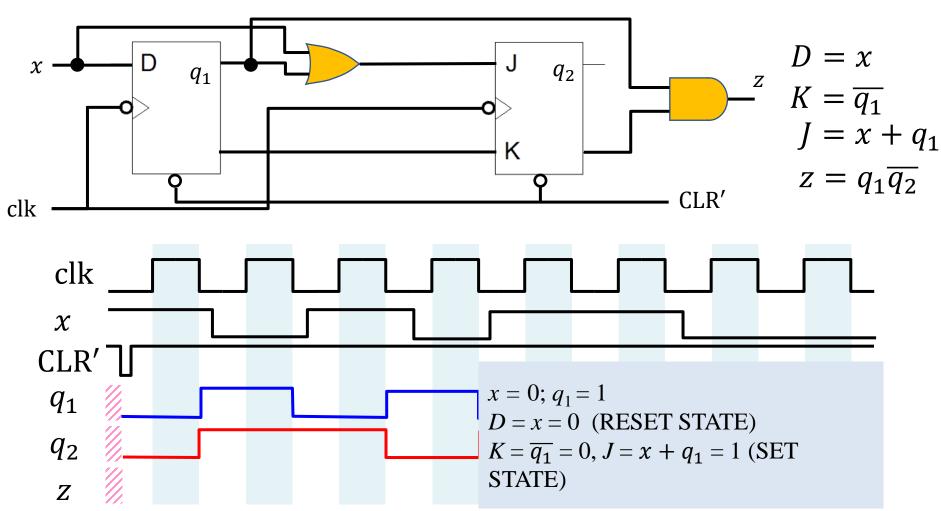


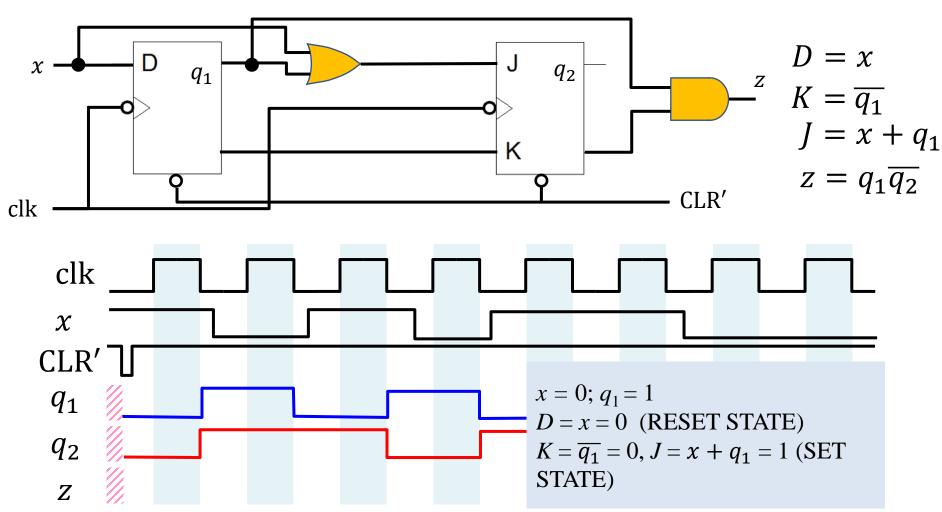


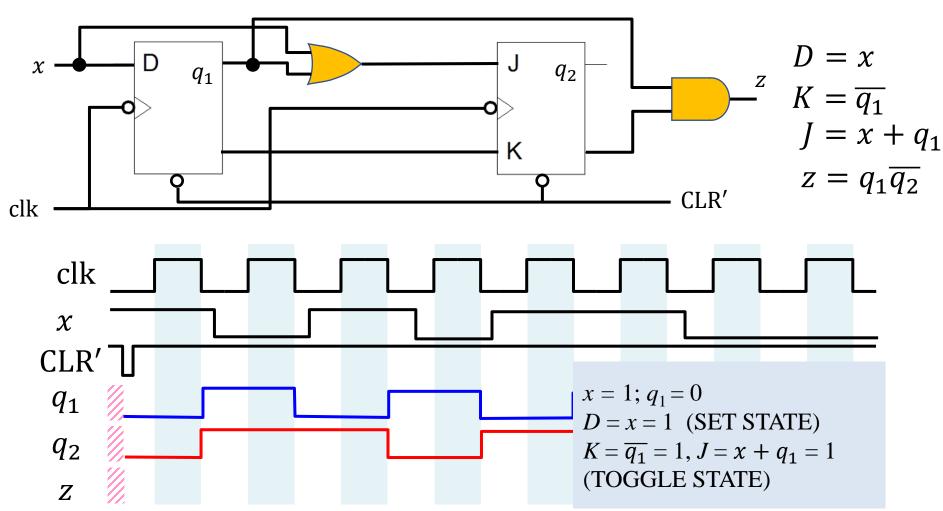


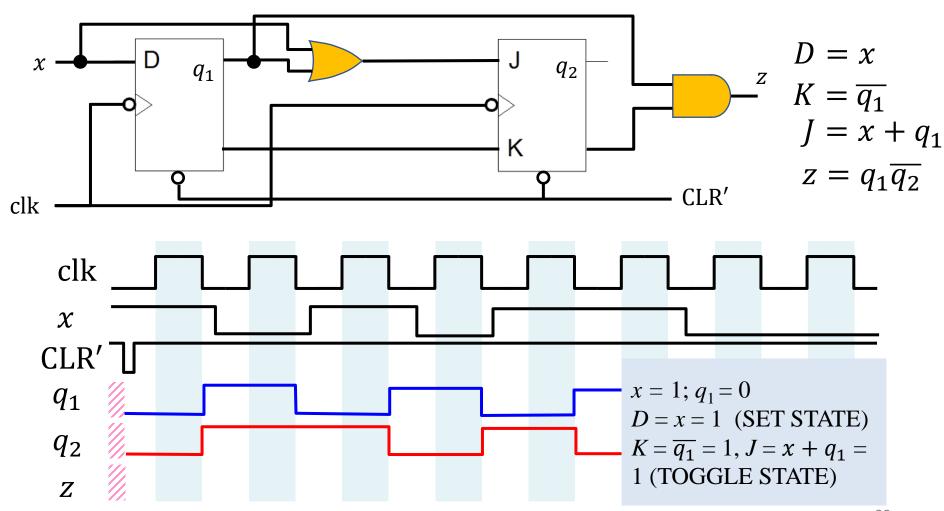


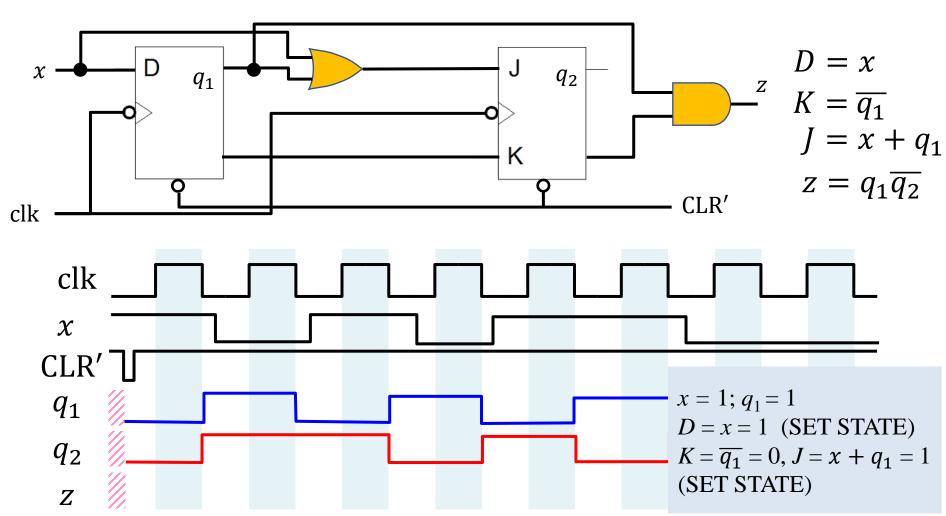


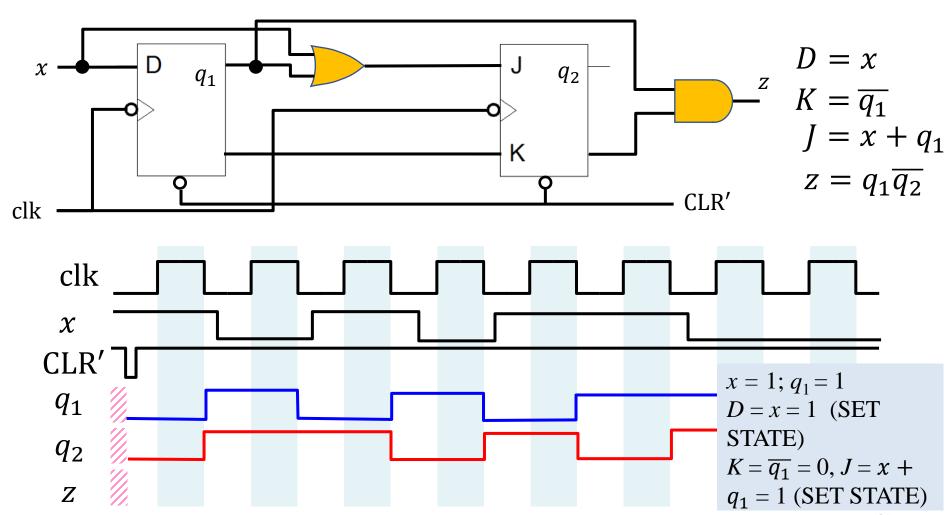


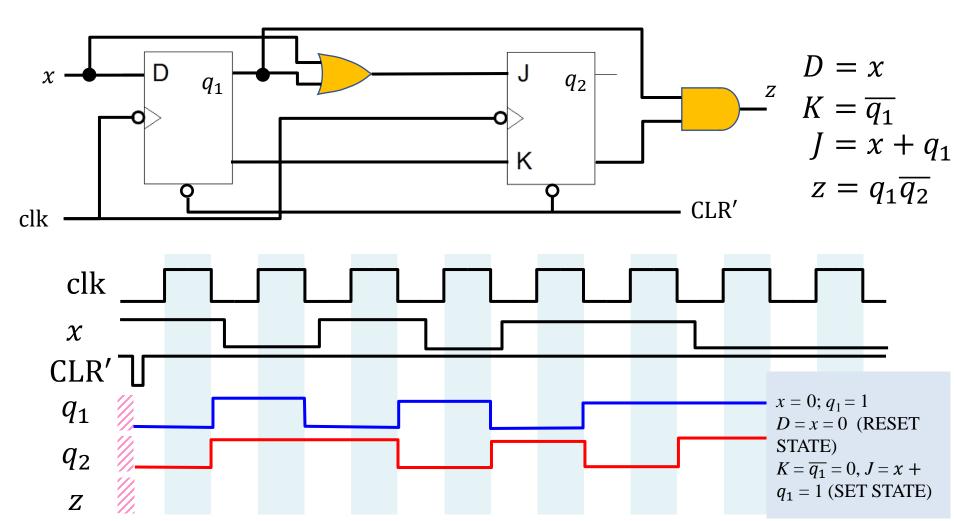


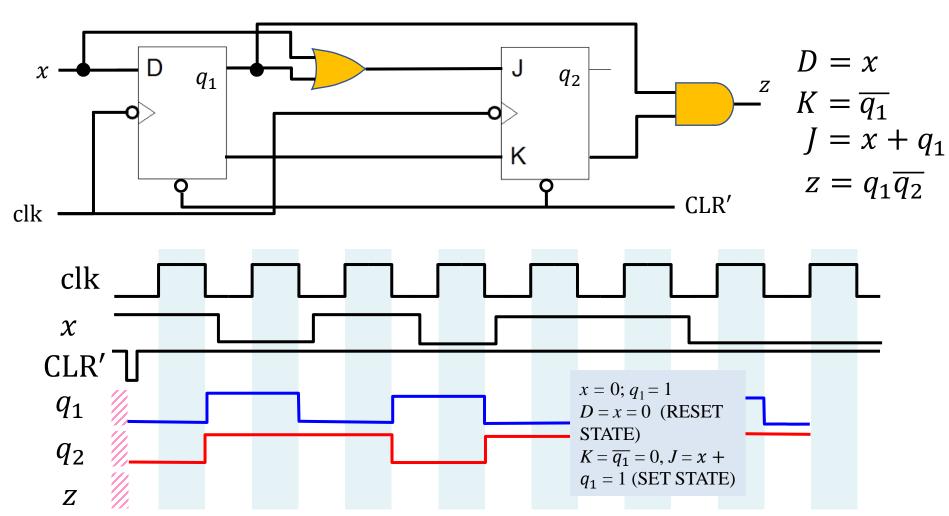


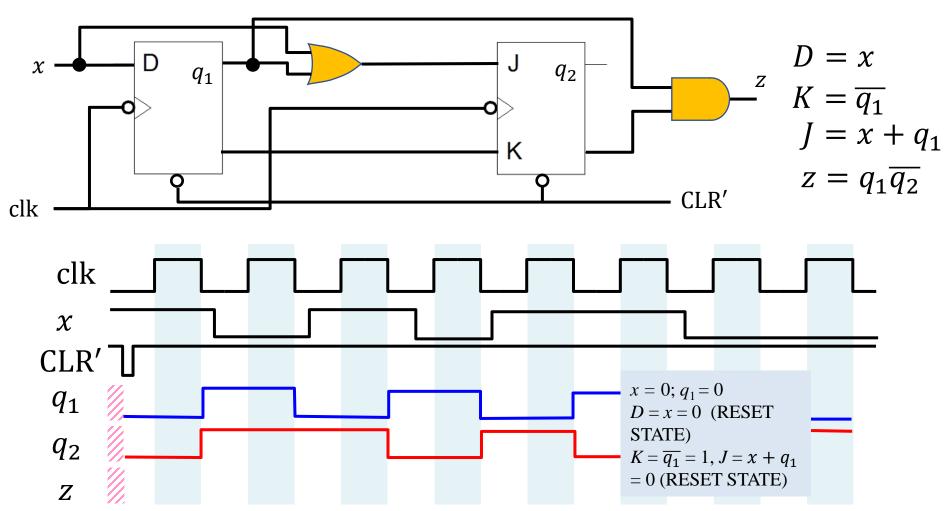


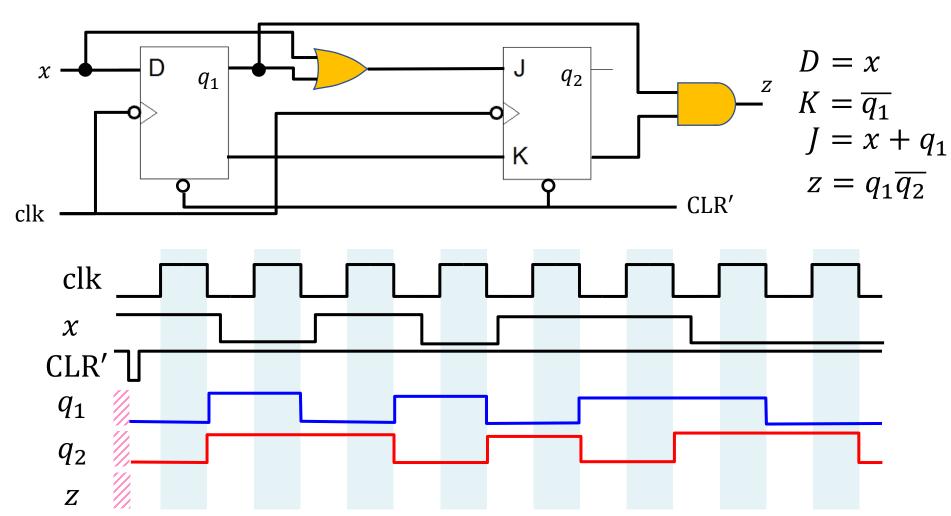


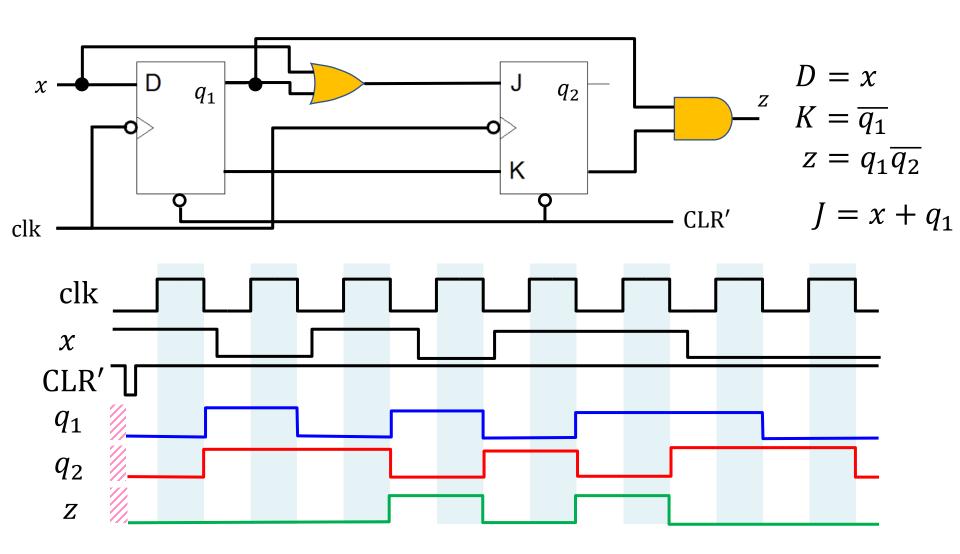












Final Step: Work out z

