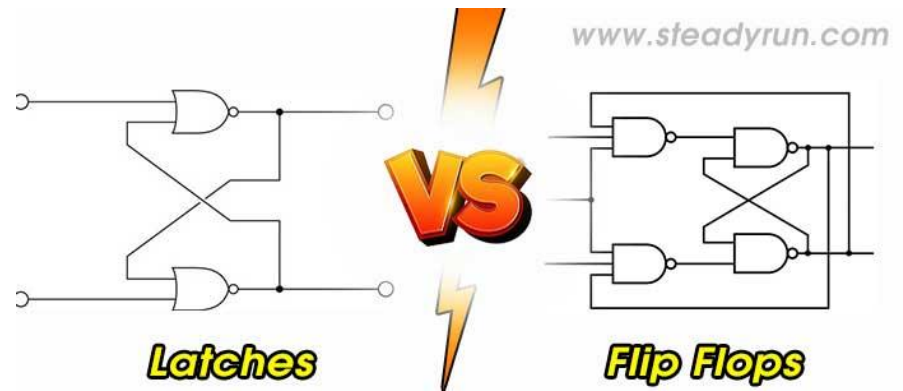
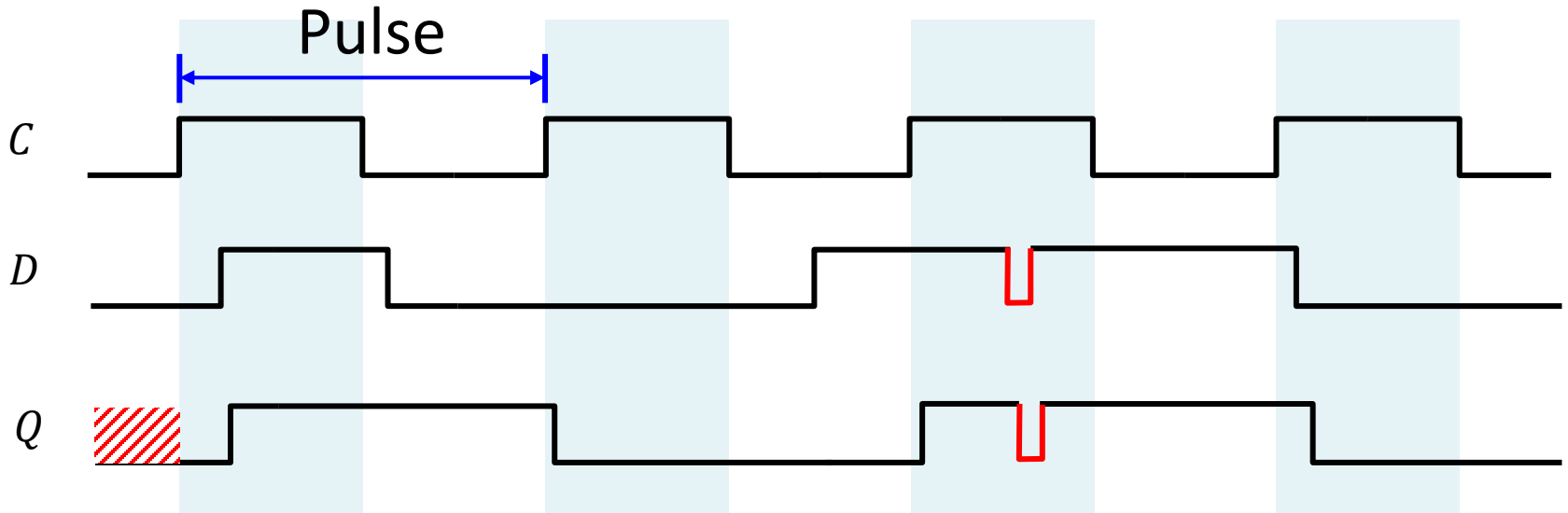
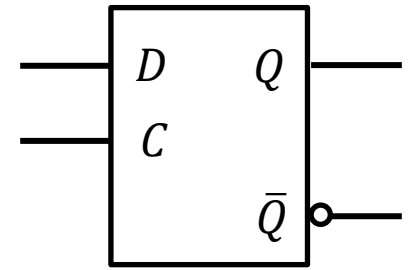


EE2000 Logic Circuit Design

Lecture 7 – Latch and Flip-Flop Circuits

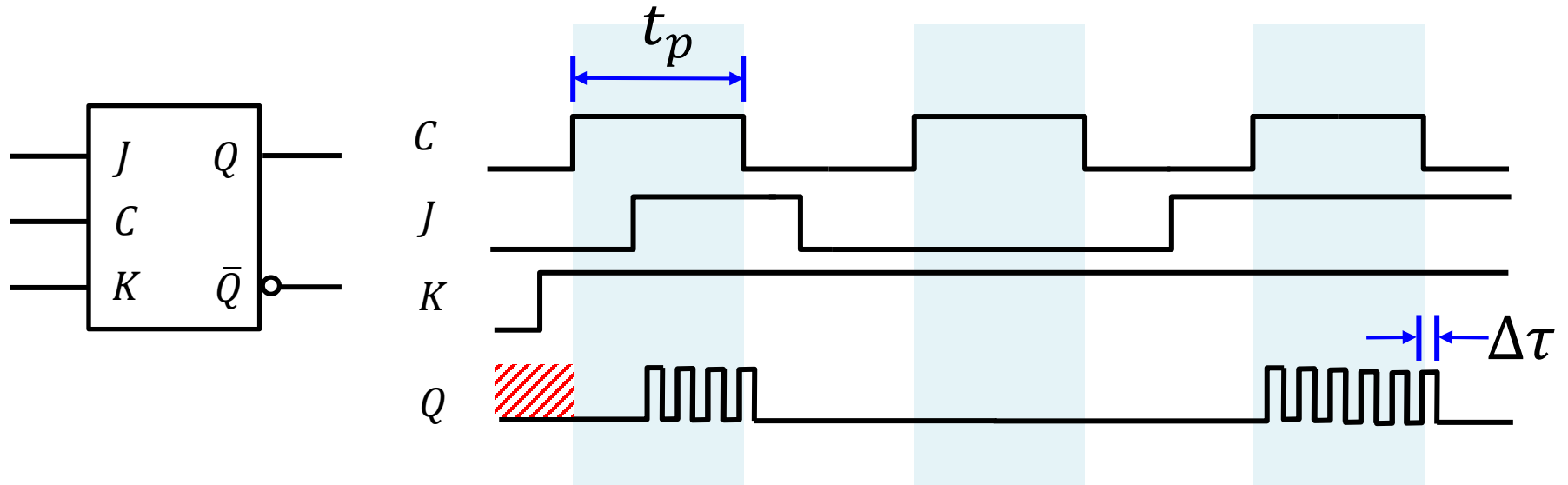


Transparency & Glitch



- FF is activated when C is HIGH (Pulse or Level triggered)
- Transparency: Input passes through directly to output
- Glitch: Undesired signal

Race Around Condition

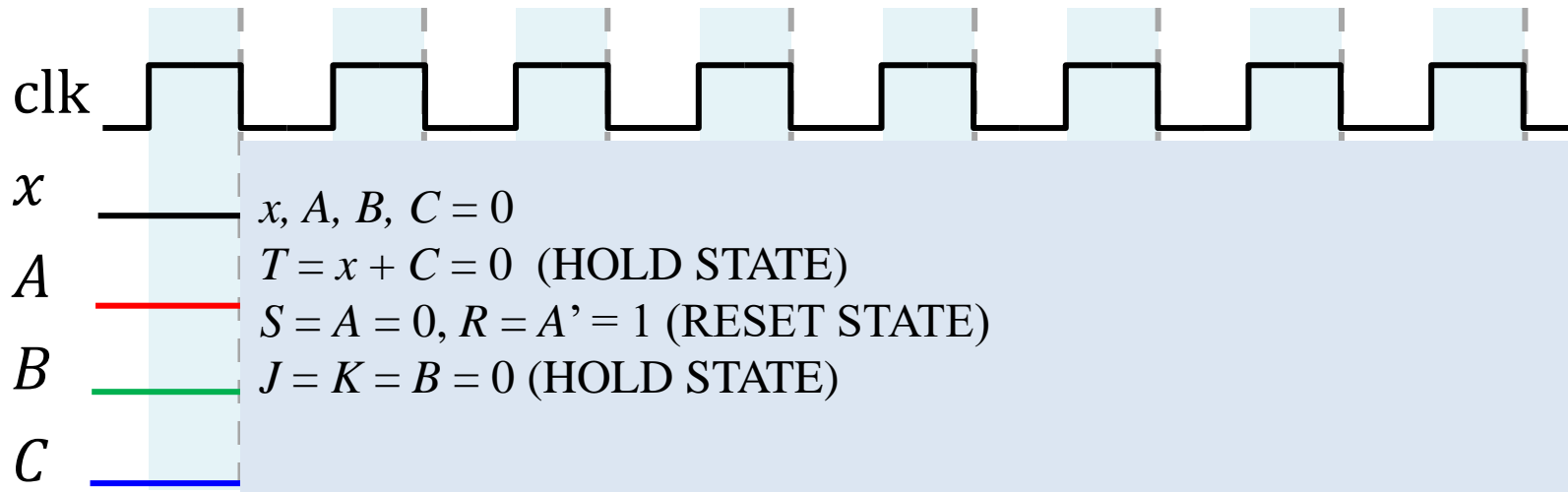
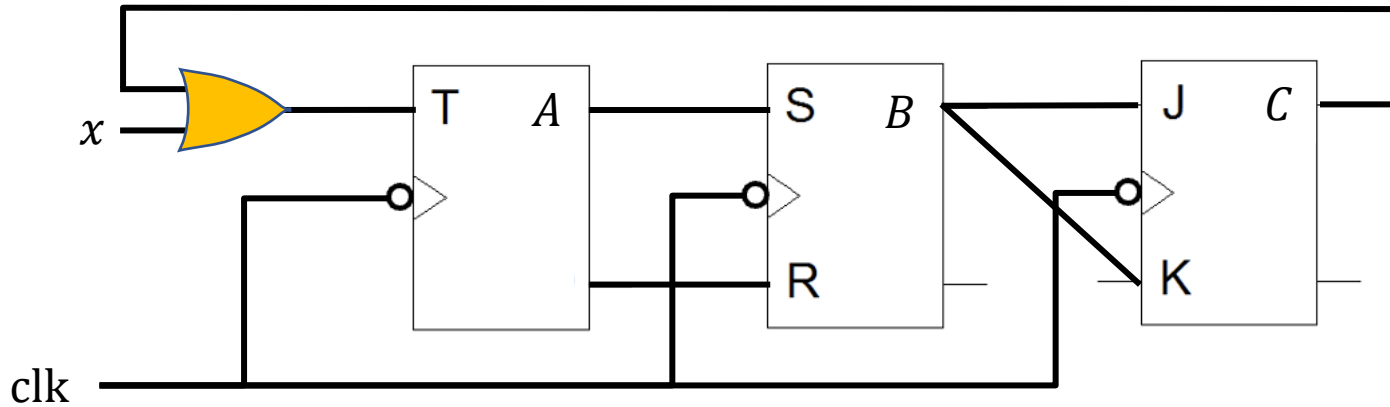


- When both J and K are HIGH, the output toggles continuously (racing) and becomes uncertain
- Unless propagation delay of the gates larger than the pulse width ($\Delta\tau > t_p$)

Exercise

$$T = x + C \quad S = A \quad R = A'$$

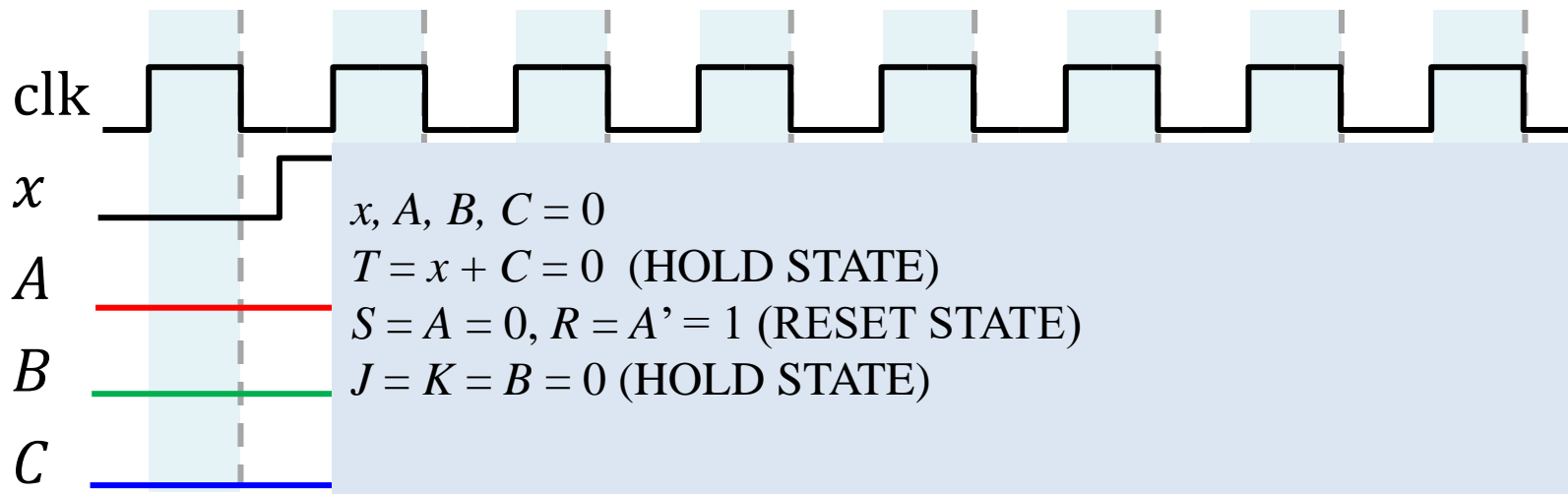
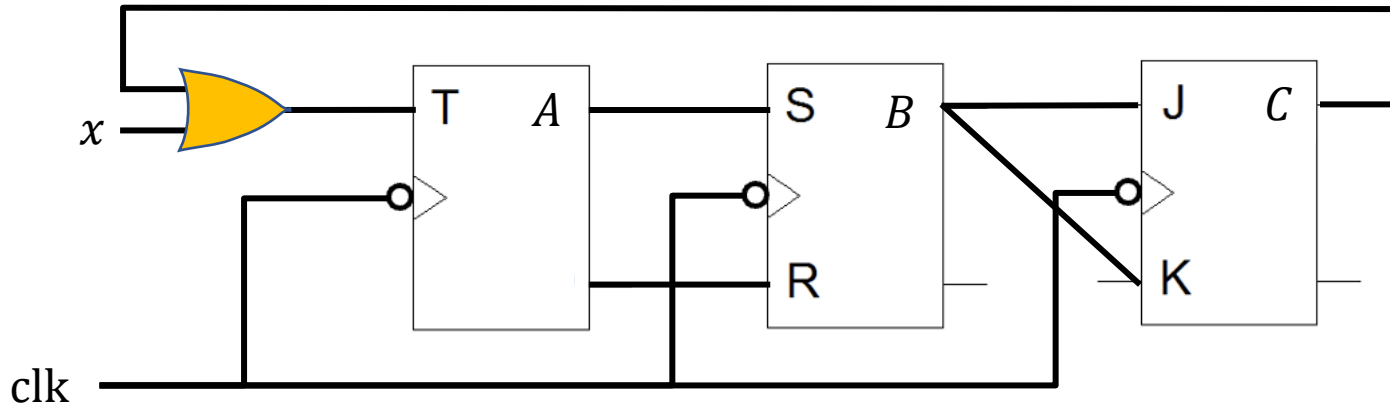
$$J = K = B$$



Exercise

$$T = x + C \quad S = A \quad R = A'$$

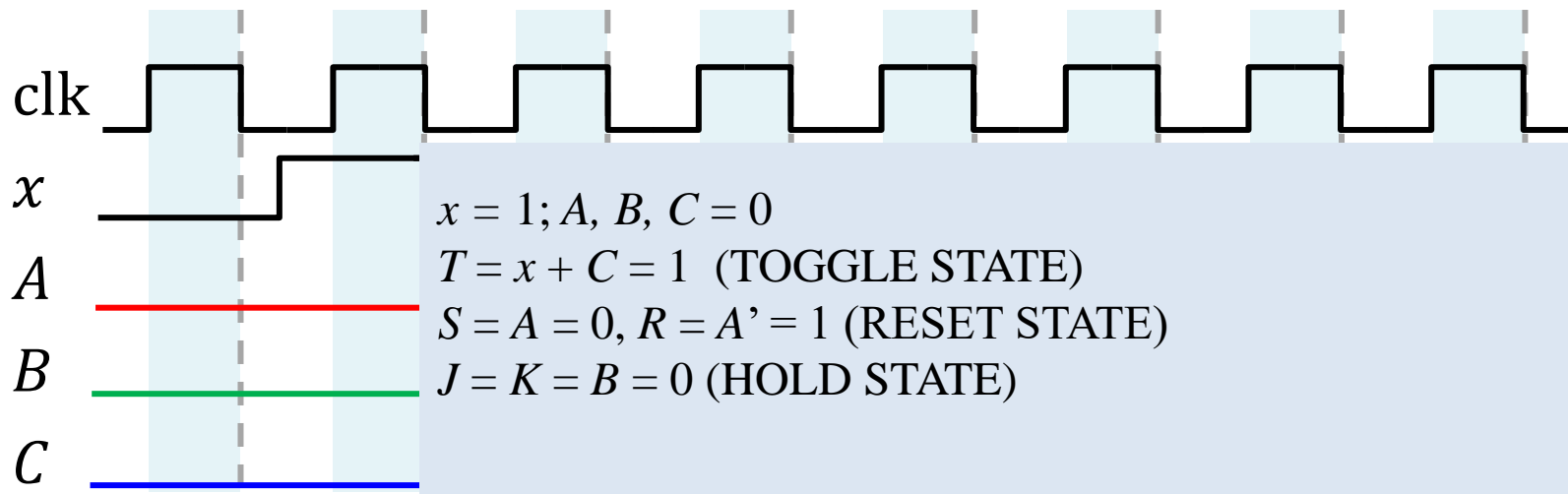
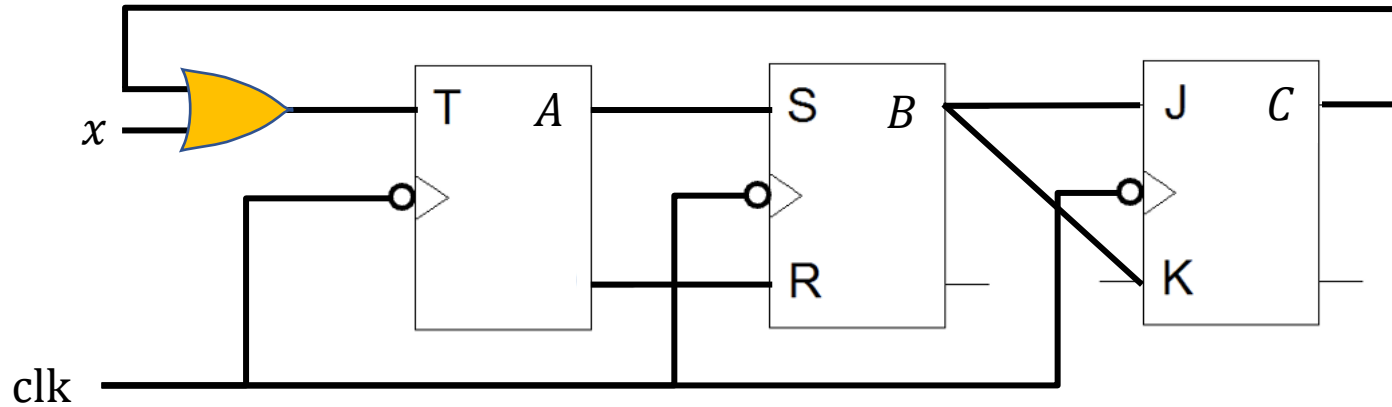
$$J = K = B$$



Exercise

$$T = x + C \quad S = A \quad R = A'$$

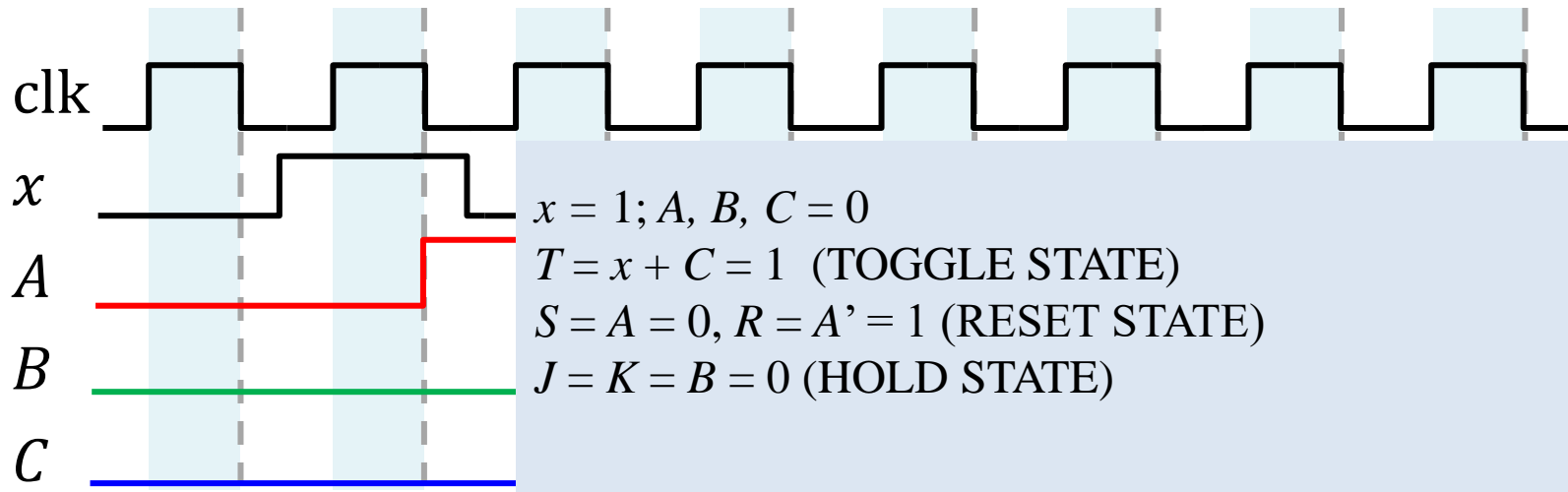
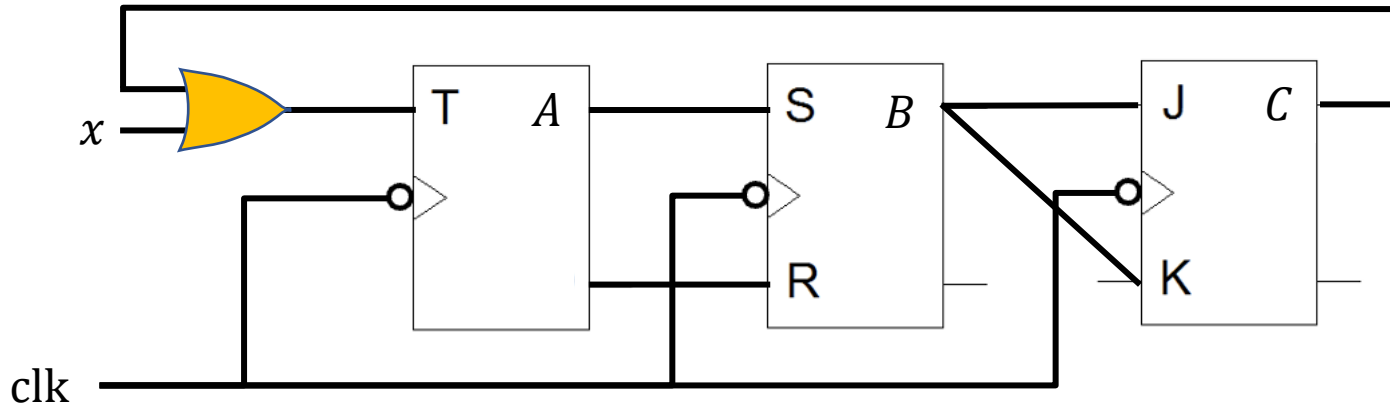
$$J = K = B$$



Exercise

$$T = x + C \quad S = A \quad R = A'$$

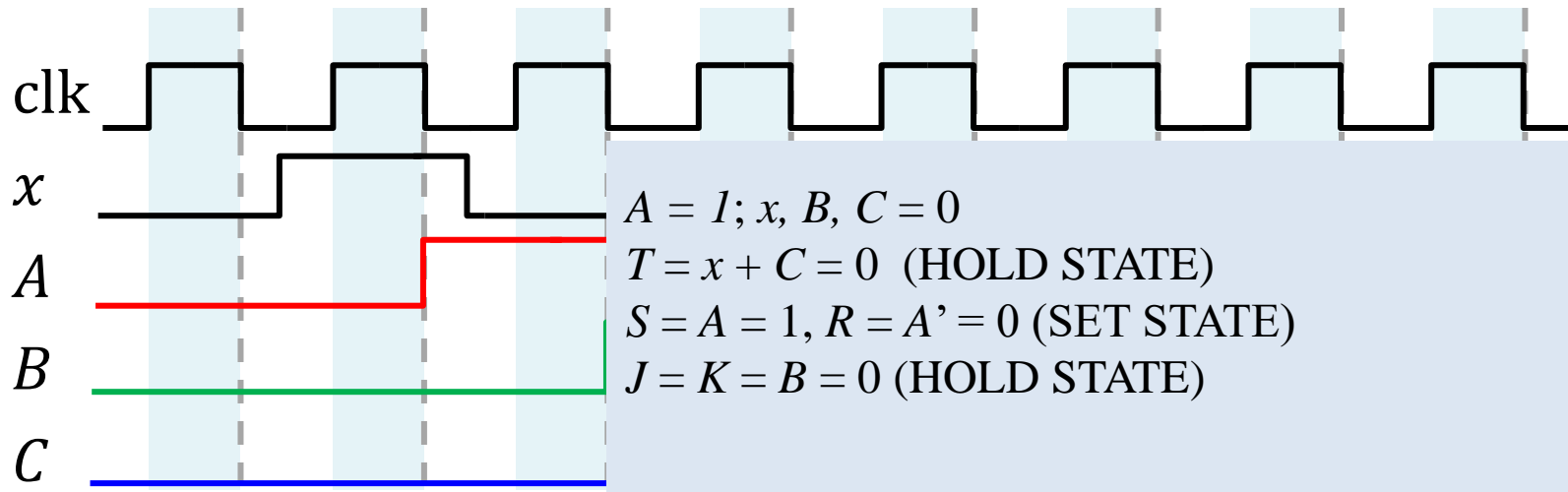
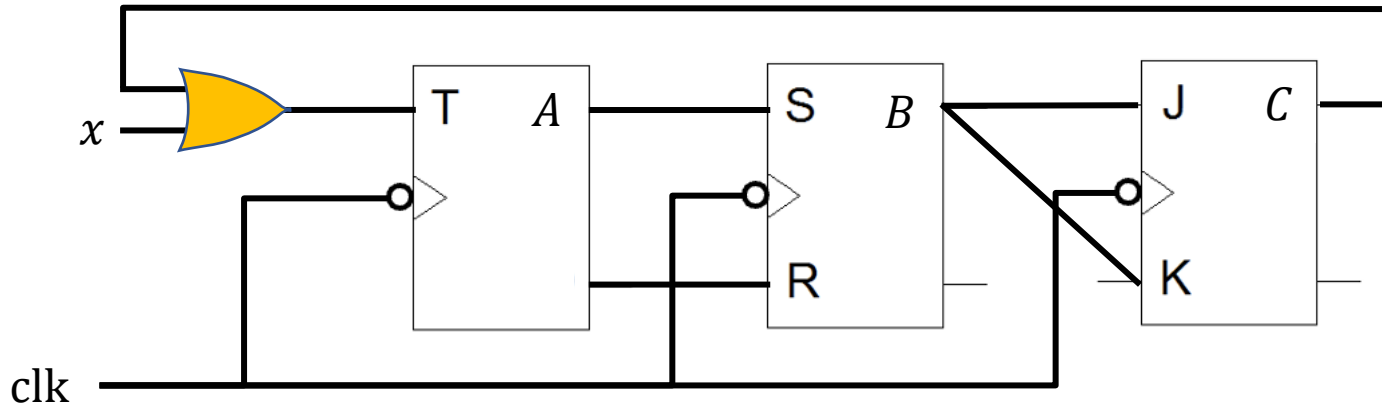
$$J = K = B$$



Exercise

$$T = x + C \quad S = A \quad R = A'$$

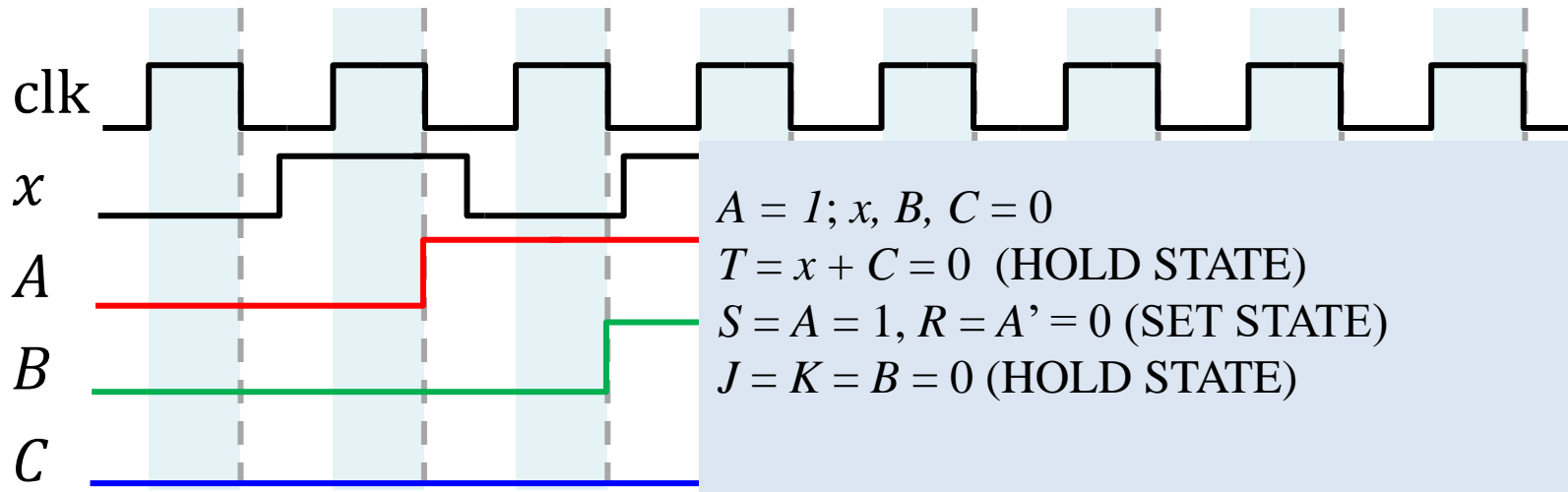
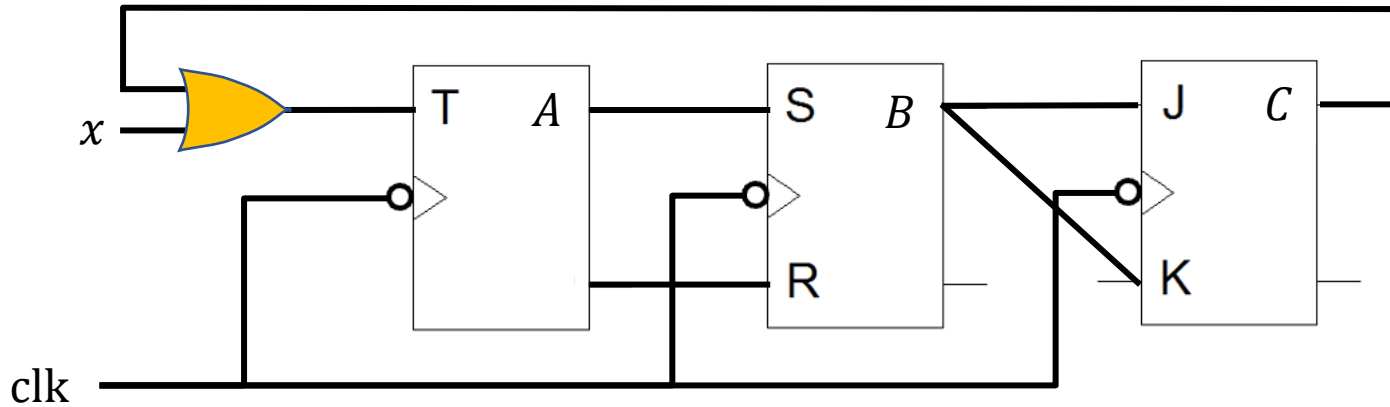
$$J = K = B$$



Exercise

$$T = x + C \quad S = A \quad R = A'$$

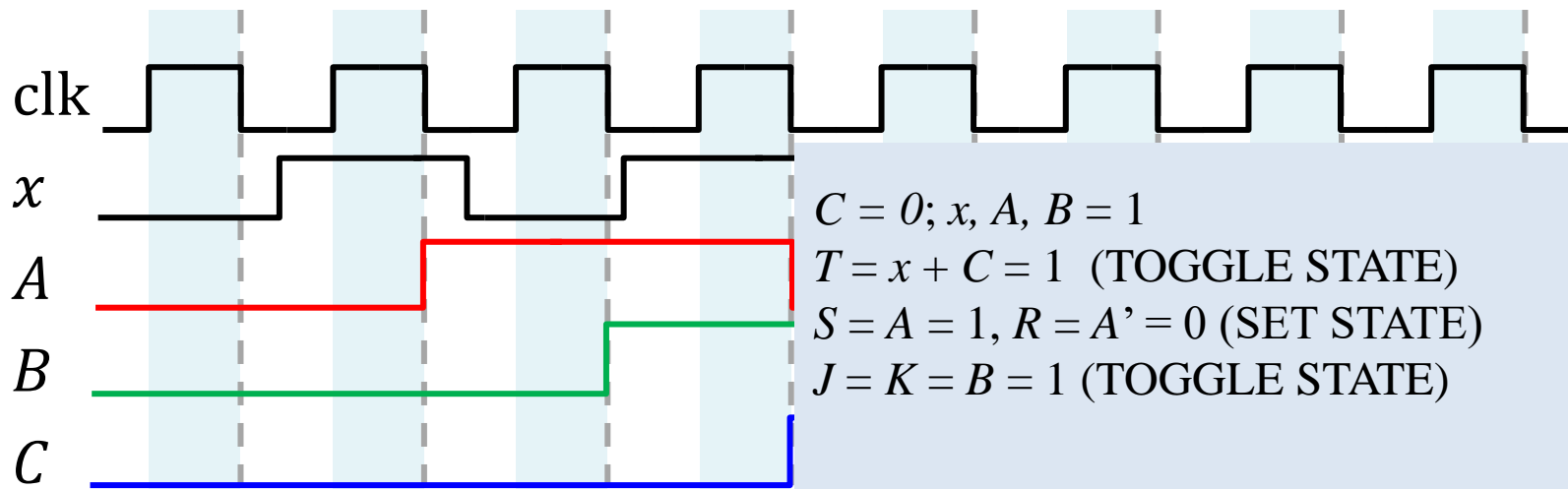
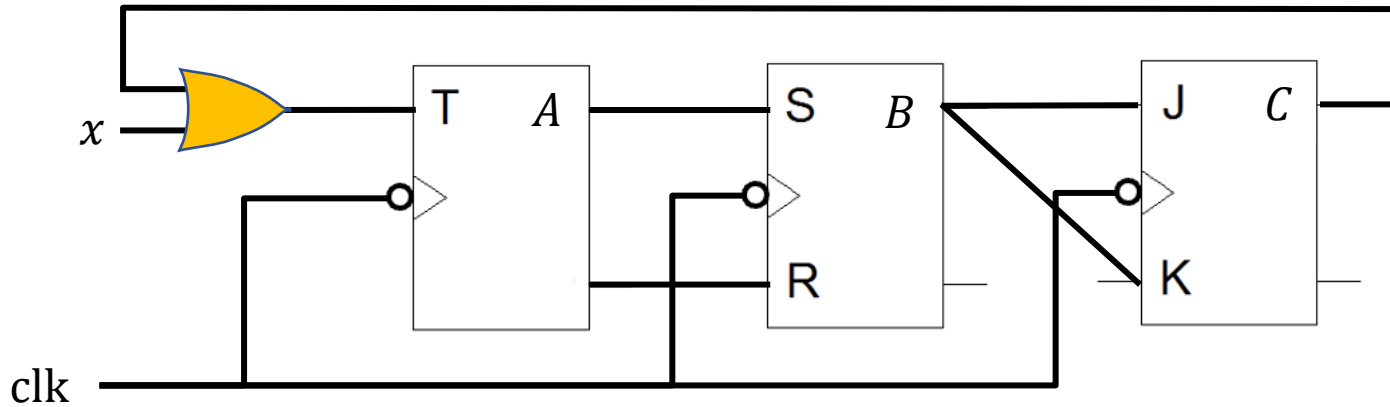
$$J = K = B$$



Exercise

$$T = x + C \quad S = A \quad R = A'$$

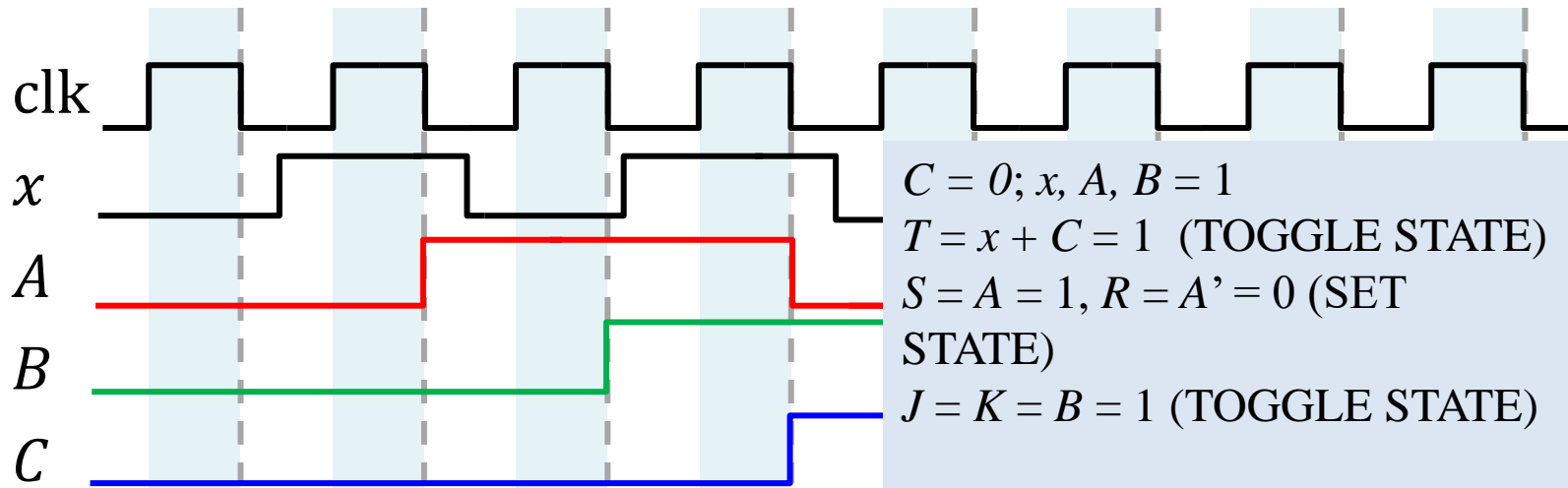
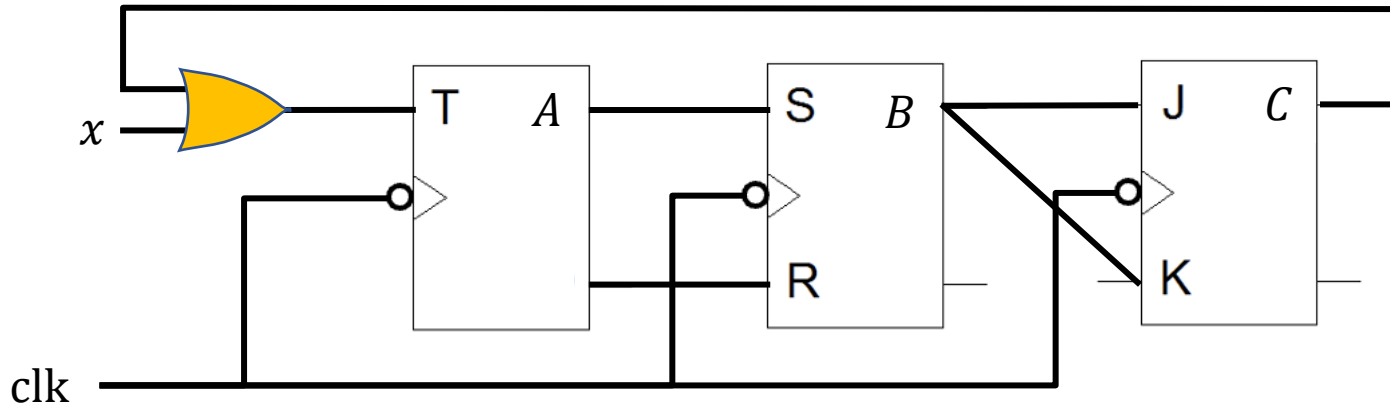
$$J = K = B$$



Exercise

$$T = x + C \quad S = A \quad R = A'$$

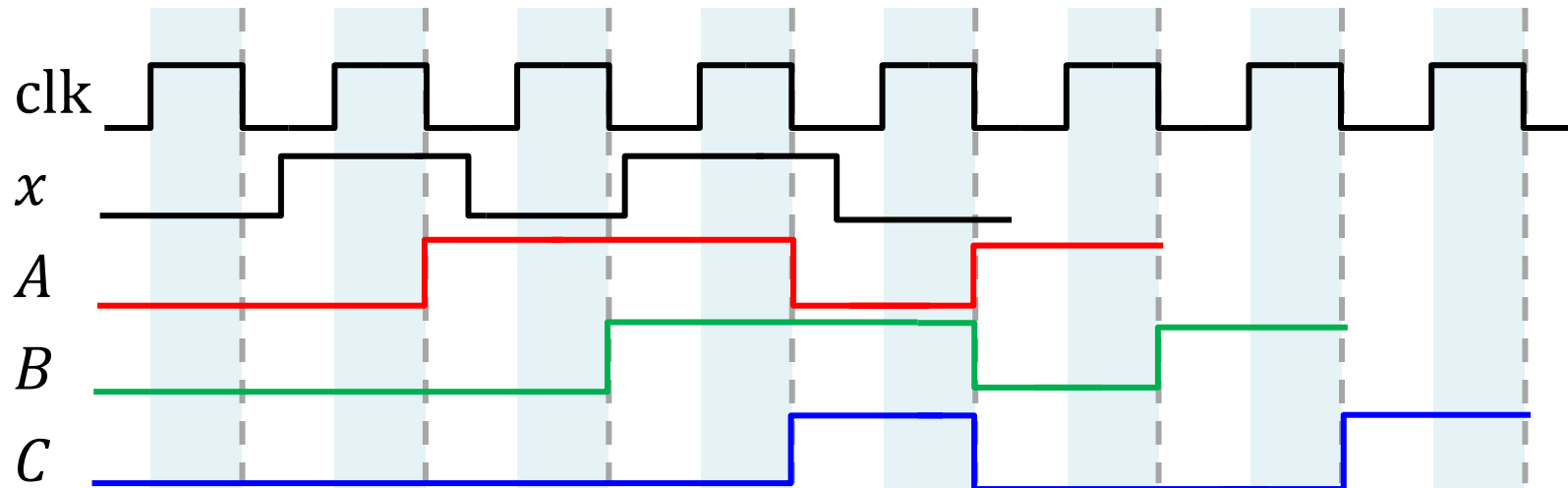
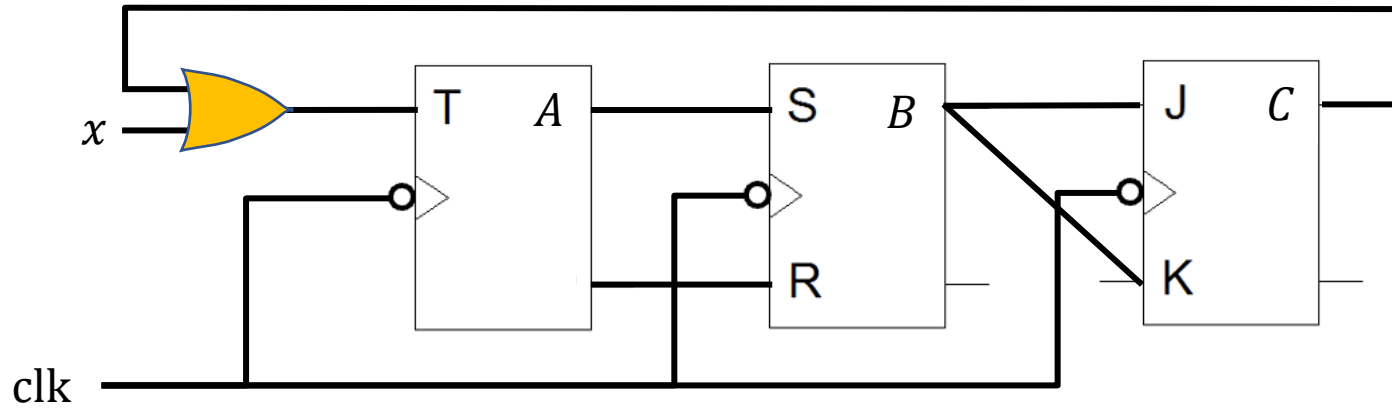
$$J = K = B$$



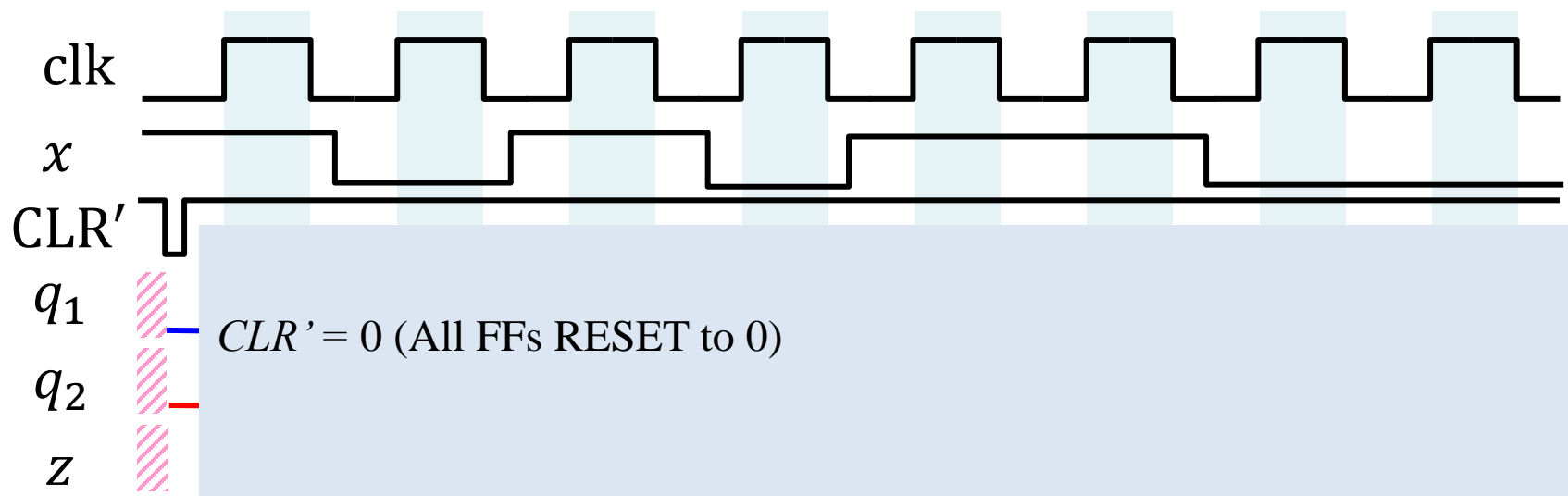
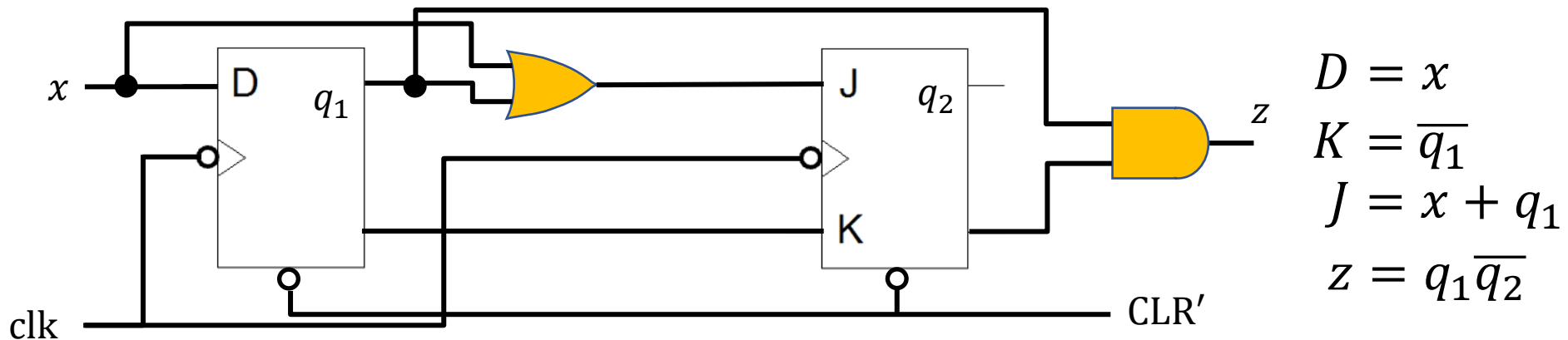
Exercise

$$T = x + C \quad S = A \quad R = A'$$

$$J = K = B$$

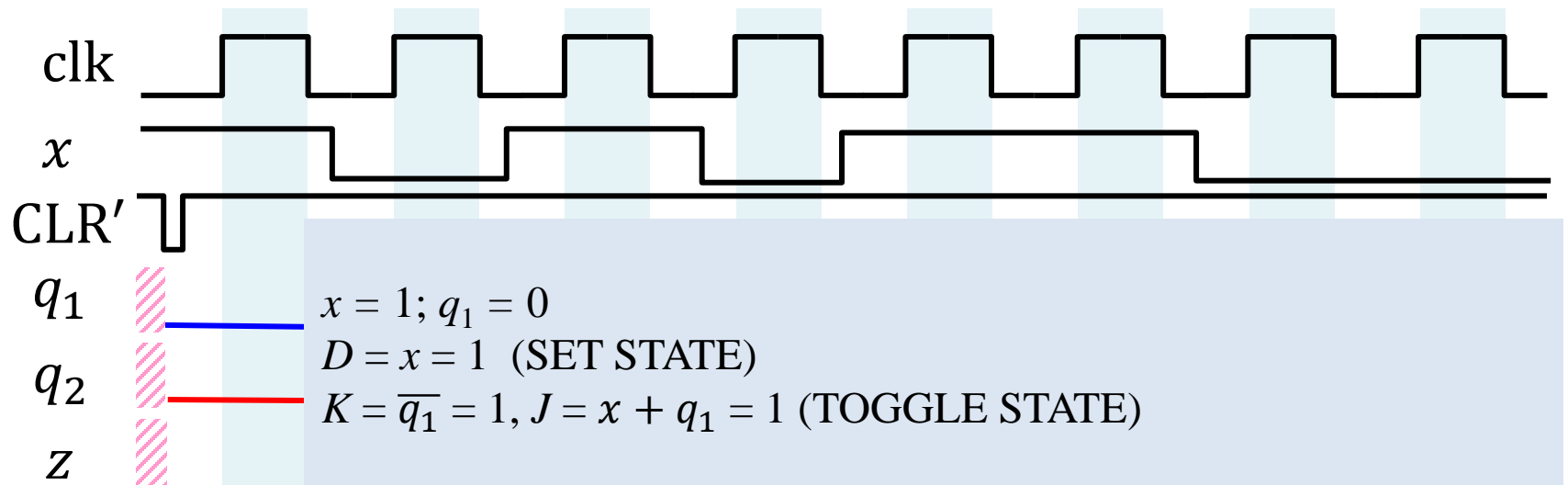
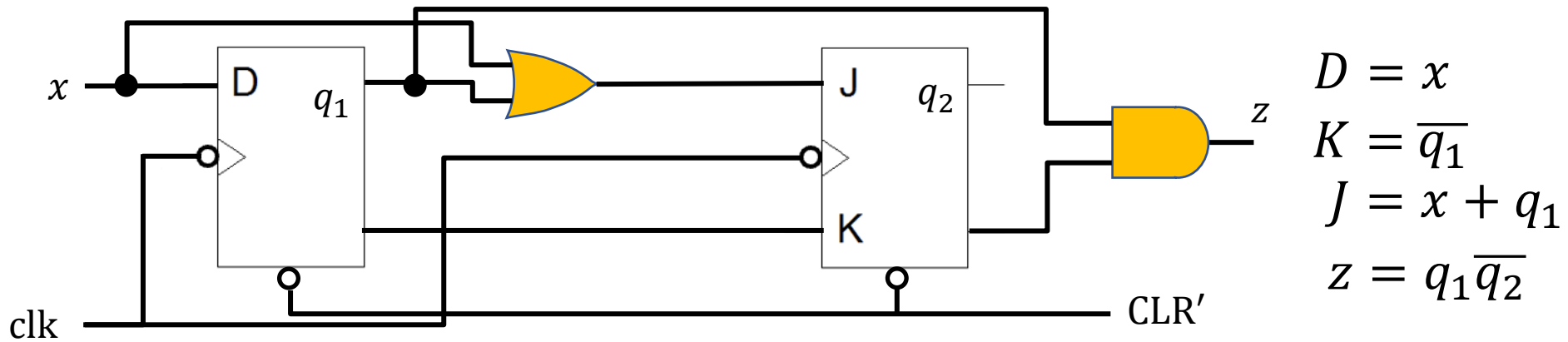


Exercise

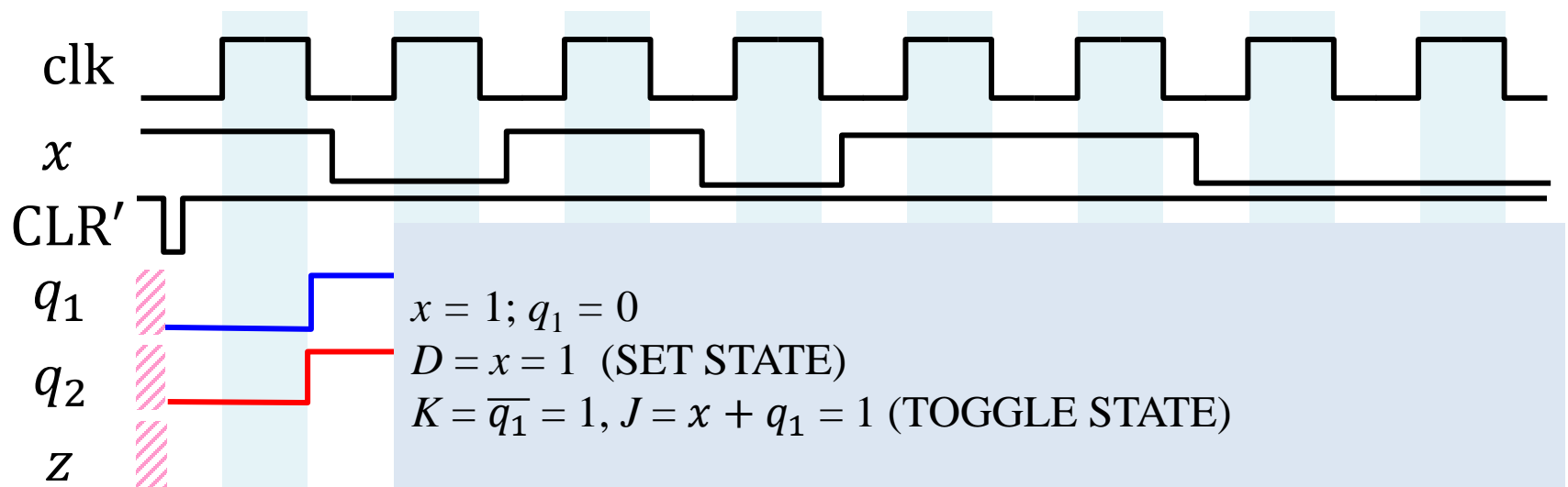
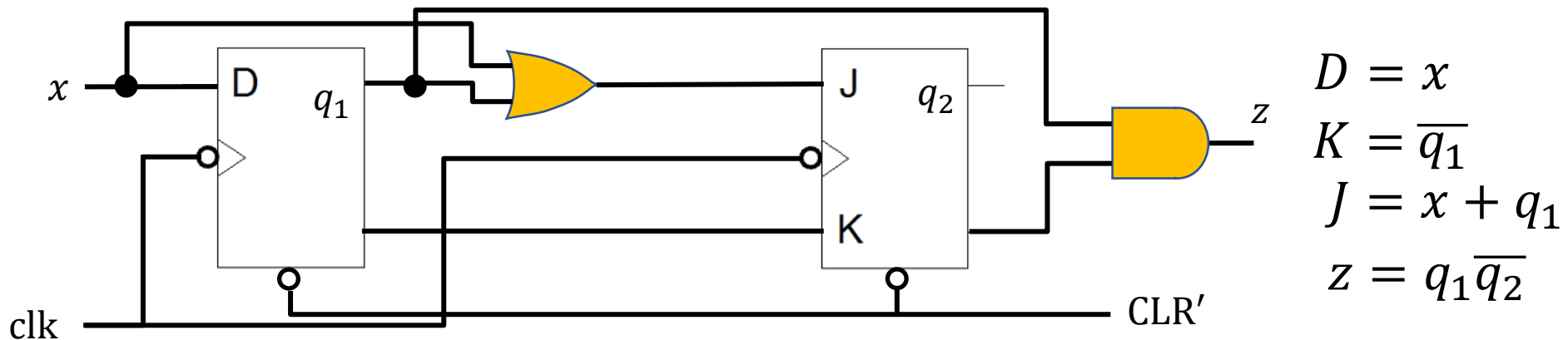


Exercise

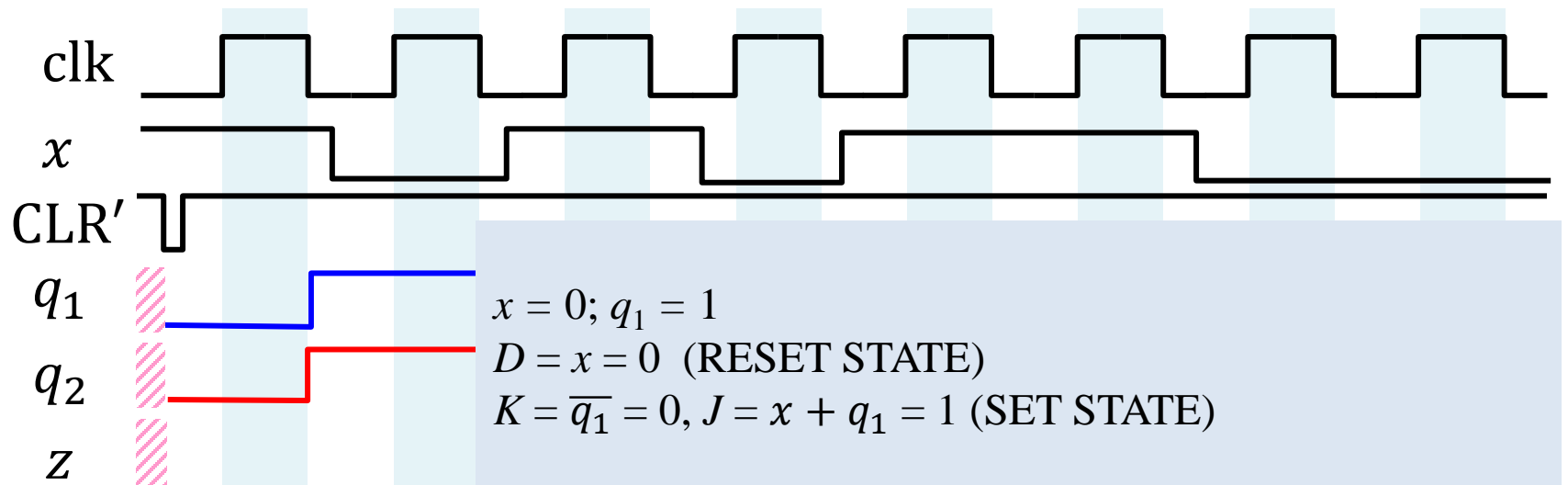
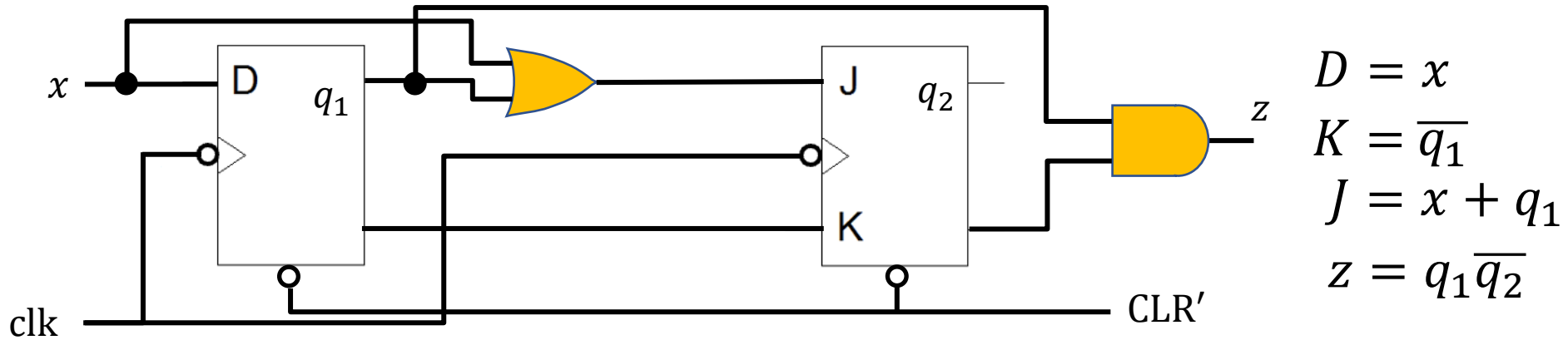
Ignore z first because z is a combinational circuit



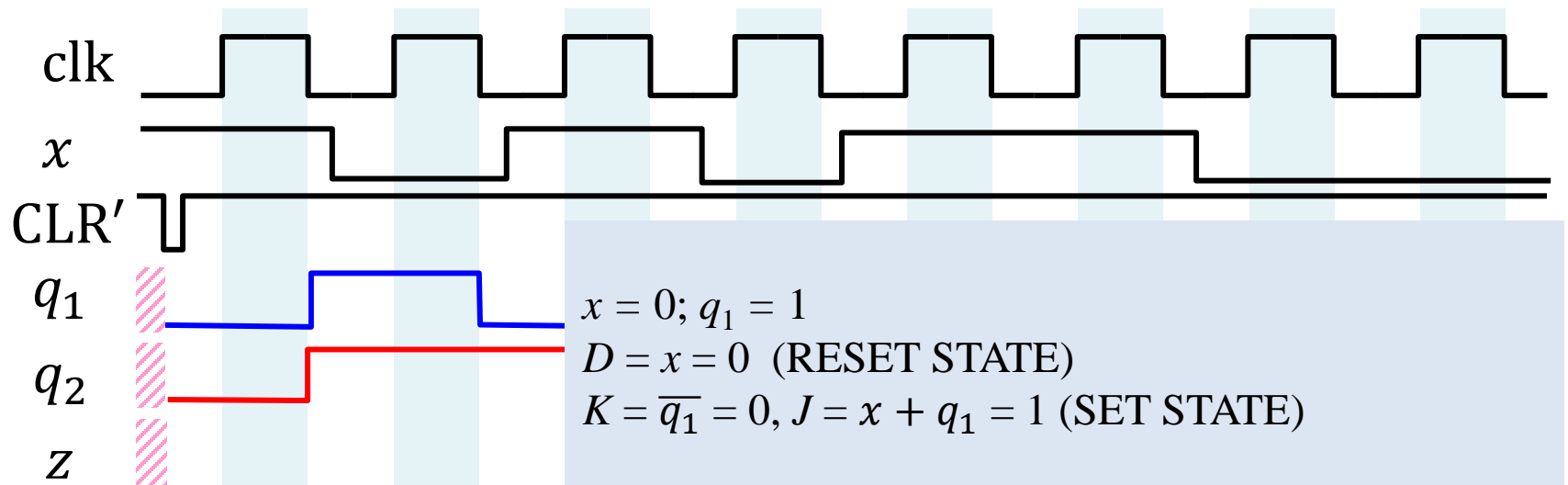
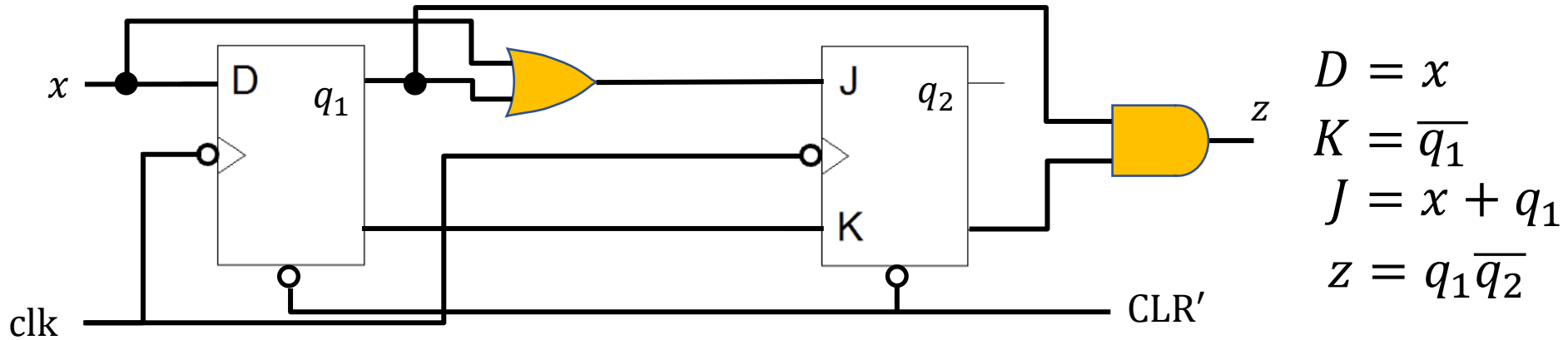
Exercise



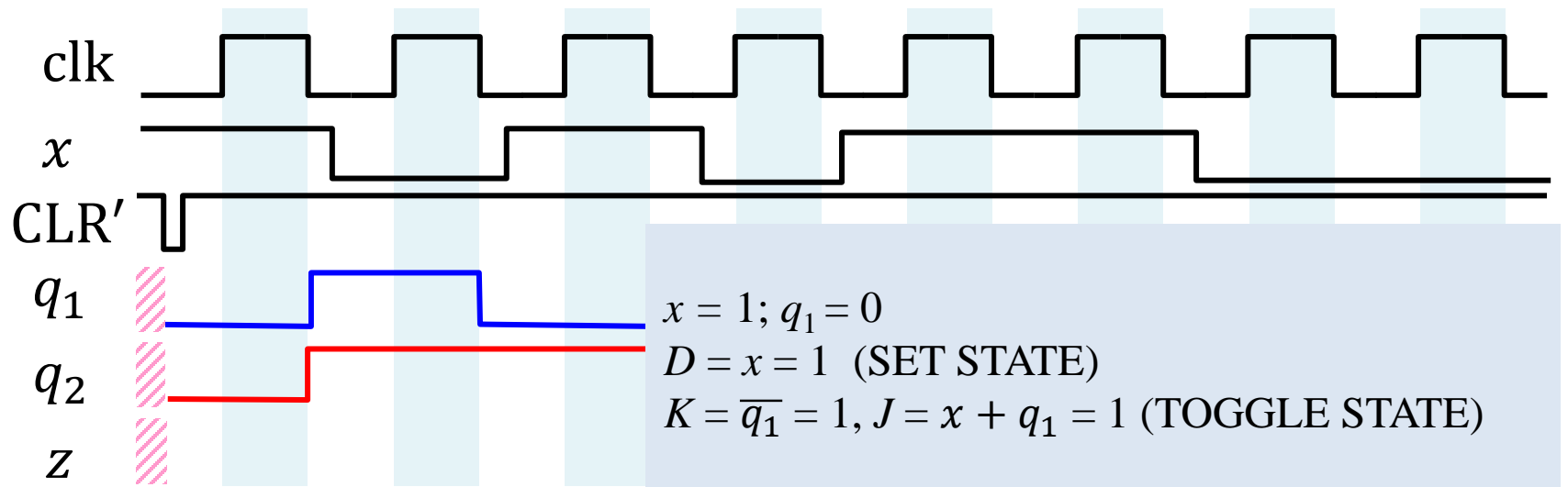
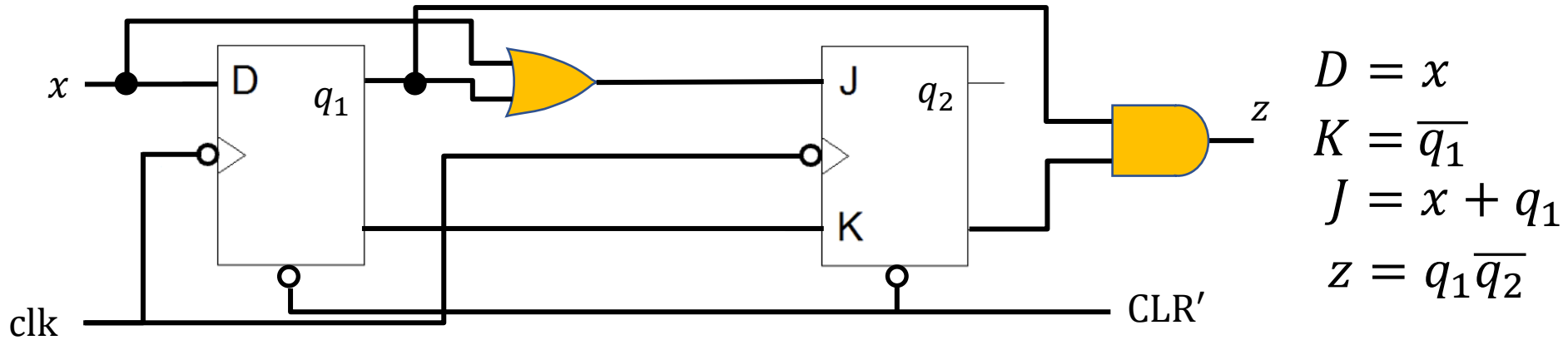
Exercise



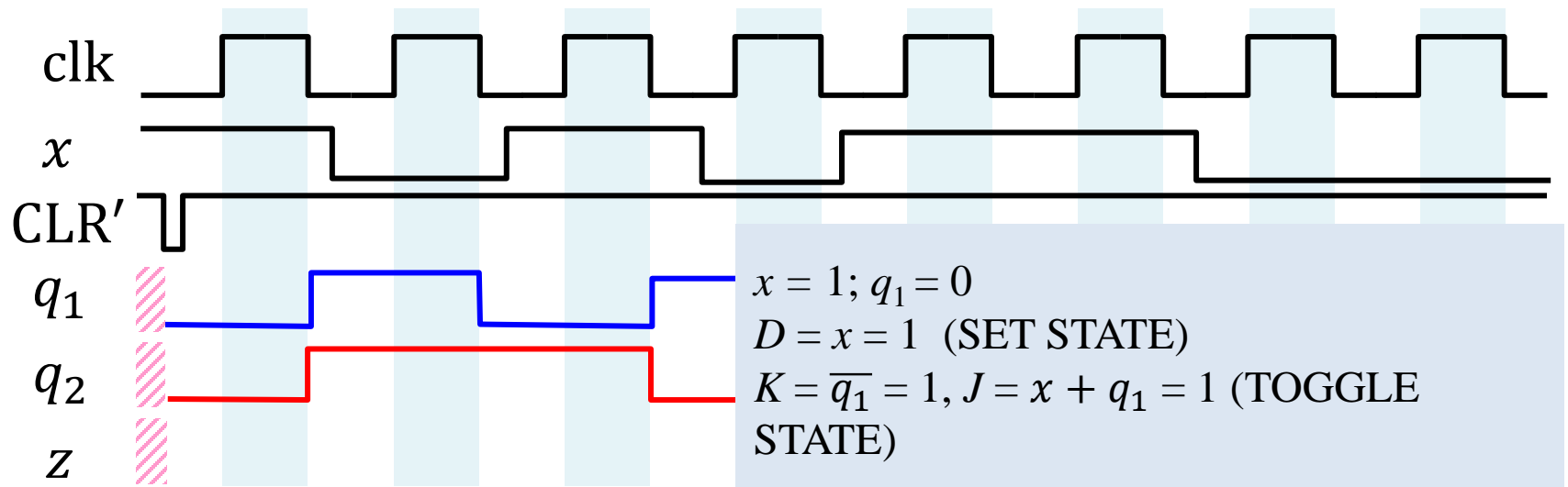
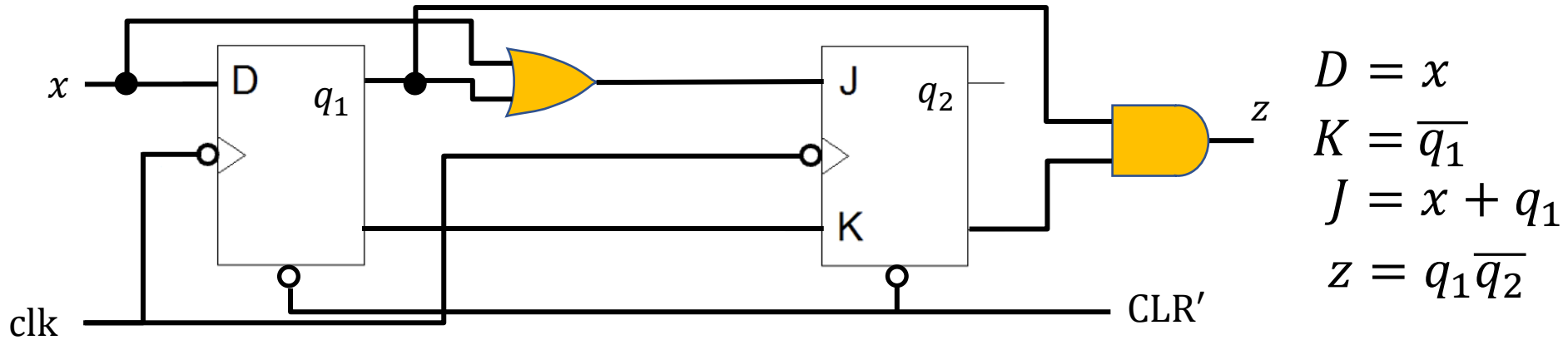
Exercise



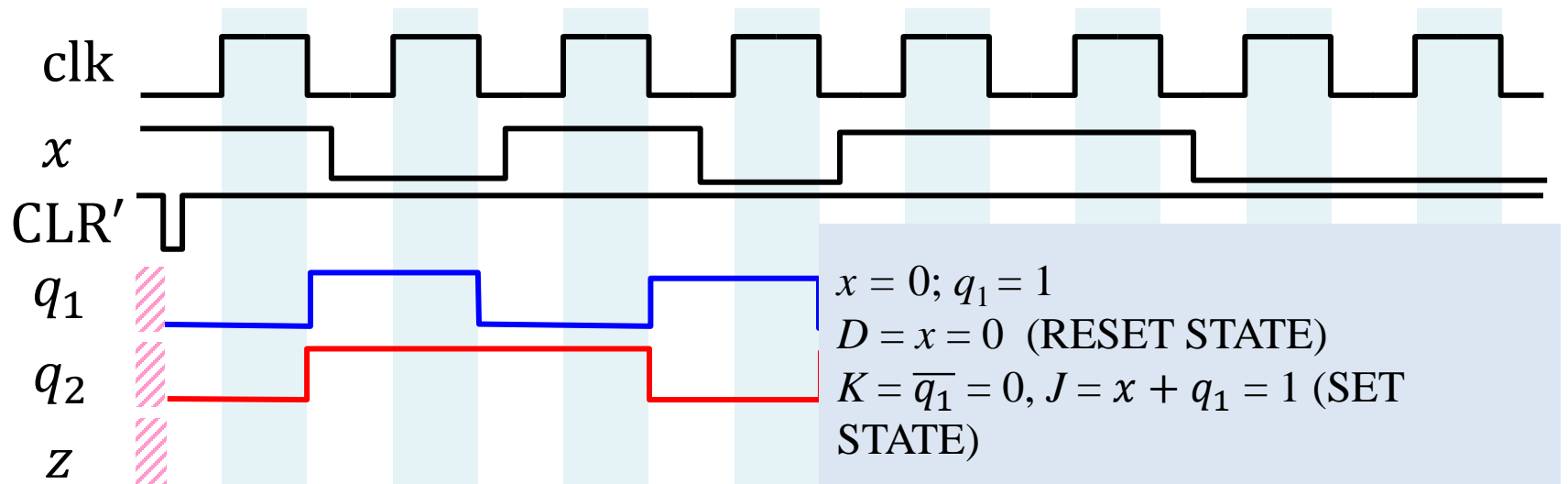
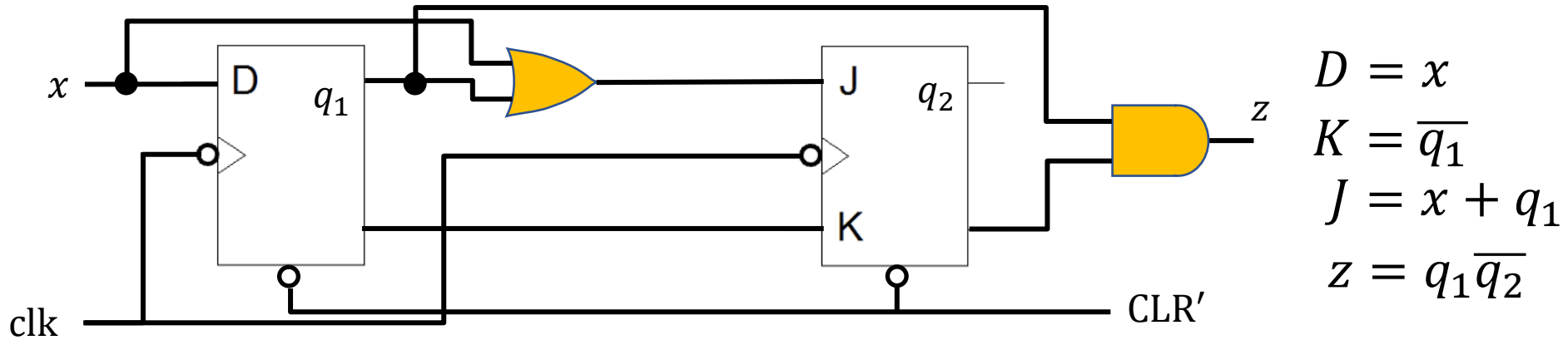
Exercise



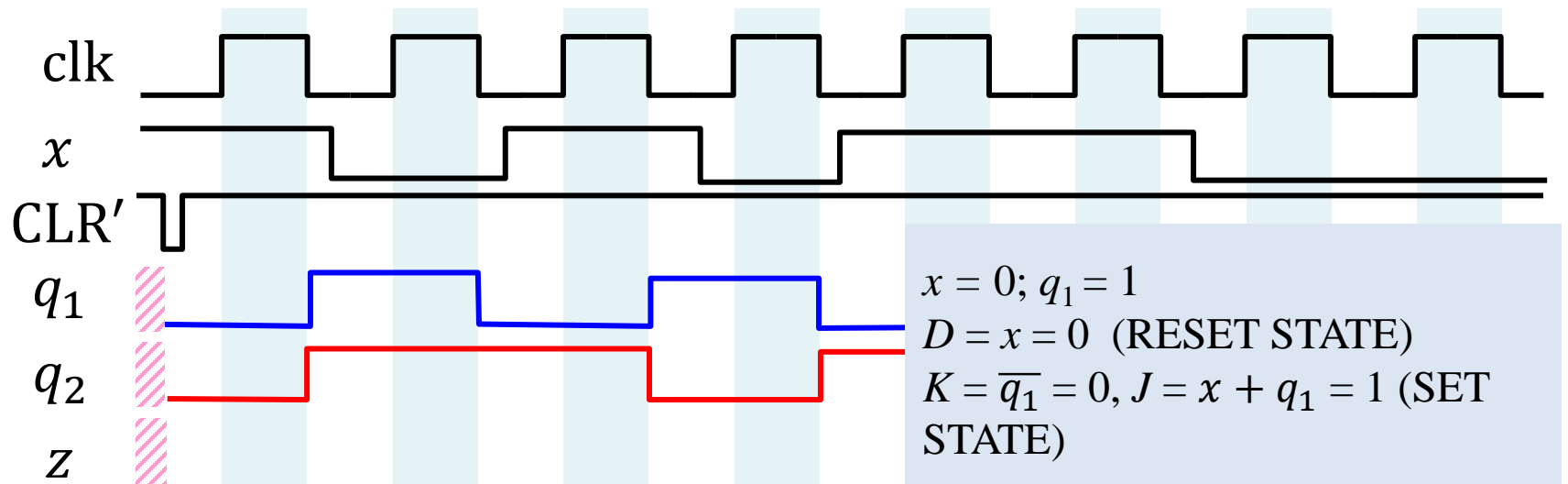
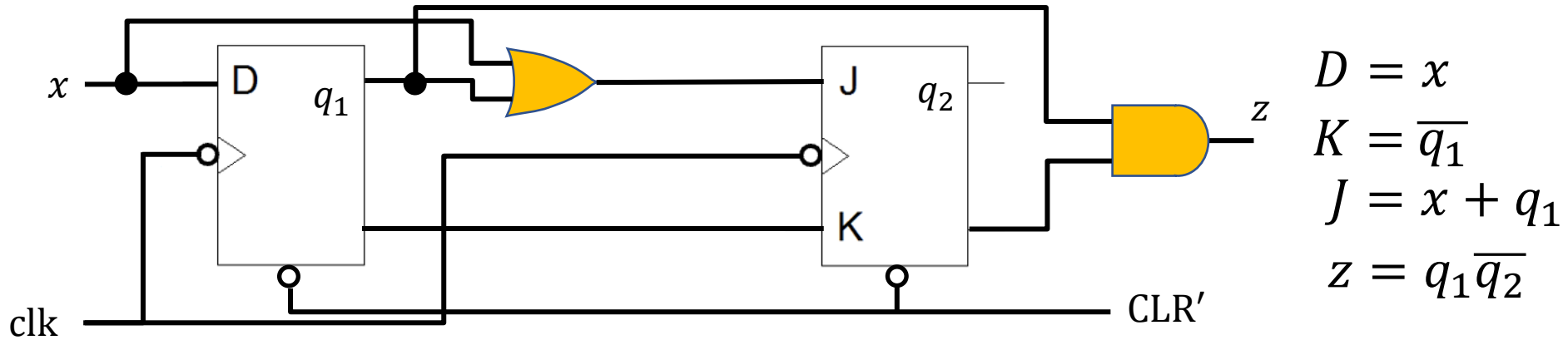
Exercise



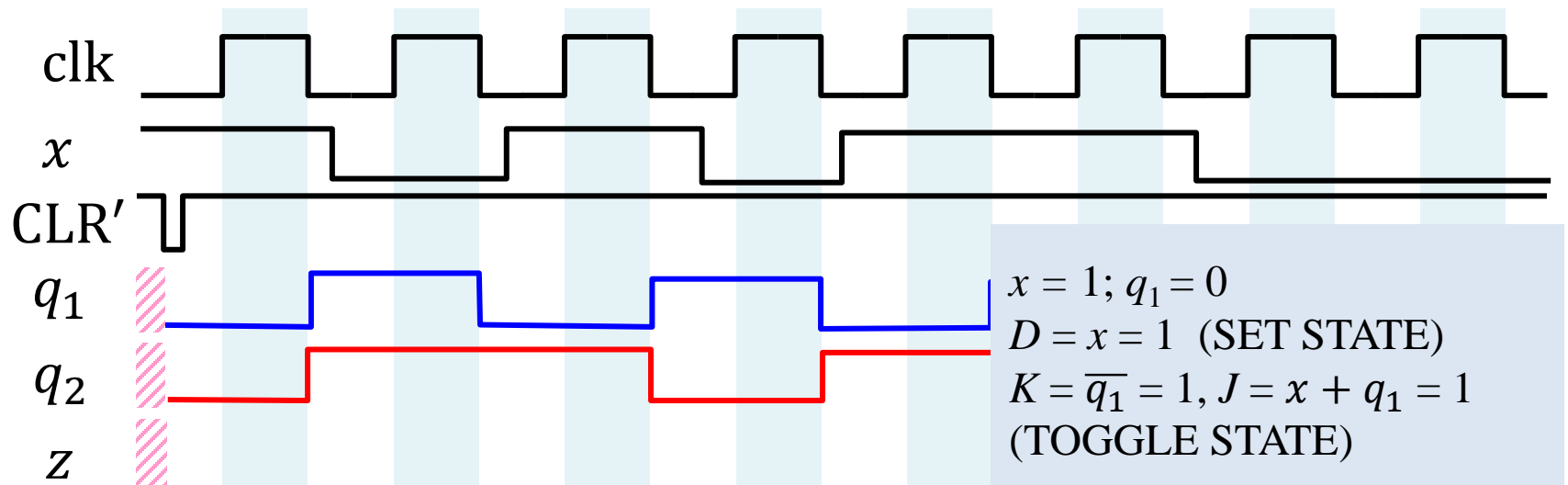
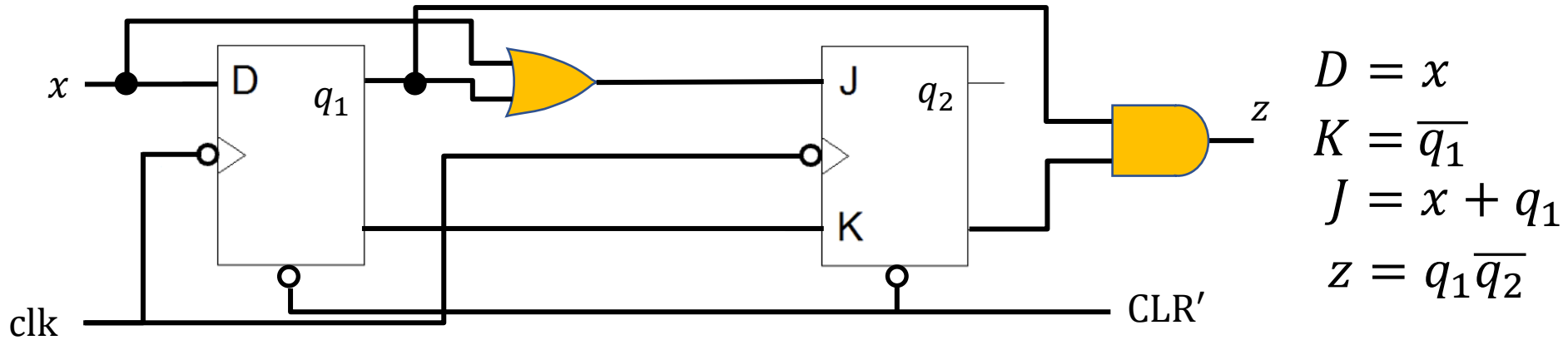
Exercise



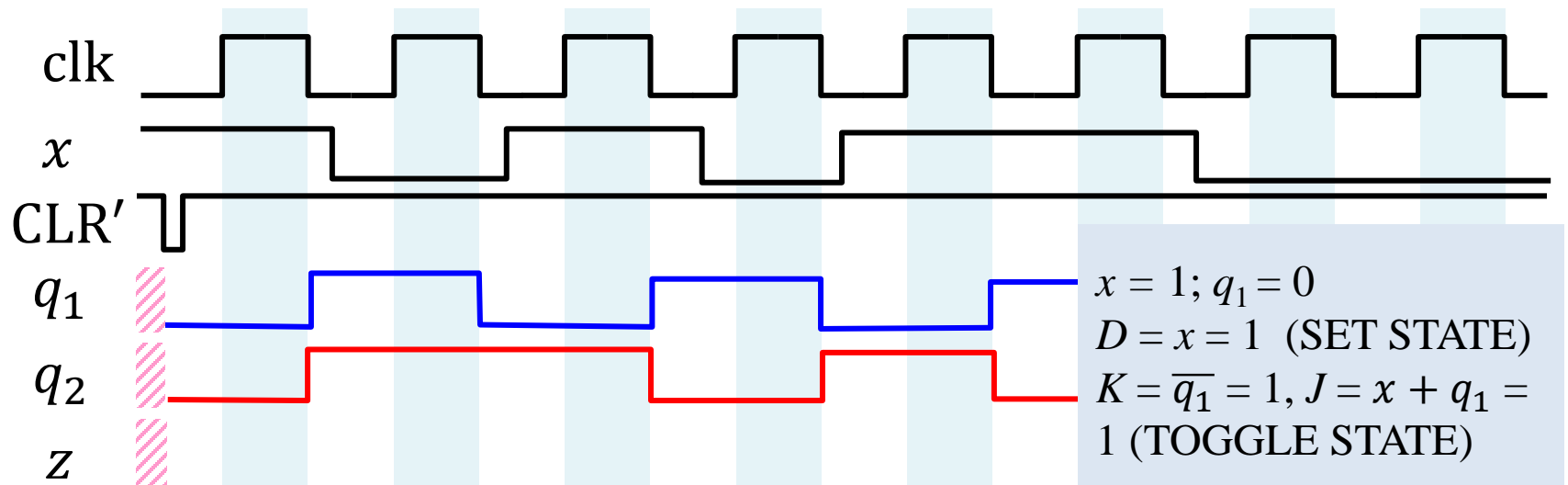
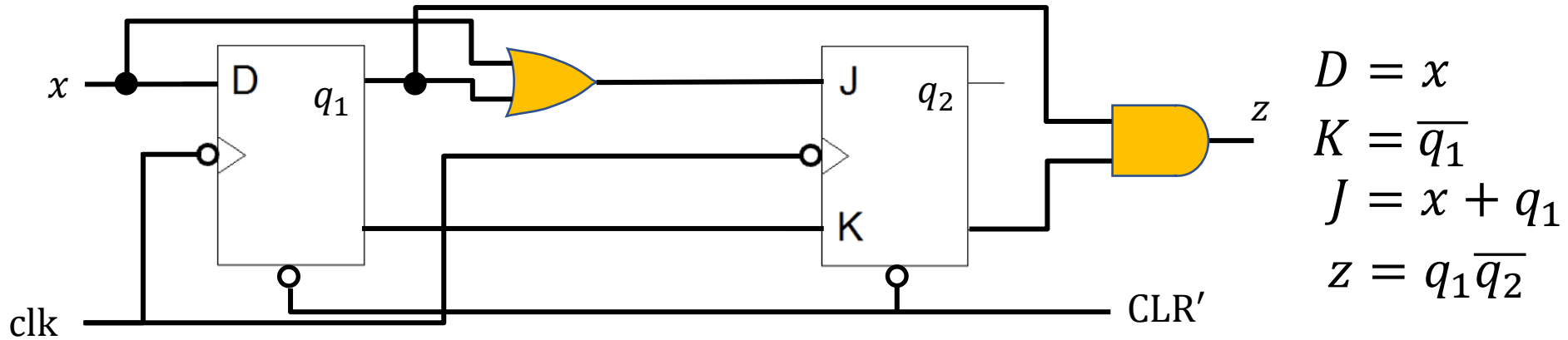
Exercise



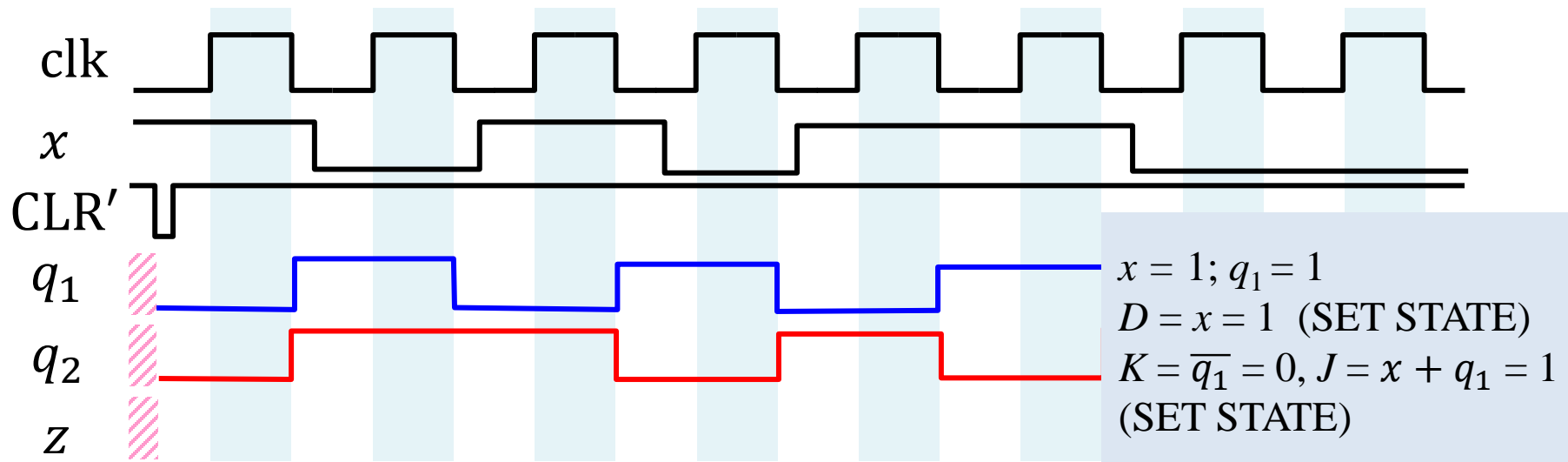
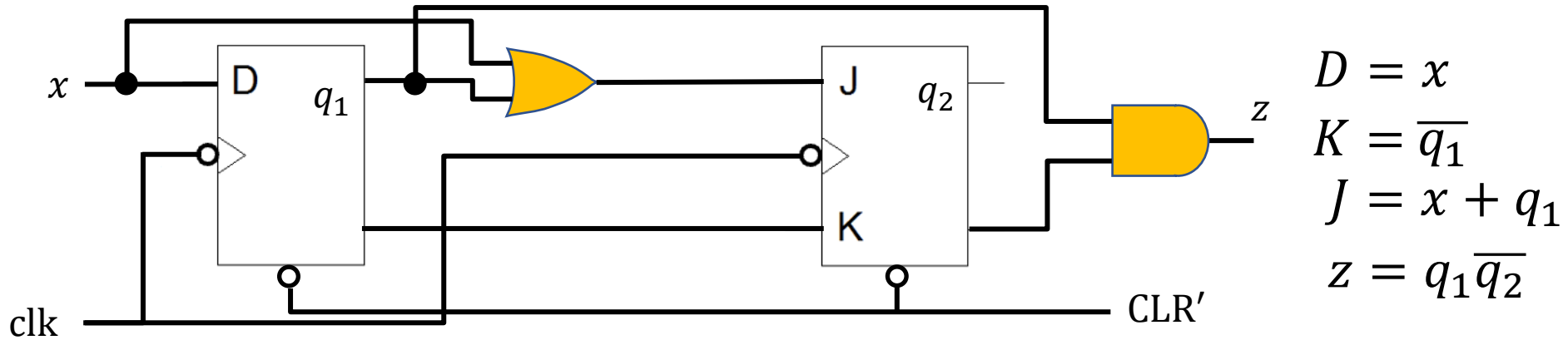
Exercise



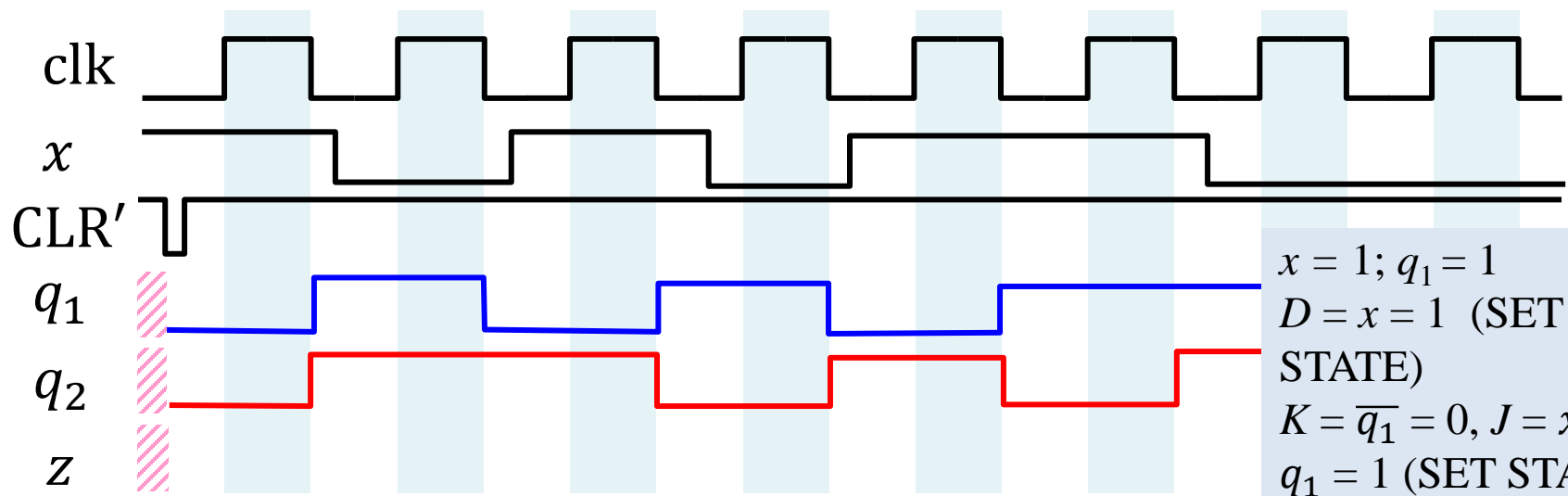
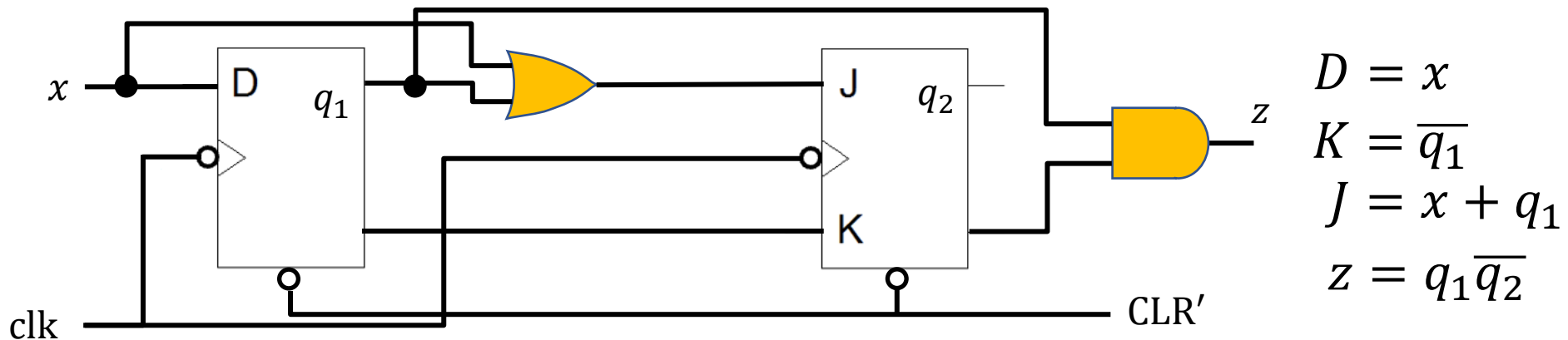
Exercise



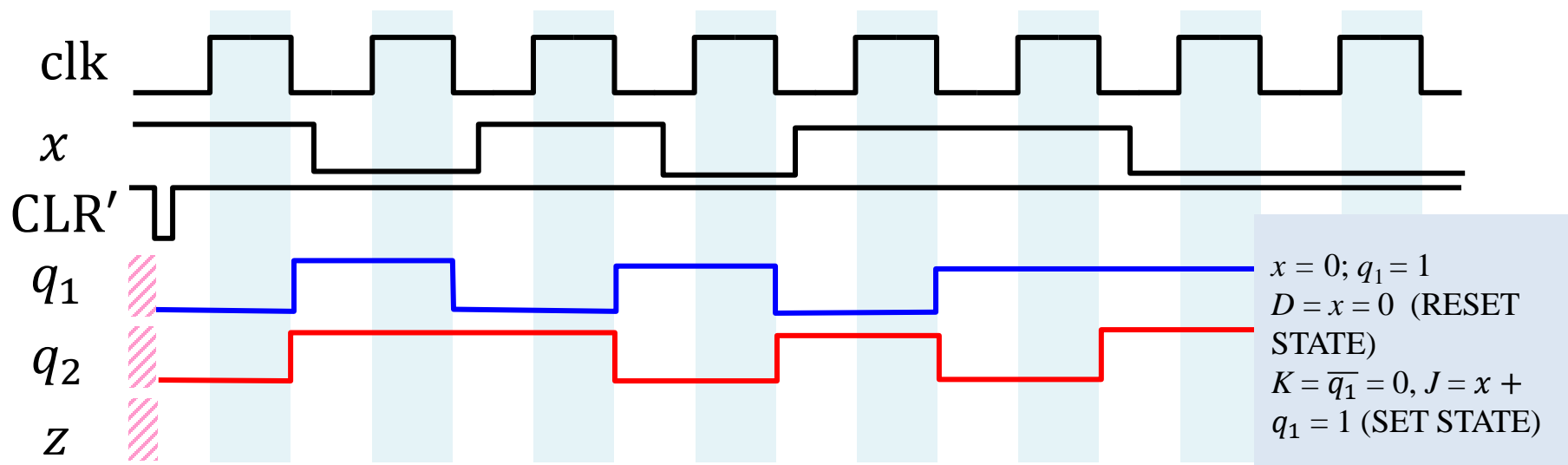
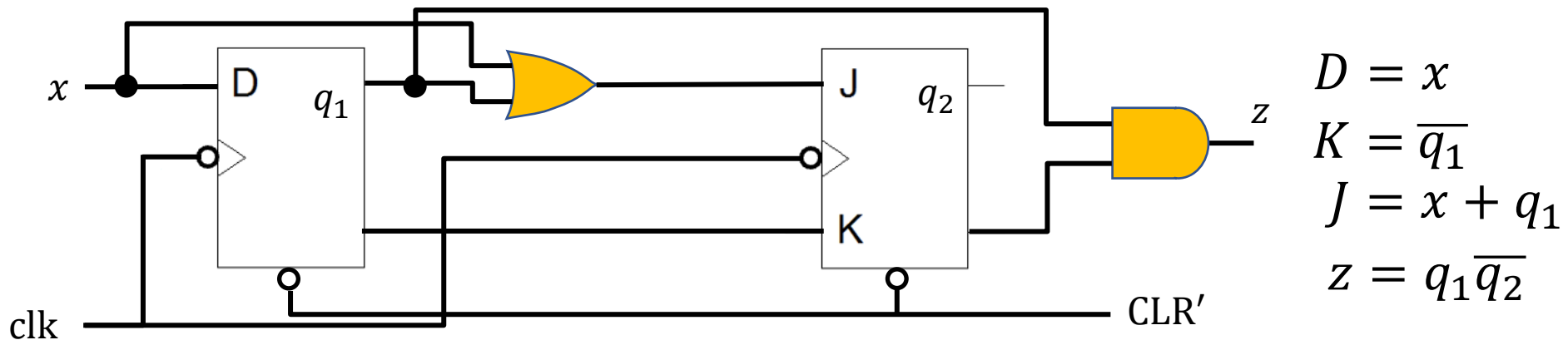
Exercise



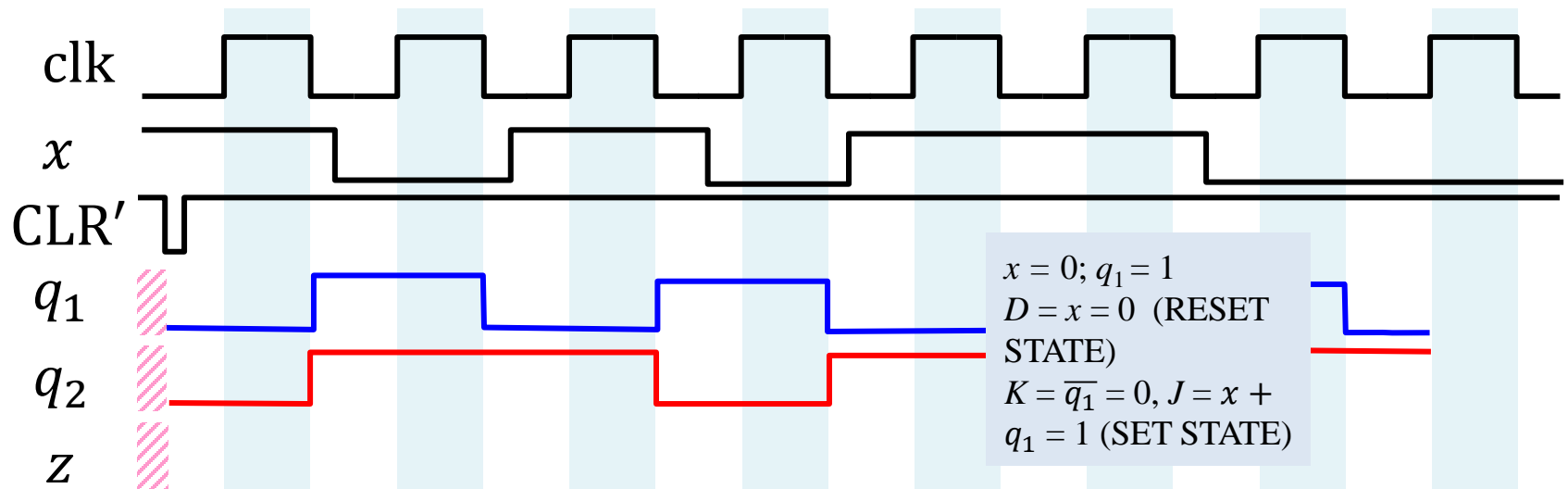
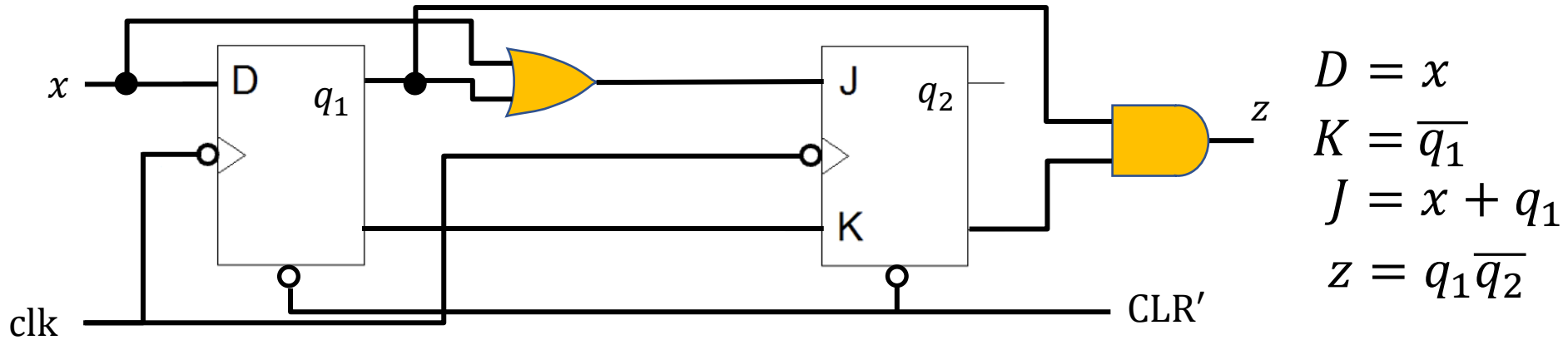
Exercise



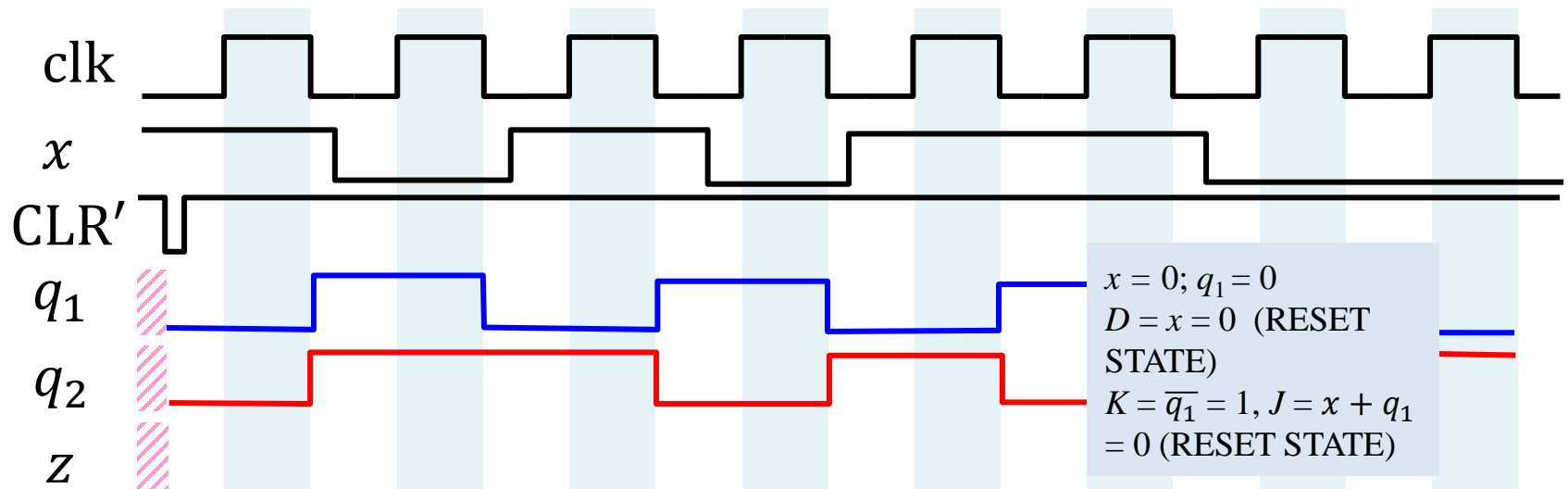
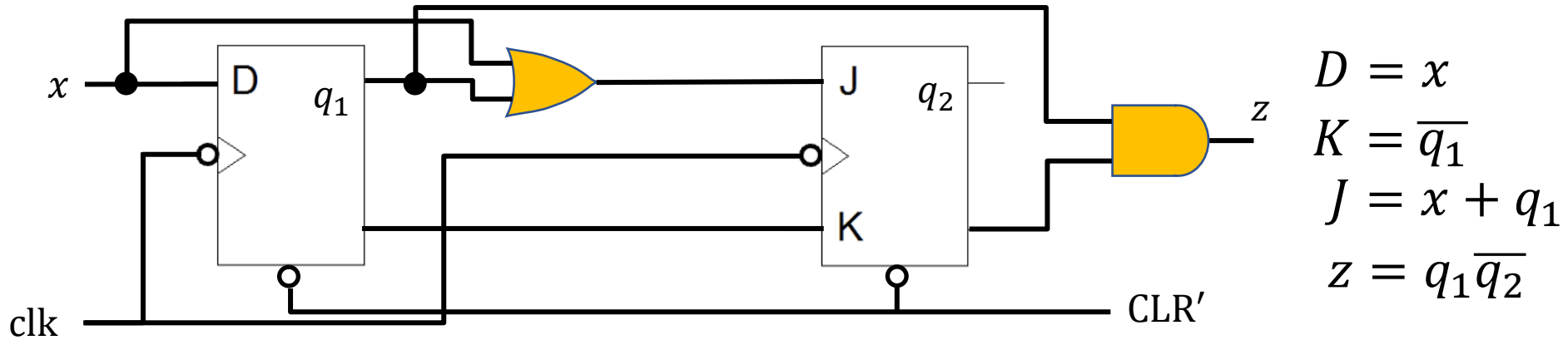
Exercise



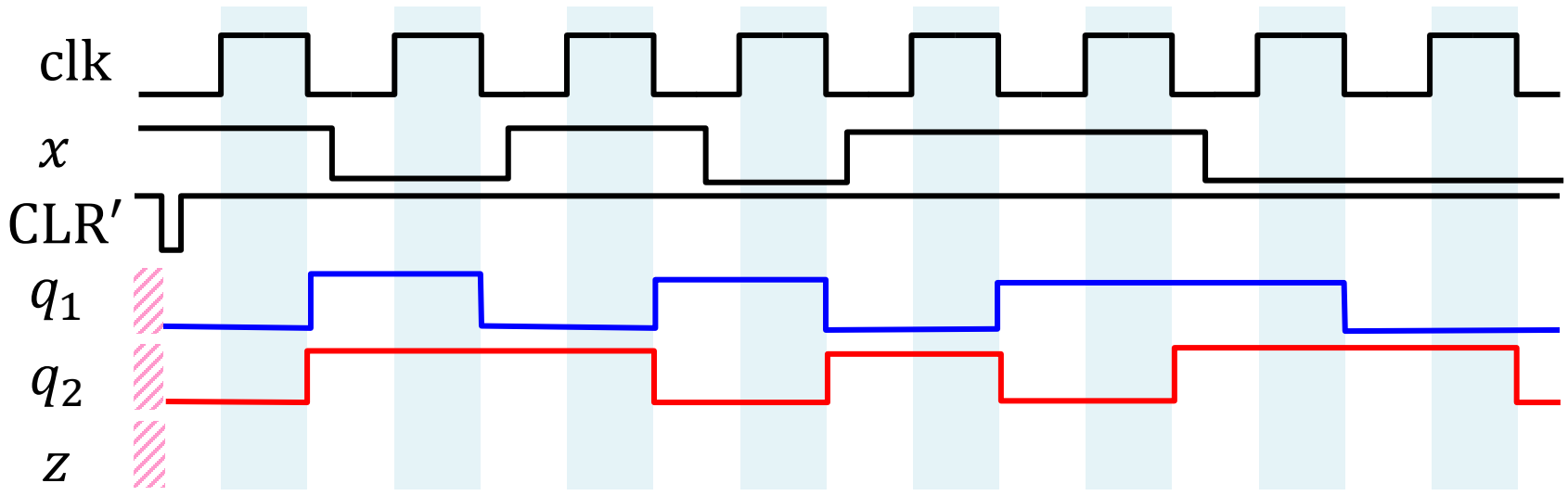
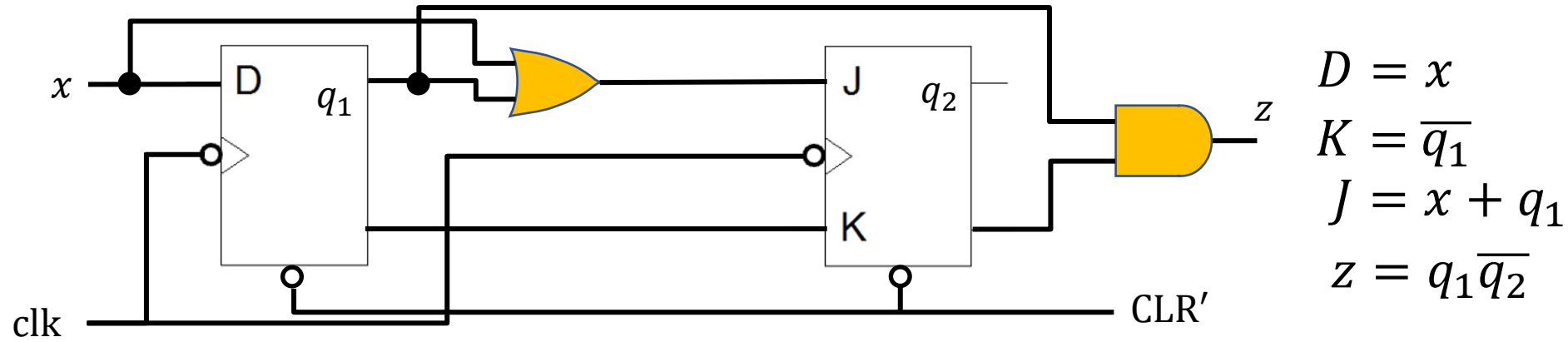
Exercise



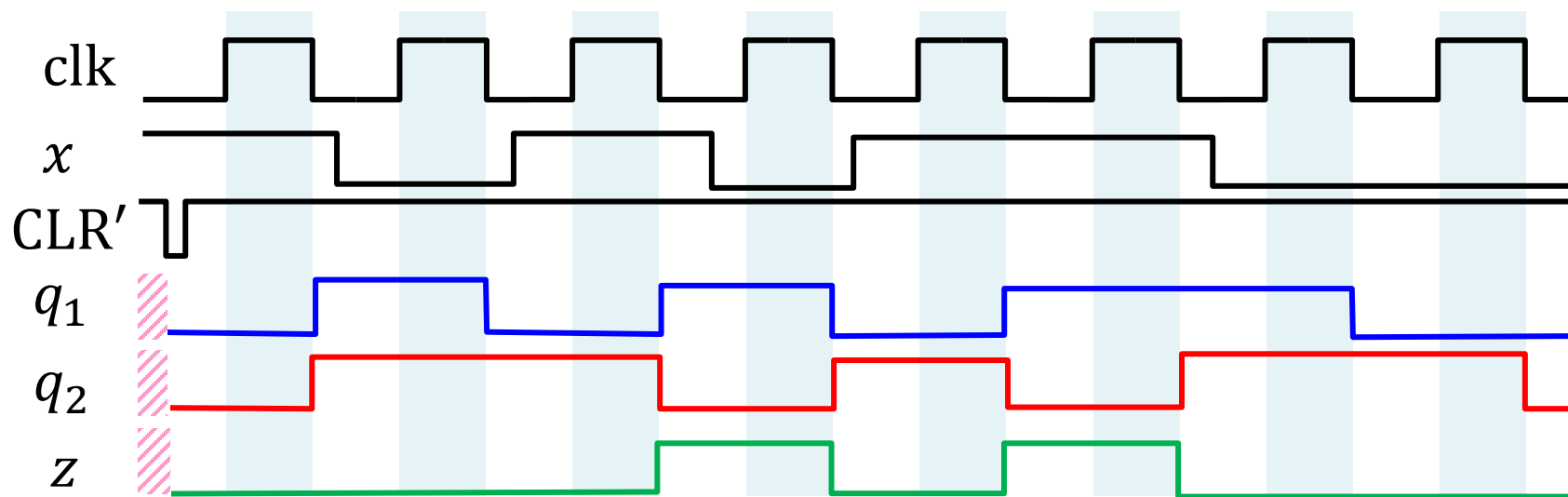
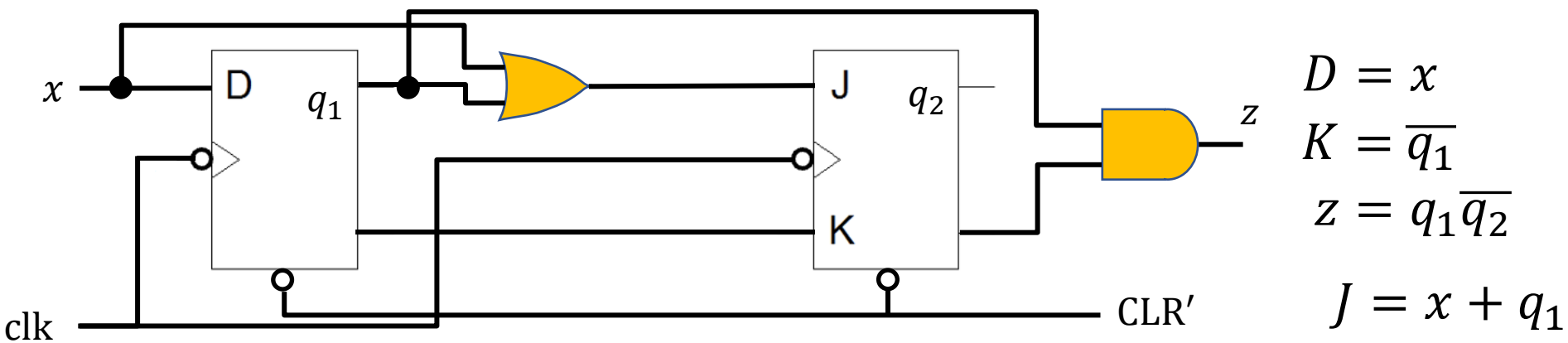
Exercise



Exercise

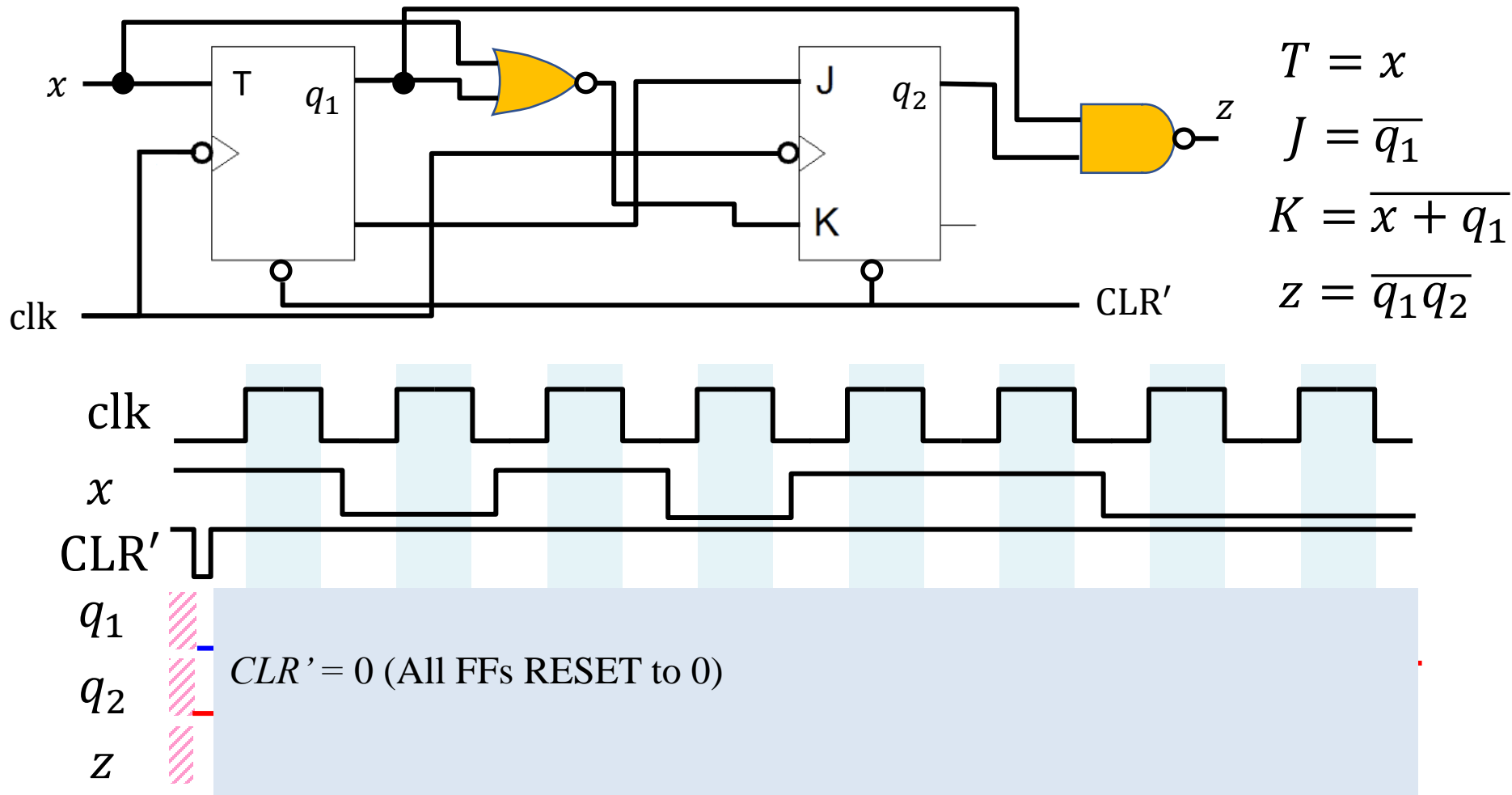


Exercise

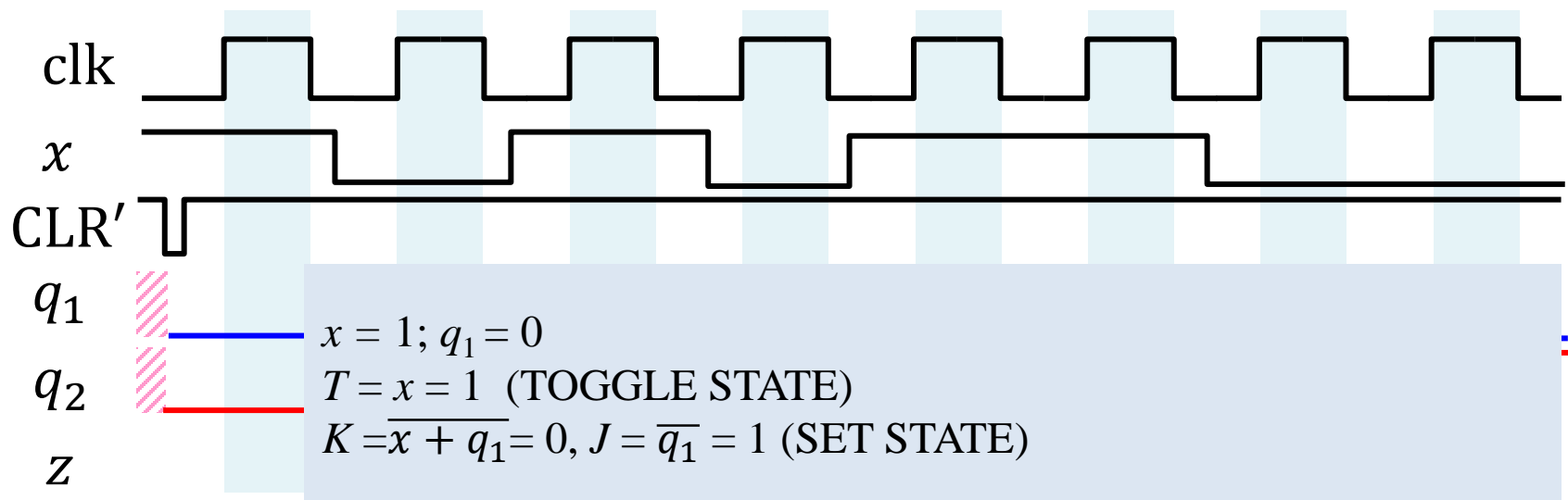
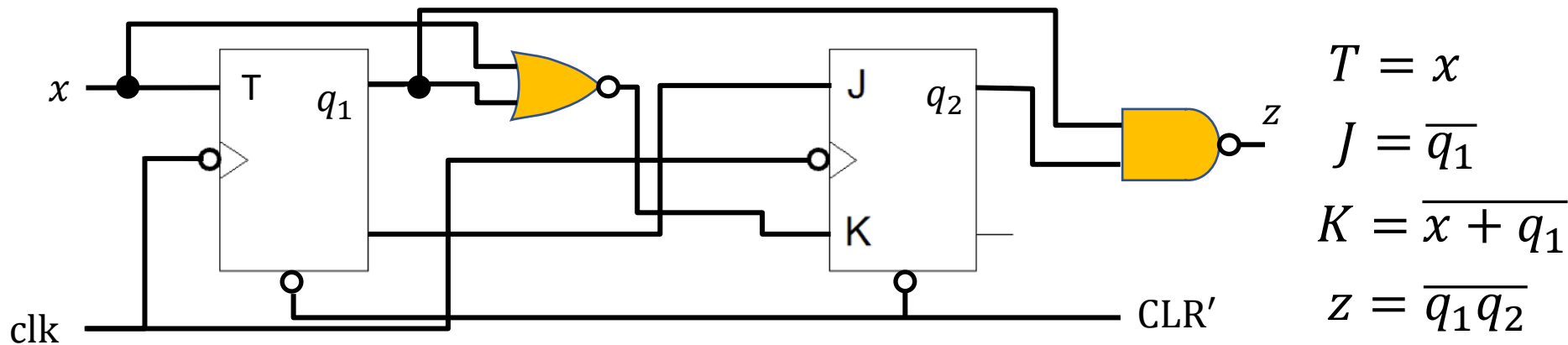


Final Step: Work out z

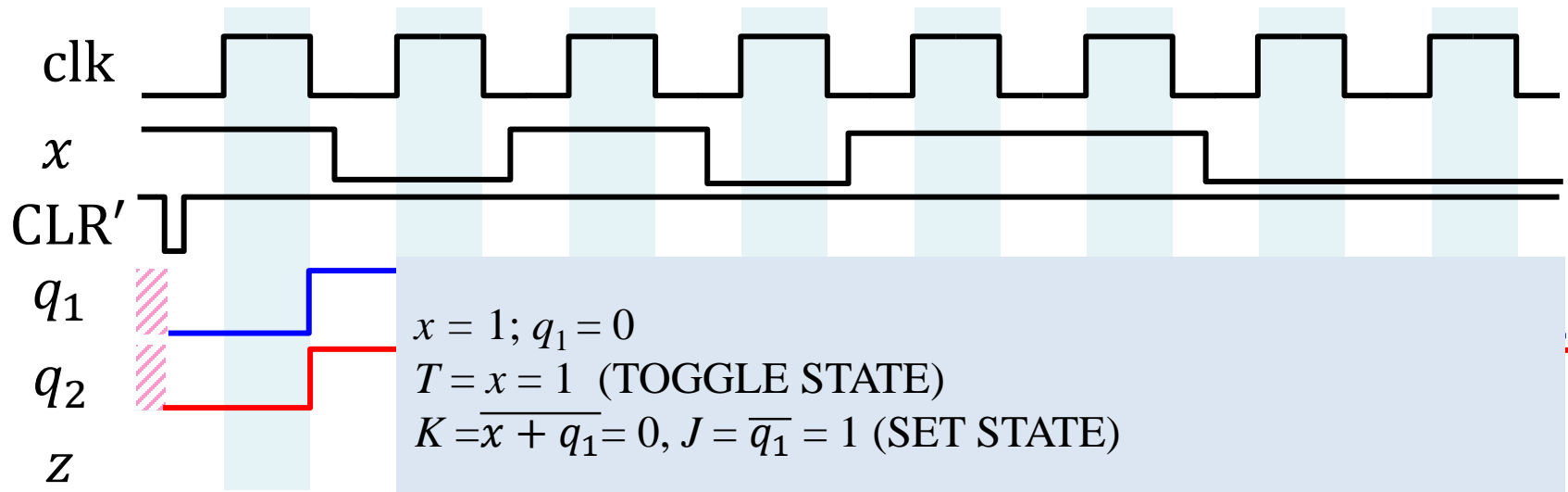
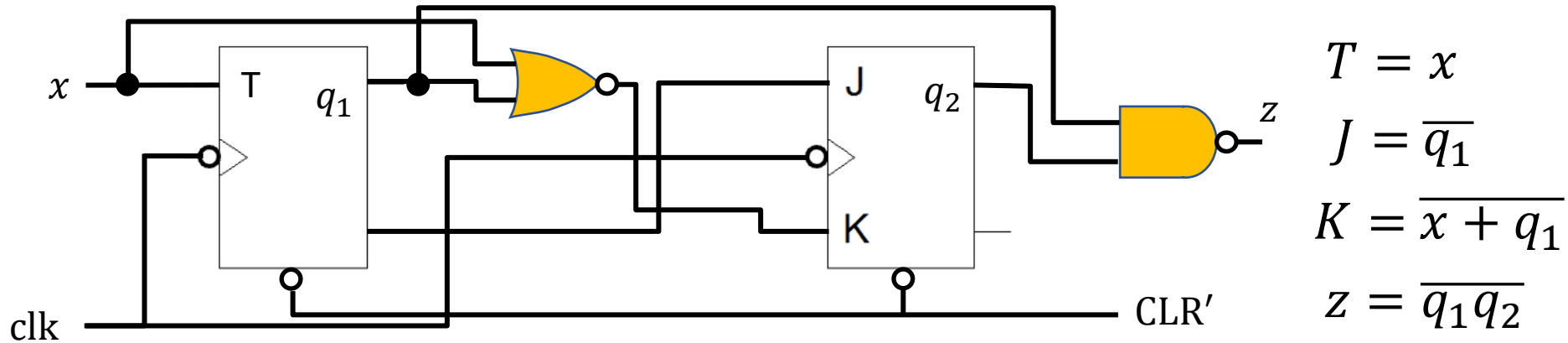
Exercise



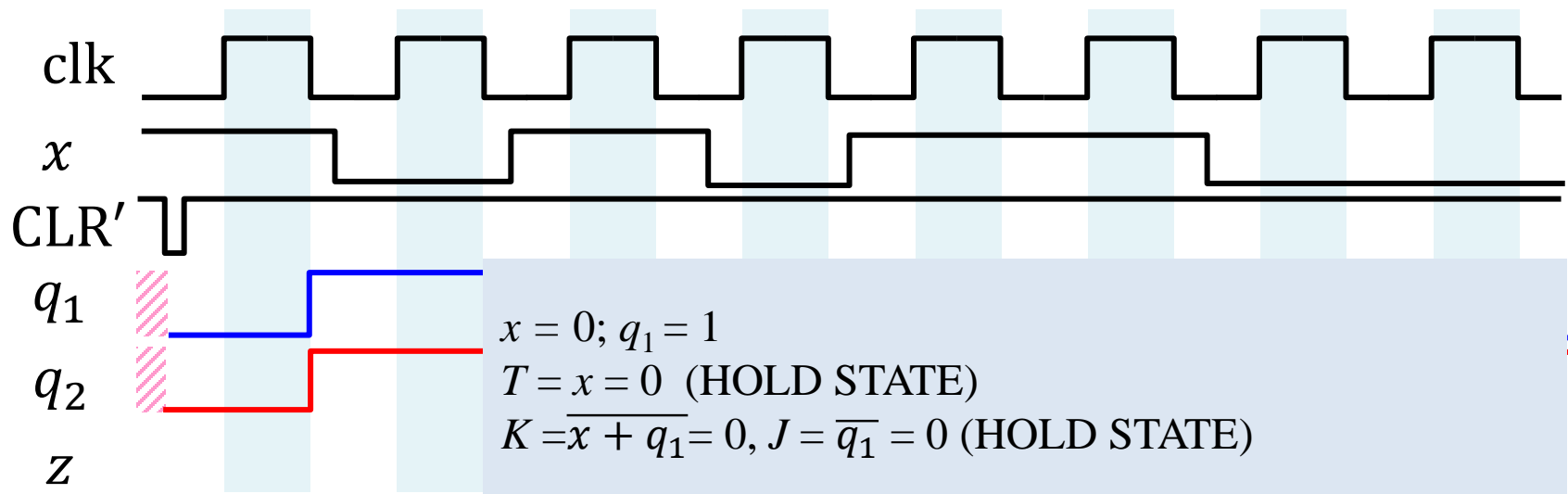
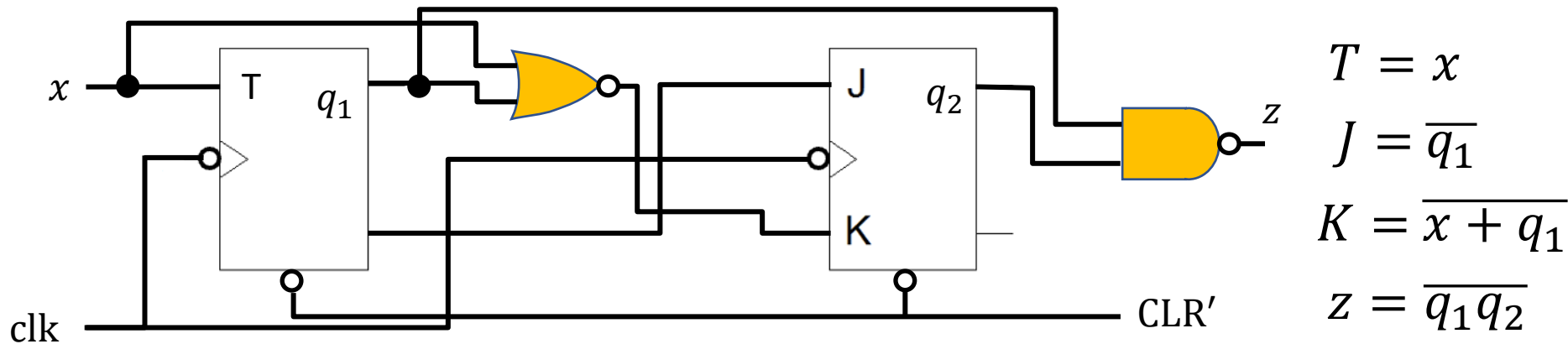
Exercise



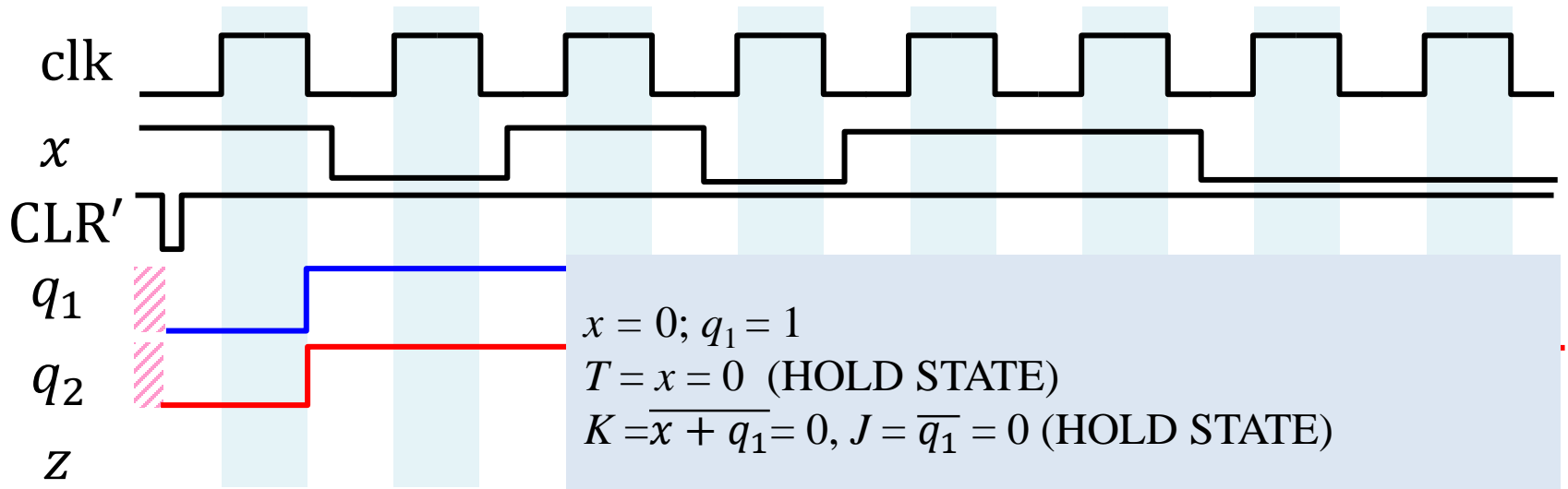
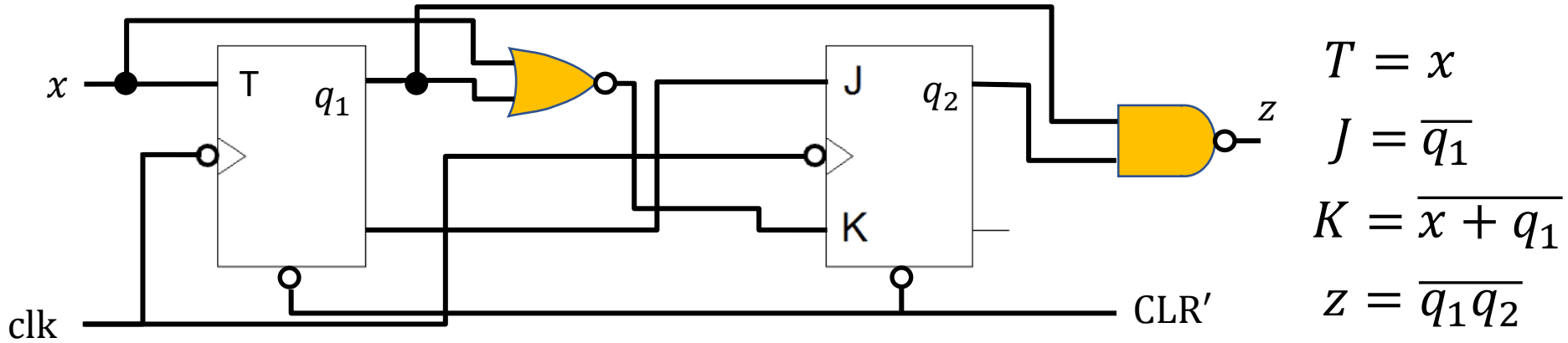
Exercise



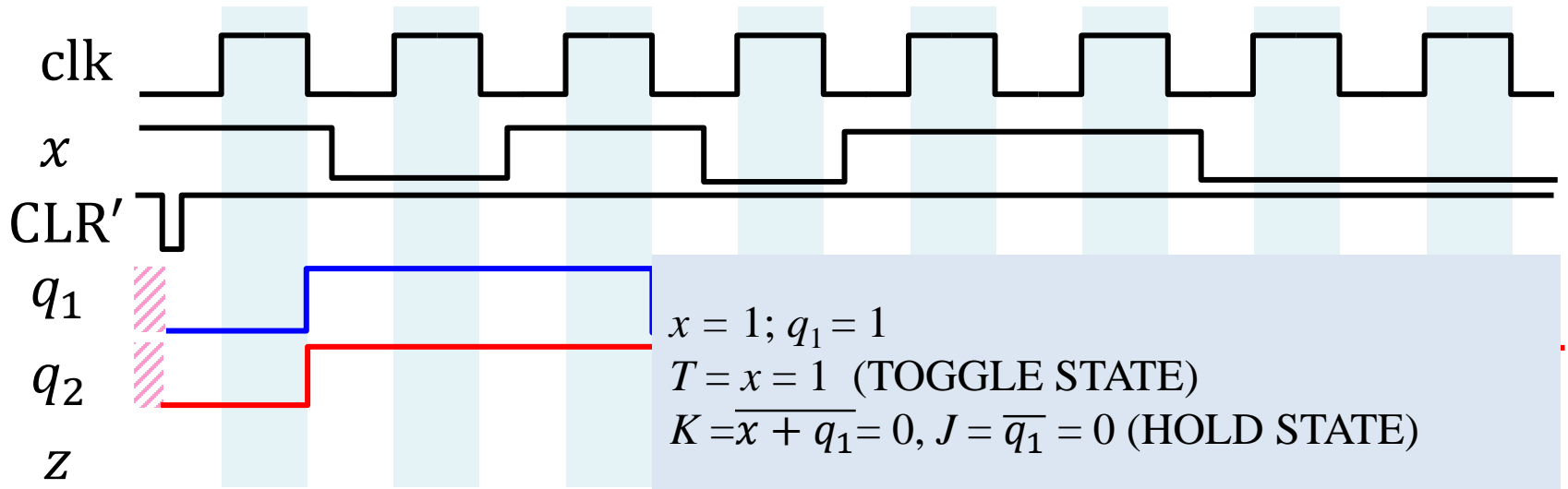
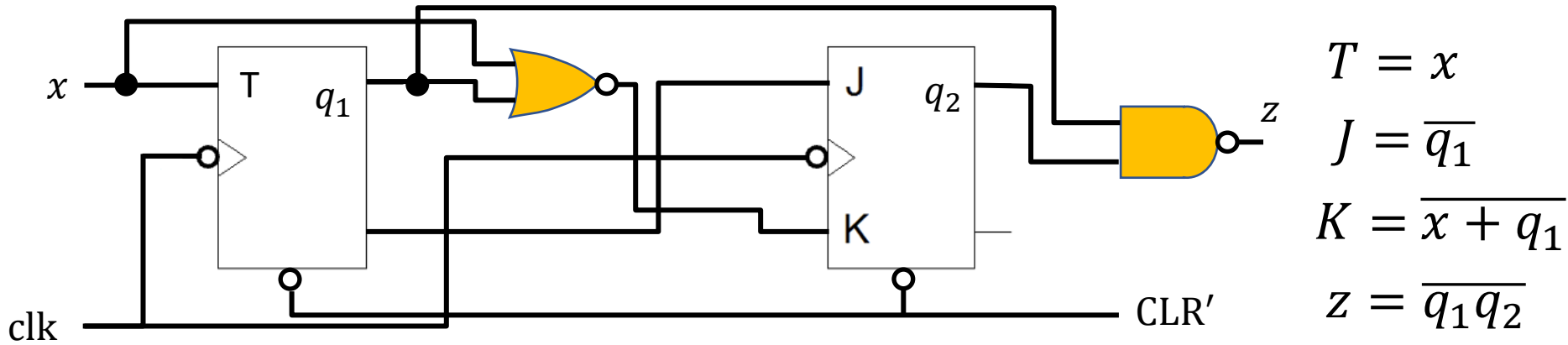
Exercise



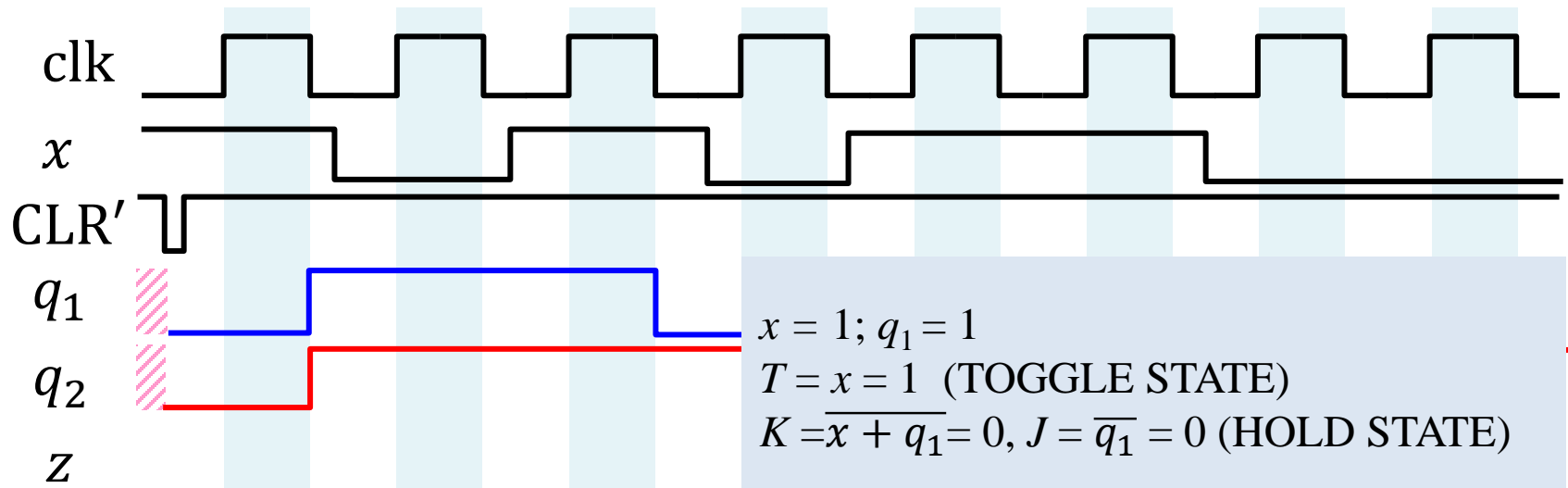
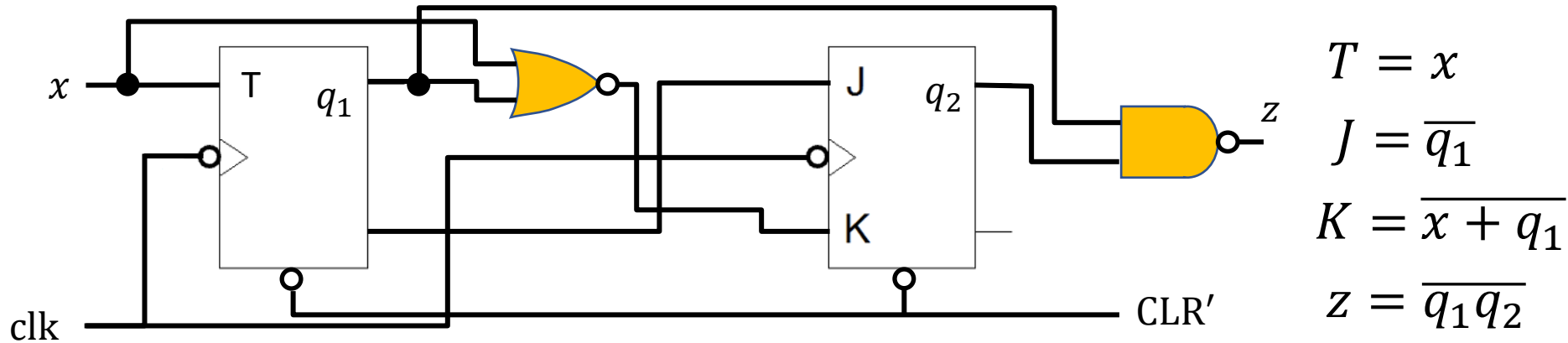
Exercise



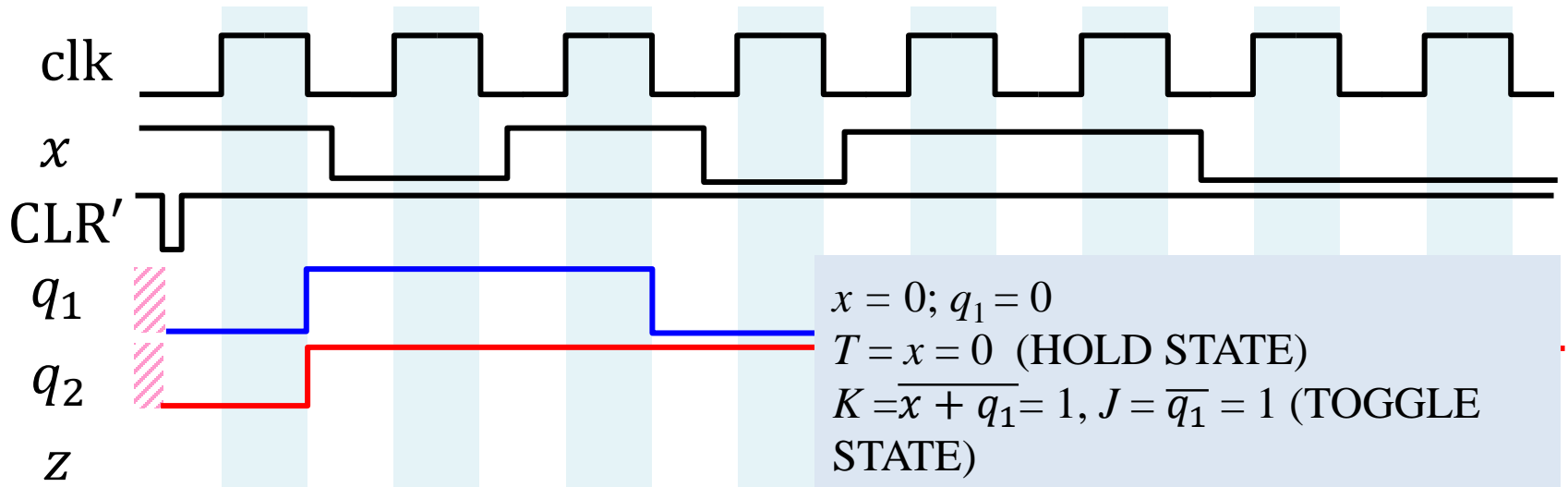
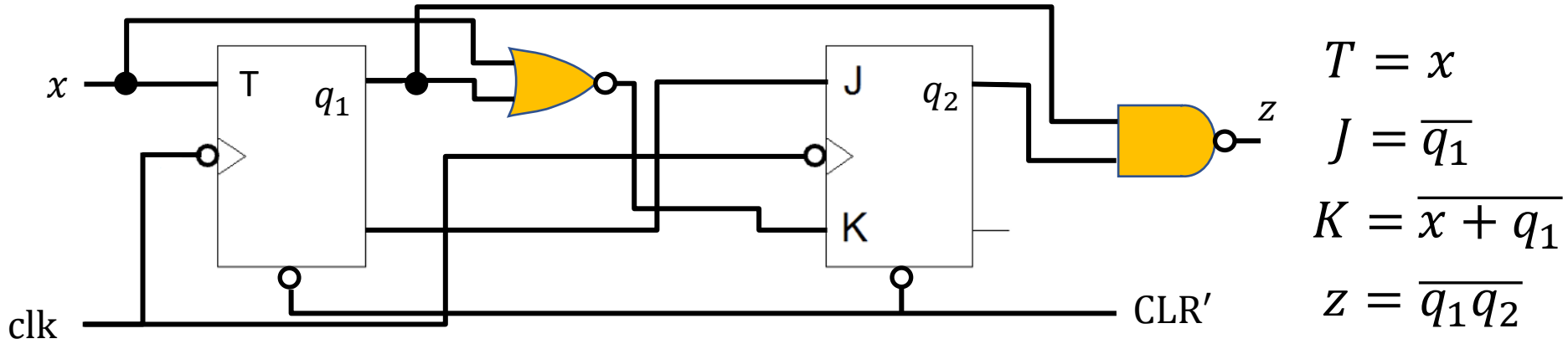
Exercise



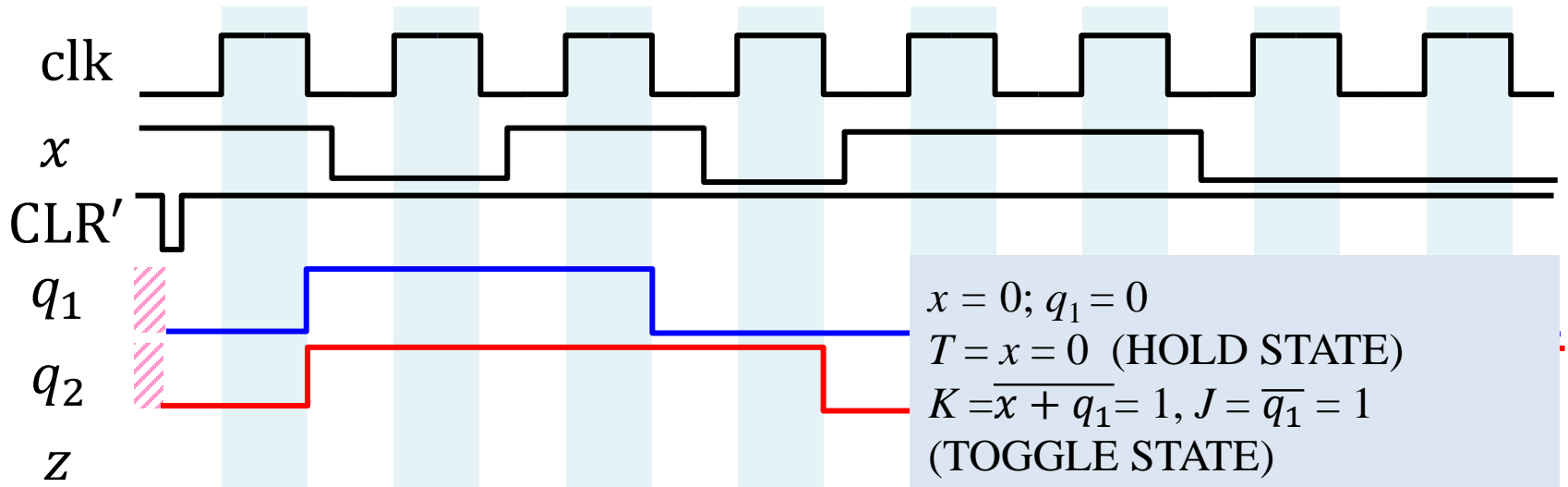
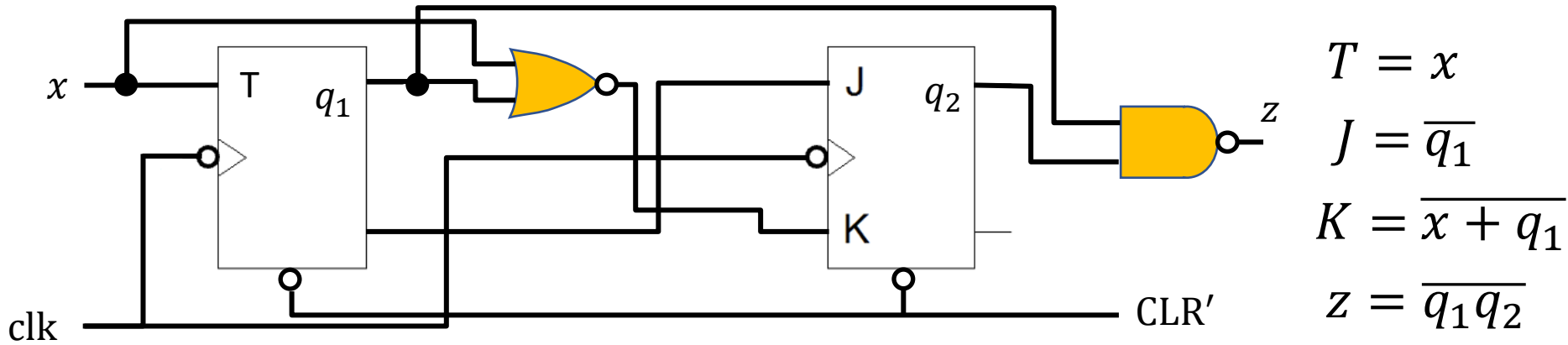
Exercise



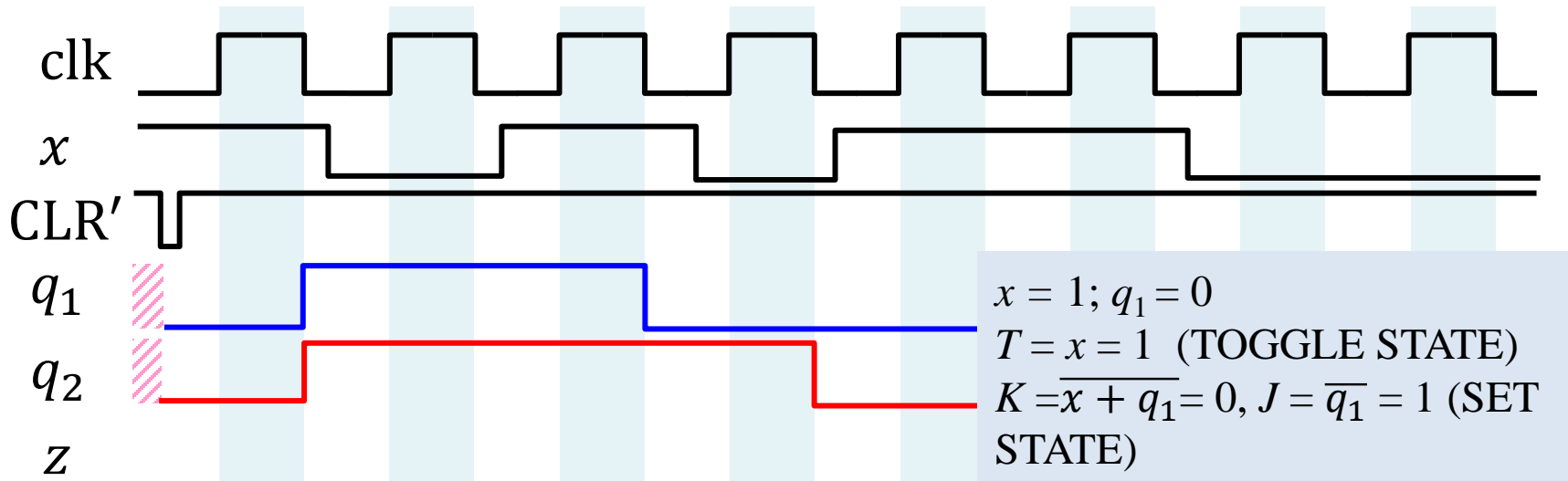
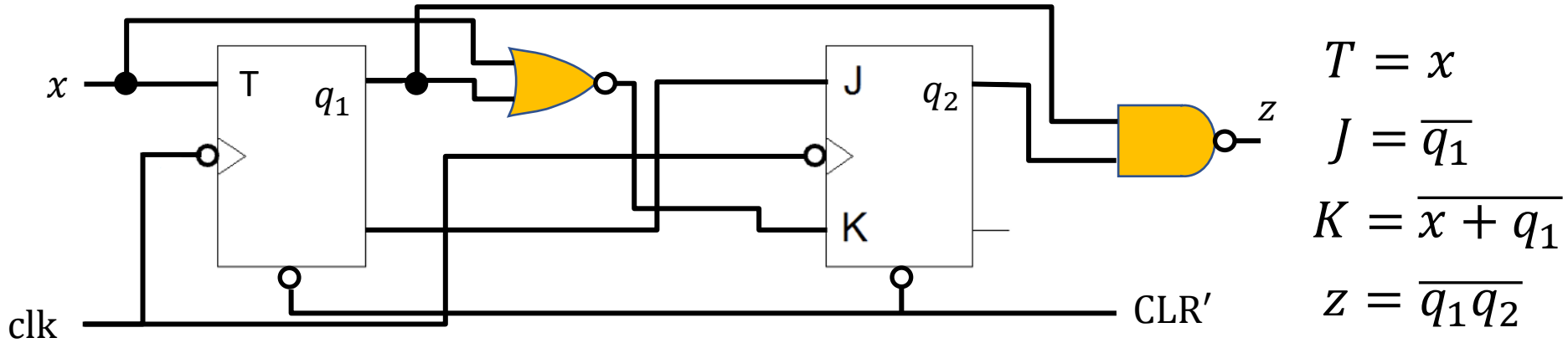
Exercise



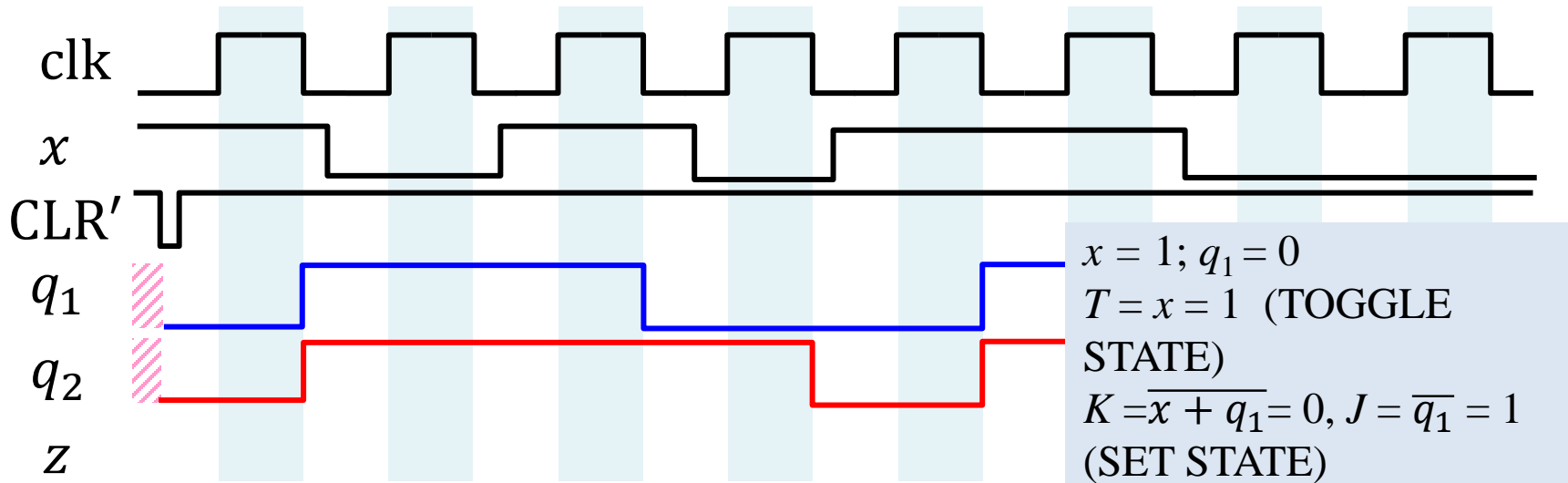
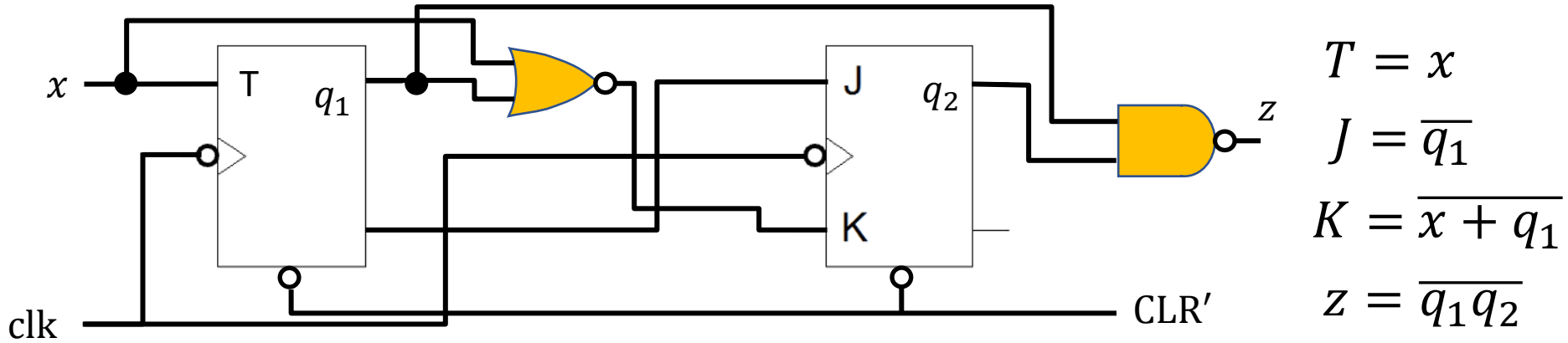
Exercise



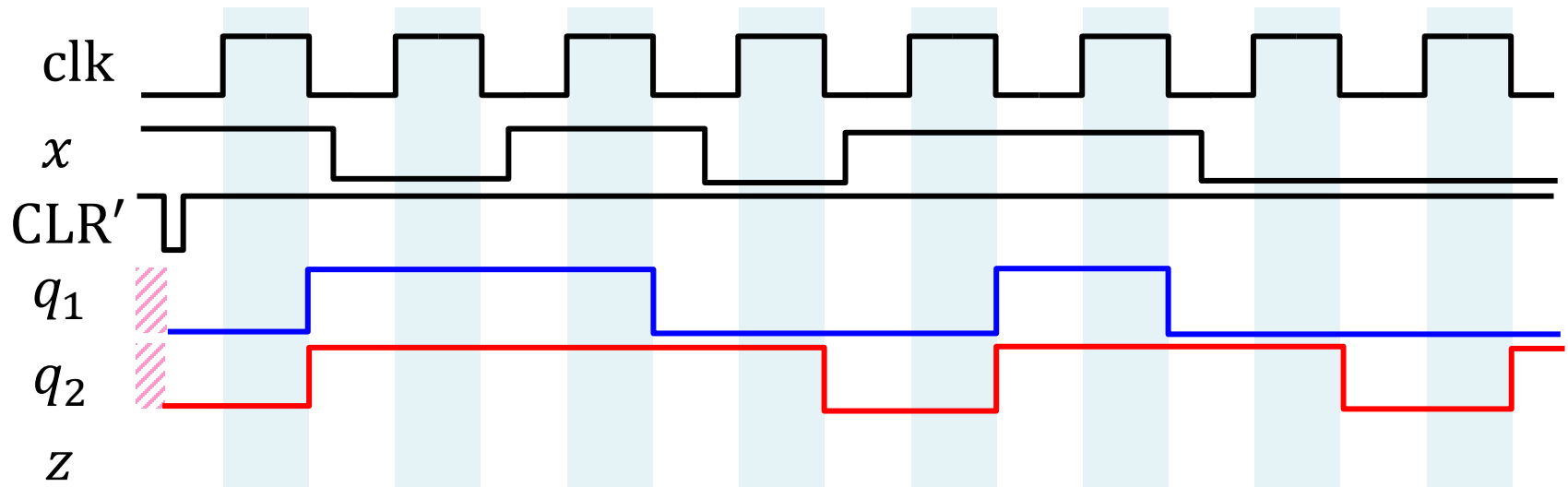
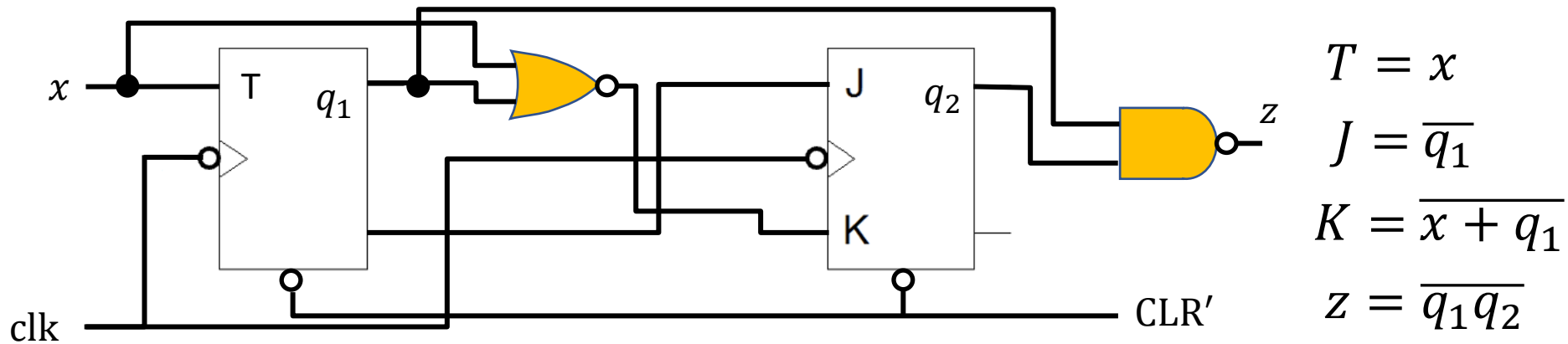
Exercise



Exercise



Exercise



Exercise

