测试结果说明

# 编译程序输出结果（目前只做到四元式）

LAB p\_proc1\_00543860

WS string\_1

NL

ADD n\_10, n\_9, t\_1

MUL t\_1, n\_8, t\_2

NEG t\_2, t\_3

ADD t\_3, n\_1024, t\_4

ASS t\_4, v\_int1\_00543658

ASS n\_65, v\_ch1\_00543728

ASS n\_48, v\_ch2\_00543790

WS string\_2

WI v\_int1\_00543658

NL

WS string\_3

WI v\_ch1\_00543728

NL

WS string\_4

WI v\_ch2\_00543790

NL

WS string\_5

NL

ASS n\_0, v\_int2\_005436C0

LAB loop\_for\_start\_label\_1

CMP v\_int2\_005436C0, n\_0

JL loop\_for\_end\_label\_2

ARAS a\_chArray\_005437F8, v\_int2\_005436C0, n\_48

ASAR a\_chArray\_005437F8, v\_int2\_005436C0, t\_5

WI t\_5

NL

ADD v\_int2\_005436C0, n\_-1, t\_6

ASS t\_6, v\_int2\_005436C0

JMP loop\_for\_start\_label\_1

LAB loop\_for\_end\_label\_2

WS string\_6

NL

ASS n\_0, v\_int2\_005436C0

LAB loop\_for\_start\_label\_3

CMP v\_int2\_005436C0, n\_2

JG loop\_for\_end\_label\_4

ARAS a\_intArray\_005435F0, v\_int2\_005436C0, v\_int2\_005436C0

ASAR a\_intArray\_005435F0, v\_int2\_005436C0, t\_7

WI t\_7

NL

ADD v\_int2\_005436C0, n\_1, t\_8

ASS t\_8, v\_int2\_005436C0

JMP loop\_for\_start\_label\_3

LAB loop\_for\_end\_label\_4

WS string\_7

NL

RTN

LAB p\_mov\_00548EA0

CMP v\_num\_00549040, n\_0

JLE condition\_else\_label\_5

PUSH v\_tstart\_00548F08

PUSH v\_tmid\_00548FD8

PUSH v\_tend\_00548F70

SUB v\_num\_00549040, n\_1, t\_9

PUSH t\_9

PUSH v\_count\_005490A8

CALP p\_mov\_00548EA0

ADD v\_count\_005490A8, n\_1, t\_10

ASS t\_10, v\_count\_005490A8

WS string\_8

WI v\_count\_005490A8

NL

WS string\_9

WI v\_tstart\_00548F08

NL

WS string\_10

WI v\_tend\_00548F70

NL

PUSH v\_tmid\_00548FD8

PUSH v\_tend\_00548F70

PUSH v\_tstart\_00548F08

SUB v\_num\_00549040, n\_1, t\_11

PUSH t\_11

PUSH v\_count\_005490A8

CALP p\_mov\_00548EA0

JMP condition\_end\_label\_6

LAB condition\_else\_label\_5

LAB condition\_end\_label\_6

RTN

LAB f\_func1\_00548D10

WS string\_11

NL

WS string\_12

NL

WI v\_num\_00548DD0

NL

WS string\_13

WI v\_num\_00548DD0

NL

ASS n\_0, v\_steps\_00548E38

PUSH n\_1

PUSH n\_3

PUSH n\_2

PUSH v\_num\_00548DD0

PUSH v\_steps\_00548E38

CALP p\_mov\_00548EA0

WS string\_14

WI v\_steps\_00548E38

NL

WS string\_15

WI v\_steps\_00548E38

NL

FAS v\_steps\_00548E38, f\_func1\_00548D10

RTN

LAB f\_func2\_00549110

WS string\_16

NL

WS string\_17

NL

RI v\_ch1\_00543728

RI v\_ch2\_00543790

RI v\_int1\_00543658

CMP v\_ch1\_00543728, v\_ch2\_00543790

JG condition\_else\_label\_7

CMP v\_ch1\_00543728, v\_ch2\_00543790

JGE condition\_else\_label\_9

CMP v\_int1\_00543658, n\_0

JNE condition\_else\_label\_11

WS string\_18

NL

JMP condition\_end\_label\_12

LAB condition\_else\_label\_11

WS string\_19

NL

LAB condition\_end\_label\_12

JMP condition\_end\_label\_10

LAB condition\_else\_label\_9

CMP v\_int1\_00543658, n\_0

JL condition\_else\_label\_13

CMP v\_int1\_00543658, n\_0

JLE condition\_else\_label\_15

WS string\_20

NL

JMP condition\_end\_label\_16

LAB condition\_else\_label\_15

WS string\_21

NL

LAB condition\_end\_label\_16

JMP condition\_end\_label\_14

LAB condition\_else\_label\_13

WS string\_22

NL

LAB condition\_end\_label\_14

LAB condition\_end\_label\_10

JMP condition\_end\_label\_8

LAB condition\_else\_label\_7

CMP v\_int1\_00543658, n\_0

JE condition\_else\_label\_17

WS string\_23

NL

JMP condition\_end\_label\_18

LAB condition\_else\_label\_17

WS string\_24

NL

LAB condition\_end\_label\_18

LAB condition\_end\_label\_8

WS string\_25

WC n\_48

NL

FAS n\_48, f\_func2\_00549110

RTN

LAB p\_proc2\_00549178

WS string\_26

NL

WS string\_27

NL

WI v\_int1\_005491E0

NL

WI v\_int2\_00549248

NL

WI v\_int3\_005492B0

NL

ASS n\_0, v\_int3\_005492B0

LAB loop\_do\_while\_label\_19

ADD v\_int3\_005492B0, v\_int2\_00549248, t\_12

ASS t\_12, v\_int3\_005492B0

ADD v\_int1\_005491E0, n\_1, t\_13

ASS t\_13, v\_int1\_005491E0

CMP v\_int1\_005491E0, v\_int2\_00549248

JL loop\_do\_while\_label\_19

WS string\_28

WI v\_int3\_005492B0

NL

WS string\_29

NL

RTN

LAB \_main

CALP p\_proc1\_00543860

PUSH v\_int1\_00543658

PUSH v\_int2\_005436C0

MUL n\_3, n\_3, t\_14

MUL n\_2, n\_5, t\_15

SUB t\_14, t\_15, t\_16

ADD t\_16, n\_1, t\_17

ASAR a\_intArray\_005435F0, t\_17, t\_18

PUSH t\_18

CALP p\_proc2\_00549178

PUSH n\_4

CALF f\_func1\_00548D10, t\_19

ASS t\_19, v\_int1\_00543658

CALF f\_func2\_00549110, t\_20

ASS t\_20, v\_ch1\_00543728

RTN

# 说明

目前只是生成了四元式，目标代码（x86汇编）还没有做出来。以上四元式结果经人工审核，与预期一致。下一步工作是根据四元式生成目标代码。