Date: 2020/11/10

Task Group: Fast Interrupts

Chair: Krste Asanovic

Co-Chair: Kevin Chen

Number of Attendees: ~5

Current issues on github: https://github.com/riscv/riscv-fast-interrupt/issues

Previous meeting minutes: https://github.com/riscv/ -fast-in riscv terrupt/tree/master/minutes

Due to the new change of meeting cadence, the attendance was much lower than usual.

Issue discussed:

#98 Name of the controller: CLIC or FLIC?

    The following 2 names are identical in meaning so they are too confusing and indistinguishable for new users:

        Core Local INTerruptor (for CLINT)

        Core Local Interrupt Controller (for CLIC)

    Member A: Although the name CLINT has not been mentioned in any RISC-V documents, there are about 3 companies using it. So we should not change it.

    Member B: A name does not automatically become official simply because a handful of companies are using it. It needs to be discussed holistically to avoid confusion for new users (especially these names will likely be written into future textbooks). Otherwise, any small group of members can simply pick and use a name by themselves then announce it becomes the official RISC-V name in the future.

    Member C: Maybe we can conduct a poll to select and decide the names.

#107 Heritage of features

    This mainly refers to issue #106.

#106 enhance support co-routines and thus allow level change already saved registers.

Since this issue had extremely long description, spent lots of time clarifying the usage scenario and details. Recognized it was a smart idea and could potentially outperform competitors. However, raised concerns about the return of investment and potential difficulties in verification.