Homework 5 Wednesday, October 27, 2021 12:46 PM Memory Read = 0 Memory Write= 0 Register Write= 1 Branch = 0 ALU MUX = 1 ALU OP=AND MUX Reg = 1 B Blocks L. Fetch, Decode, execute, write Back data Memory is notused, Branch Add poduces 0 unused output #2 @ Data Memory -> /dur + Stur 52596+10% -> 35% B Rtype + I Type = 24% + 28% = 52% @ Itype+ Ldur+ Stdur + CBZ+B = 282+252+102+112+28 = 762 Doctput is ignored A mem to Reg = 0 means you can't head from #3 Memory so low instructions don't work B Alu is always set to O So no immediate instructions will hork @ Key to Loc = 0 All read instructions will not Wor K F 8 0 1 4 0 6 2 1111 1000 0000 0001 0100 0000 0110 0010 Sign = 0000 6000 0000 0000 0/60 6000 0/10 6011 La Shift 0000 0000 0101000000 11 0001000 (B)hex conversion ALU OP 1111/0 = 62,00 3 E, 0 PC+4 > Next sequential instr D Write Registr-0 ALU MUX so don't core A Lu Mux - 20 Jump -> PC+4 Bouch - PC+1 (E) Alv-7 20, -3 ADD > PC+4 Branch + PC+4 (F) Read 1 = 3 Red 2 = 2 Rey Wrik= 0 data = 1 B) head heg 2 - 0 unconditional Branch - 0 Brancho > 0 Memory Read -> 0 Men to Reg => 0 ALUOP => 0 Memory Write = 0 Alus 1 taking in immediak Reg Write => 1 write result #6 P2 7 2.5 x 109 IPS P3-9 1.8 IPS (P2 Clock speed (GHZ) x & (10 sec) B P1 - 3 x10'° P2 -> 2.5×1010 P3 -> M×1010 (clock *time) ep7 = instr. P1 = 2x10 10 P2 = 2.5x10 10 P3 = 1.82x1010 7 PI = (1×105) (.1×3) + (1×105) (.3×2) + (1×105) (.4×2) + (1×105) (.2×1) PF 2.5 CPI 92= (1x105)(.1x2)+(1x105)(.3x2)+(1x105)(.4x2)+(1x105)(.2x2) (1×105) 72=2 CP7 P7=> (1x105)(2.5)= 8.3×10/3 $P2 = \frac{(1 \times 10^5)(2)}{3 \times 10^7} = 6.6 \times 10$

PI is faster