# **ASSEMBLY**

JANUARY 26, 2024

### ÜBERSICHT

- Setup

  Dockerfile: github.com/Kernware/Presentations
- Einleitung per Beispiel
- Verschiedene Architekturen
- x86\_64 Architektur
- Instruktionen
- Programmier Beispiele

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**HTL** Weiz

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Denuvo by Irdeto (irdeto.com/denuvo) Kernware (kernware.at)

github.com/gasto4 kurt@kernware.at



```
#include <stdio.h>

int main() {
    printf("Hello World\n");
    return 0;
}
```

```
#include <stdio.h>

int main() {
    printf("Hello World\n");
    return o;
}

Kompilieren mit:
```

clang++ 1\_sample.c -> ./a.out

```
; godbolt.org
      .data:
2
          .string "Hello World"
      main:
          push
                  rbp
5
          mov
                  rbp, rsp
                   edi, OFFSET FLAT:.data
          mov
          call
                   puts
8
                   eax, o
          mov
                   rbp
          pop
10
          ret
11
```

```
; godbolt.org
      .data:
2
          .string "Hello World"
      main:
          push
                 rbp
5
          mov
                  rbp, rsp
                  edi, OFFSET FLAT:.data
          mov
         call
                  puts
                  eax, o
          mov
                  rbp
          pop
10
          ret
11
```

Und nun?

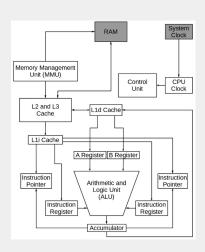
```
1 .text:01140 ; int __cdecl main(int argc, const char **argv, const char **envp)
2 .text:01140
3 .text:01140 55
                                      push
                                              rbp
4 .text:01141 48 89 E5
                                      mov
                                              rbp, rsp
5 .text:01144 48 83 EC 10
                                              rsp, 10h
                                      sub
6 .text:01148 C7 45 FC 00 00 00 00
                                              [rbp+var 4], o
                                      mov
7 .text:0114F 48 8D 3D AE 0E 00 00
                                      lea
                                              rdi. format
                                                              ; "Hello World\n"
8 .text:01156 Bo oo
                                      mov
                                              al. o
9 .text:01158 E8 D3 FE FF FF
                                      call
                                              printf
10 .text:0115D 31 Co
                                      xor
                                              eax, eax
11 .text:0115F 48 83 C4 10
                                      add
                                              rsp, 10h
12 .text:01163 5D
                                              rbp
                                      pop
13 .text:01164 C3
                                      ret
```

```
1 .text:01140 ; int __cdecl main(int argc, const char **argv, const char **envp)
2 .text:01140
3 .text:01140 55
                                      push
                                              rbp
4 .text:01141 48 89 E5
                                      mov
                                              rbp, rsp
5 .text:01144 48 83 EC 10
                                              rsp, 10h
                                      sub
6 .text:01148 C7 45 FC 00 00 00 00
                                              [rbp+var 4]. o
                                      mov
7 .text:0114F 48 8D 3D AE 0E 00 00
                                      lea
                                              rdi. format
                                                              : "Hello World\n"
8 .text:01156 Bo oo
                                      mov
                                              al. o
9 .text:01158 E8 D3 FE FF FF
                                      call
                                              printf
10 .text:0115D 31 Co
                                      xor
                                              eax, eax
11 .text:0115F 48 83 C4 10
                                      add
                                              rsp, 10h
12 .text:01163 5D
                                              rbp
                                      pop
13 .text:01164 C3
                                      ret
```

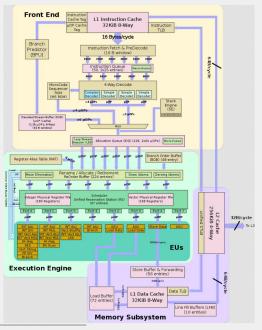
CPU macht: Fetch -> Decode -> Execute

#### **CPU ARCHITEKTUR**

```
push
                               rbp
  55
  48 89 E5
                        mov
                               rbp, rsp
  48 83 EC 10
                        sub
                               rsp, 10h
  C7 45 FC 00 00 00 00 mov
                              [rbp+var 4], o
  48 8D 3D AE OE OO OO
                              rdi, format_str
                       lea
6 Bo oo
                               al, o
                        mov
  E8 D3 FE FF FF
                        call _printf
  31 Co
                        xor
                               eax, eax
  48 83 C4 10
                        add
                               rsp, 10h
  5D
                               rbp
                        pop
  C3
                        ret
```



<sup>&</sup>lt;sup>1</sup>https://www.redhat.com/sysadmin/cpu-components-functionality



<sup>&</sup>lt;sup>1</sup>https://en.wikichip.org/wiki/intel/microarchitectures/skylake

```
addi
                sp, sp, -16
                                              ; RISC-V
       sd
                ra,8(sp)
       sd
                so,o(sp)
       addi
                so, sp, 16
       lui
                a5,%hi(.LCo)
5
       addi
                ao, a5,%lo(.LCo)
       call
                puts
7
       li
                a5,0
9
                ao, as
       mv
       ld
                ra,8(sp)
10
       ld
                so,o(sp)
11
12
       addi
                sp, sp, 16
13
       ir
                ra
```

```
addi
                sp, sp, -16
                                               ; RISC-V
       sd
                ra,8(sp)
2
                so,o(sp)
       sd
       addi
                so, sp, 16
4
        lui
                a5,%hi(.LCo)
5
6
                ao, a5,% lo (.LCo)
        addi
        call
                puts
8
        ti
                a5,0
9
                ao, as
       mv
10
        ld
                ra,8(sp)
        ld
                so,o(sp)
11
        addi
                sp, sp, 16
        ir
                 ra
        push
                {r11, lr}
                                              : Arm-v7
                r11, sp
2
       mov
       sub
                sp, sp, #8
3
                ro, #o
4
       mov
                ro, [sp]
        str
5
6
        str
                ro, [sp, #4]
        ldr
                ro, .LCPIo o
8
        bl
                printf
        ldr
                ro, [sp]
9
10
       mov
                sp, r11
                 {r11, lr}
11
        pop
12
        bx
                 ۱r
```

```
x29, x30, [sp, -16]!; Arm64
       stp
               X29, SP
       mov
       adrp
               xo, .LCo
       add
               xo, xo, :lo12:.LCo
       bl
               puts
       mov
               WO, O
       ldp
              X29, X30, [SD], 16
       ret
       push r28
                                           ; AVR
       push r29
       in r28,__SP_L__
       in r29,__SP_H__
       ldi r24, lo8(.LCo)
       ldi r25, hi8(.LCo)
       rcall puts
8
       ldi r24,0
9
       ldi r25,0
10
       pop r29
11
       pop r28
12
       ret
```

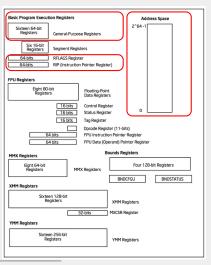
## ARCHITEKTUR X86\_64

■ weitesten verbreitet

<sup>1</sup>https://www.intel.com/content/dam/www/public/us/en/documents/manuals/64-ia-32-architectures-software-developer-vol-1-manual.pdf

## ARCHITEKTUR X86\_64

#### ■ weitesten verbreitet



<sup>1</sup>https://www.intel.com/content/dam/www/public/us/en/documents/manuals/64-ia-32-architectures-softwaredeveloper-vol-1-manual.pdf

## ARCHITEKTUR X86\_64 GENERAL PURPOSE REGISTER

rax	1
eax	
ax	
al	

64bit	32bit	16bit	8bit
rax	eax	ax	al
rbx	ebx	bx	bl
rcx	есх	CX	cl
rbp	ebp	bp	bpl
r11	r11d	r11W	r11b
r12	r12d	r12W	r12b

### FLAG REGISTER



ZF - Zero Flag

### **CMP Instruction**

```
cmp rax, rbx -> (CF, OF, SF, ZF, AF, PF)
```

```
CF = 1 unsigned rbx > unsigned rax
OF = 1 MSB rax != MSB result
SF = 1 MSB result == 1
ZF = 1 rax == rbx

JE, JZ ZF == 1 rax == rbx
JNE, JNZ ZF == 0 rax != rbx
JG ZF == 0 & SF == OF rax > rbx
```

### **INSTRUCTION** SET

### ■ Data Transfer (mov, ...)

```
1 mov rax, 0
2 mov rax, rbx
3 mov [rbp], 1
4 lea rax, [2]
```

### Data Transfer (mov, ...)

```
1 mov rax, 0
2 mov rax, rbx
3 mov [rbp], 1
4 lea rax, [2]
```

### ■ Binary Arithmetic (add, sub, inc, dec, mul, div, ...)

```
inc rax
add rax, rbx
sub rax, rbx
imul rax, rcx
idiv rbx; (RDX:RAX)/RBX, quotient in RAX, remainder in RDX
```

#### ■ Data Transfer (mov, ...)

```
1 mov rax, 0
2 mov rax, rbx
3 mov [rbp], 1
4 lea rax, [2]
```

### ■ Binary Arithmetic (add, sub, inc, dec, mul, div, ...)

```
inc rax
add rax, rbx
sub rax, rbx
imul rax, rcx
idiv rbx; (RDX:RAX)/RBX, quotient in RAX, remainder in RDX
```

### ■ Logical (and, or, xor, ...)

```
and rdx, rcx
or rax, rbx
xor rax, rax
not rcx
```

■ Data Transfer (mov, ...)

```
1 mov rax, 0
2 mov rax, rbx
3 mov [rbp], 1
4 lea rax, [2]
```

■ Binary Arithmetic (add, sub, inc, dec, mul, div, ...)

```
inc rax
add rax, rbx
sub rax, rbx
imul rax, rcx
idiv rbx ; (RDX:RAX)/RBX, quotient in RAX, remainder in RDX
```

■ Logical (and, or, xor, ...)

```
and rdx, rcx
or rax, rbx
xor rax, rax
not rcx
```

■ Shift and rotate (sal, shl, sar, shr, ...)

```
shl rax, 3
shr rbx, 1
rol rdx, 4
```

### INSTRUCTION SET

### ■ Control transfer (jmp, jl, call, ret, ...)

```
cmp rax, rbx
                              ; sub rax, rbx
3
      ie equal target
      ine not_equal_traget
      jg greater_target
                              ; Jump if RAX is greater than RBX
      jle less_target
                              ; Jump if RAX is less than or equal to RBX
8
      z zero target
                              ; Jump if ZF (zero flag) is set
      inz not zero target
                              ; Jump if ZF (zero flag) is not set
9
10
      jmp unconditional_target
11
```

■ Control transfer (jmp, jl, call, ret, ...)

```
cmp rax, rbx
                                  ; sub rax, rbx
3
       ie equal target
       ine not equal traget
       jg greater_target ; Jump if RAX is greater than RBX ile less target : Jump if RAX is less than or equ
       jle less_target
                                  ; Jump if RAX is less than or equal to RBX
8
       z zero target
                                  ; Jump if ZF (zero flag) is set
       jnz not_zero_target    ; Jump if ZF (zero flag) is not set
9
10
       jmp unconditional_target
11
```

■ Viele mehr (Bit and Byte, I/O, Decimal Arithmetic, ...)

### **PUSH UND POP**

```
mov rax, 5
push rax
pop rax
```

#### PUSH UND POP

```
mov rax, 5
push rax
pop rax

mov rax, 5
sub rsp, 8; push rax
mov [rsp], rax
xor rax, rax
mov rax, [rsp]; pop rax
add rsp, 8
```

```
1 -> mov rax, 5
2     sub rsp, 8 ; push rax
3     mov [rsp], rax
4     xor rax, rax
5     mov rax, [rsp] ; pop rax
6     add rsp, 8
7     nop
```

#### Register File:

RSP	0x100108
RAX	OX123456

#### Memory:

•		
OX100100	0	
0x100108	0	(RSP)

```
mov rax, 5
sub rsp, 8; push rax
mov [rsp], rax

xor rax, rax
mov rax, [rsp]; pop rax
add rsp, 8
nop
```

#### Register File:

RSP	0x100108
RAX	5

#### Memory:

01/400400		
OX100100	0	
0x100108	0	(RSP)

```
mov rax, 5
sub rsp, 8; push rax
mov [rsp], rax

xor rax, rax
mov rax, [rsp]; pop rax
add rsp, 8
nop
```

#### Register File:

RSP	OX100100
RAX	5

#### Memory:

OX100100	0	(RSP)
0x100108	0	

```
mov rax, 5
sub rsp, 8; push rax
mov [rsp], rax
-> xor rax, rax
mov rax, [rsp]; pop rax
add rsp, 8
nop
```

#### Register File:

RSP	OX100100
RAX	5

#### Memory:

0X100100	5	(RSP)
0x100108	Ω	

```
mov rax, 5
sub rsp, 8; push rax
mov [rsp], rax

xor rax, rax
rax, rax
add rsp, 8
nop
```

#### Register File:

RSP	0X100100
RAX	0

#### Memory:

0X100100	5	(RSP)
0x100108	0	

#### Register File:

RSP	0X100100
RAX	5

#### Memory:

OX100100	5	(RSP)
0x100108	0	

```
mov rax, 5
sub rsp, 8; push rax
mov [rsp], rax
xor rax, rax
mov rax, [rsp]; pop rax
add rsp, 8
nop
```

#### Register File:

RSP	0x100108
RAX	5

#### Memory:

0X100100	5	
0x100108	0	(RSP)
0/(100100		(1101)

### **ERSTES BEISPIEL**

```
Schreib ein Schleife die genau 5 ma ausgeführt
wird.
(http://asmdebugger.com/)
C PseudoCode:
int a = 5;
while(a > o) {
    a--;
```

Achtung: Code muss mit 'start:' anfangen und 'nop' Instructions sind nicht unterstützt.

# **ERSTES BEISPIEL**

```
start:
mov rax, 0
mov rbx, 5
loop_start:
inc rax  ; Increment RAX
cmp rax, rbx  ; Compare RAX with loop count
jl loop_start  ; Jump if RAX < loop_count
exit:</pre>
```

# **ERSTES BEISPIEL**

```
start:
mov rax, 5
loop_start:
dec rax  ; Decrement RAX, sets ZF if o
    jnz loop_start
exit:
```

# **ERSTES BEISPIEL**

```
start:
mov rcx, 5
loop_start:
loop loop_start ; Alias for "dec rcx; jnz"
exit:
```

## WAS MACHT FOLGENDER CODE?

```
1 Xor eax, eax
2 lea rbx, [o]
3 mov rdx, o
4 and esi, o
5 sub edi, edi
```

# How to do it local?

```
; hello.asm
global _start

_start:
_mov rax, o
mov rbx, 5

loop_start:
_inc rax
cmp rax, rbx
jl loop_start
nop
```

## How to do it local?

```
: hello.asm
      global _start
      _start:
      mov rax, o
5
      mov rbx, 5
      loop_start:
           inc rax
9
           cmp rax, rbx
10
           il loop_start
11
      nop
12
```

nasm -felf64 hello.asm & ld hello.o & ./a.out

```
C PseudoCode:
int a = 5;
int b = 3;
if (a > b) {
    b = a;
else {
    a = 0;
while(1){};
. . .
```

```
; if.asm
       global _start
3
       _start:
5
      mov rax, 5
      mov rbx, 3
6
7
      cmp rax, rbx
8
      jg if_target
9
       ; else_target
10
      mov rax, o
11
      jmp while_target
12
13
       if_target:
14
      mov rbx, rax
15
16
       while_target:
17
      imp while_target
18
```

```
C PseudoCode:
int getOne(int arg) {
    if (arg > 5) {
        return 1;
    return o;
};
int main() {
    int a = 42;
    int b = getOne(a);
    while(1){};
```

32

```
; call.asm
1
       global _start
2
3
       getOne: ; argument stored in rax
4
       cmp rax, 5
5
       jg if_target
6
       mov rax, o
7
       ret
8
9
       if_target:
10
       mov rax, 1
11
       ret
12
13
       _start:
14
       mov rax, 42
15
       call getOne
16
       mov rbx, rax
17
18
       while_target:
19
       jmp while_target
20
```

## **CALLING CONVENTION**

### **CALLING CONVENTION**

```
// Microsoft x64
func(int a, int b, int c, int d, int e, int f);
// RCX, RDX, R8, R9, everything else pushed on stack
// Linux
func(int a, int b, int c, int d, int e, int f);
// RDI, RSI, RDX, RCX, R8, R9
```

```
global _start ; challenge.asm
        verify:
 2
       push
              rbp
 3
 4
       mov
              rbp, rsp
       sub
              dword [rbp + 16], 0x18
              dword [rbp + 16], 0x7a69
       xor
              dword [rbp + 16]
       not
8
       xor
              dword [rbp + 16], 0x11e61
              dword [rbp + 16], 0x37
9
       xor
              dword [rbp + 16], 0x26
       add
10
11
       mov
              eax. dword [rbp + 16]
              rbp
       pop
        ret
14
        start:
15
        ; ... TODO
16
17
        call verify
18
       mov rbx, oxfffe8906
19
       cmp rax, rbx
       jne while_target
20
       lea rsi, [msg]
21
22
       mov rdi, 1
       mov rdx, 2
23
       mov rax, 1
24
        syscall
25
26
27
        while target:
       imp while target
28
29
       msg db ':', ')', o
30
```

```
global start ; challenge.asm
       verify:
 2
       push
             rbp
 3
 4
       mov
              rbp, rsp
       sub
             dword [rbp + 16], 0x18
             dword [rbp + 16], 0x7a69
       xor
       not
             dword [rbp + 16]
8
       xor
             dword [rbp + 16], 0x11e61
             dword [rbp + 16], 0x37
9
       xor
       add
              dword [rbp + 16], 0x26
10
11
       mov
              eax. dword [rbp + 16]
              rbp
       pop
       ret
14
       start:
15
16
       mov rax, ox1338
17
       push rax
18
       call verify
       mov rbx, oxfffe8906
19
       cmp rax, rbx
20
       jne while_target
21
       lea rsi, [msg]
       mov rdi. 1
23
       mov rdx, 2
24
       mov rax, 1
25
26
       svscall
27
       while target: imp while target
28
29
       msg db ':', ')', o
30
```

```
int verify(int arg) {
           int ret = arg;
2
           ret -= ox18;
           ret ^= ox7a69;
4
           ret = ~ret;
5
           ret ^= 0x11e61;
6
           ret ^= ox37;
           ret += 0x26;
8
           return ret;
9
10
11
       int main() {
12
           int a = 0x1338;
13
           int b = verify(a);
14
           if (b == oxfffe8906) {
15
                printf(":)");
16
17
           while (1) {};
18
19
```

# **ZU GUTER LETZT**

Vielen Dank!