

Experiment – 6

Aim of the experiment –

To study the operation of 8×1 multiplier and 1:16 de mux.

Objective –

To verify the truth table of multiplexer and de mux.

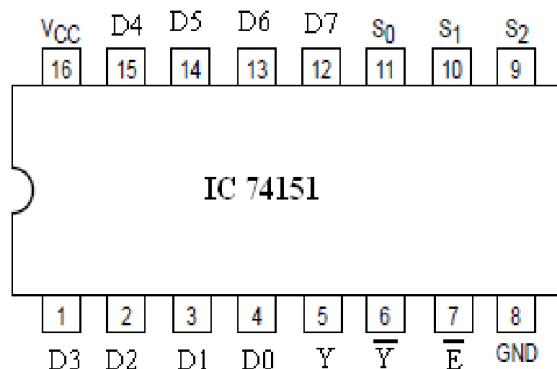
Apparatus required-

Serial No	Items Required	Specifications
1.	Project Board	
2.	IC 74151	8:1 MUX
3.	IC 74154	1:16 complementary DE MUX
4.	Connecting Wires	

Theory -

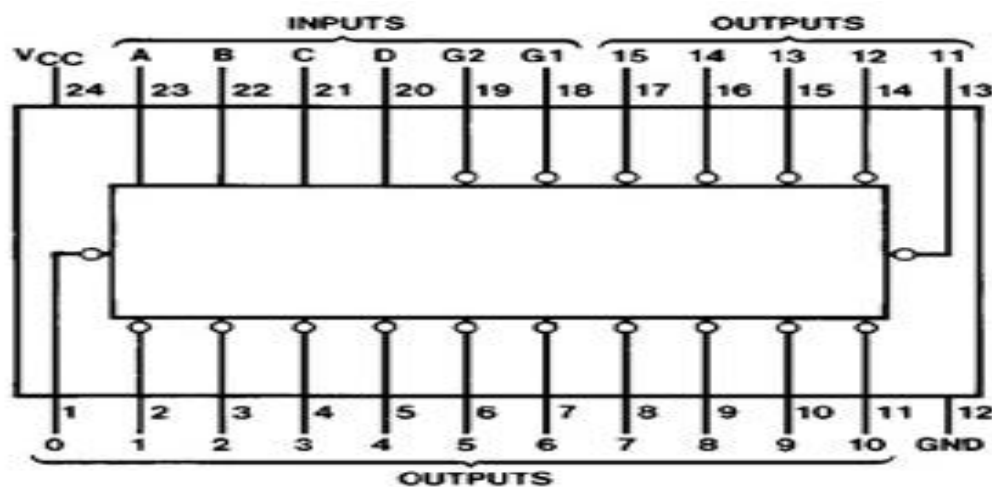
Multiplexer - A multiplexer (mux) is a device that select one of several analog or digital i/p signals and forward the selected l/p into a single output it is having two types of input in the ratio of $2^m = n$, where 'n' is no. of data input and 'm' is no. of select input.

8:1 multiplexer :



S2	S1	S0	I	Q
0	0	0	$I_0=0$ $I_0=1$	0,1
0	0	1	$I_1=0$ $I_1=1$	0,1
0	1	0	$I_2=0$ $I_2=1$	0,1
0	1	1	$I_3=0$ $I_3=1$	0,1
1	0	0	$I_4=0$ $I_4=1$	0,1
1	0	1	$I_5=0$ $I_5=1$	0,1
1	1	0	$I_6=0$ $I_6=1$	0,1
1	1	1	$I_7=0$ $I_7=1$	0,1

DE – MUX – It is combination logic having only one input and many no. of output lines along with the select lines. The select lines are used to select one output data at a time the relationship between output lines and the select lines are $m = 2^s$ where 's' is the no. of select lines and 'm' is the no. of output lines.



[illegible]

Tabulation –

Make the table in high and low format.

Table for 1:16 DEMUX-

Table for 8:1 MUX –

[illegible]

H = High Level, L = Low Level, X = Don't Care

Conclusion –