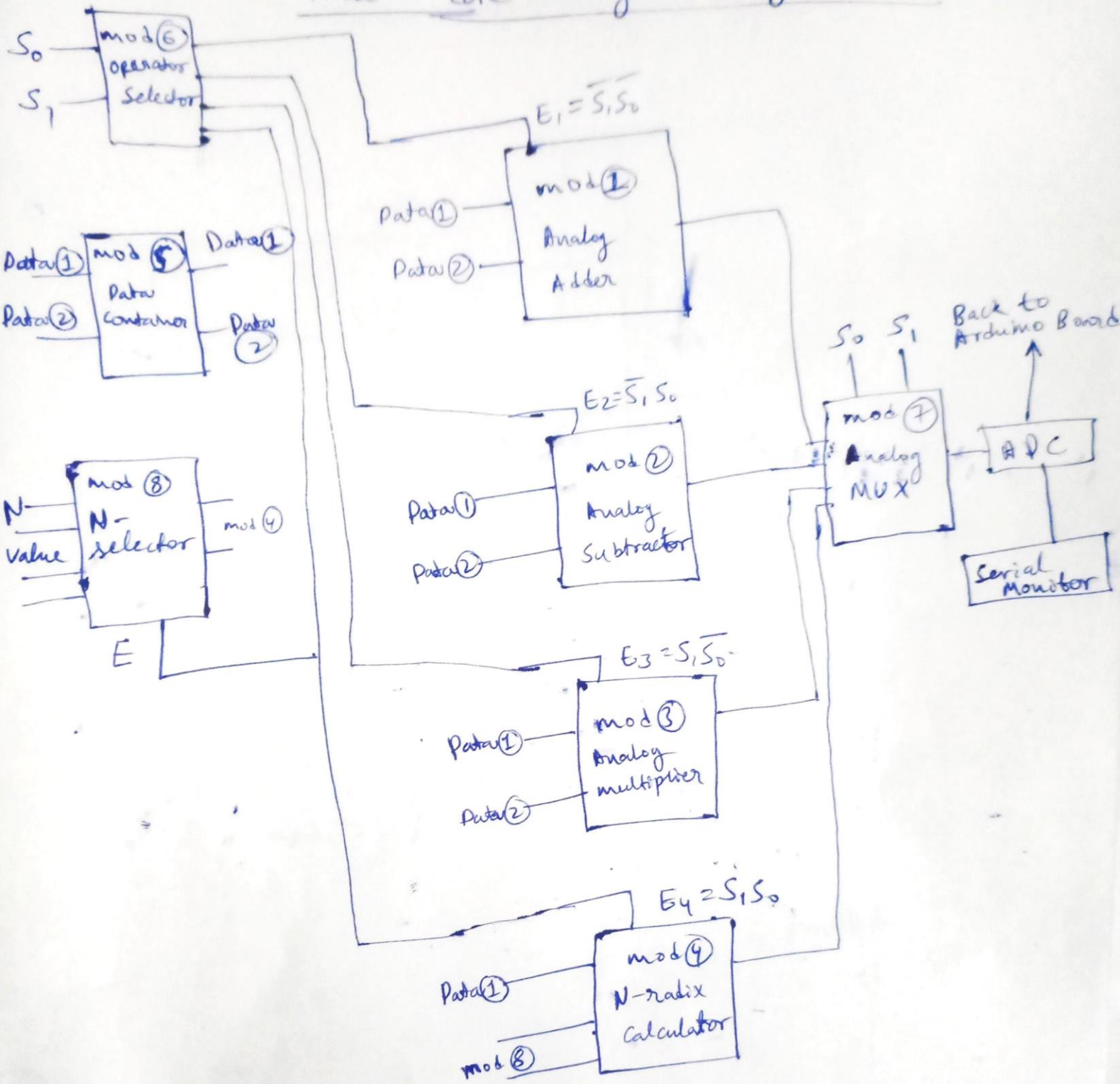
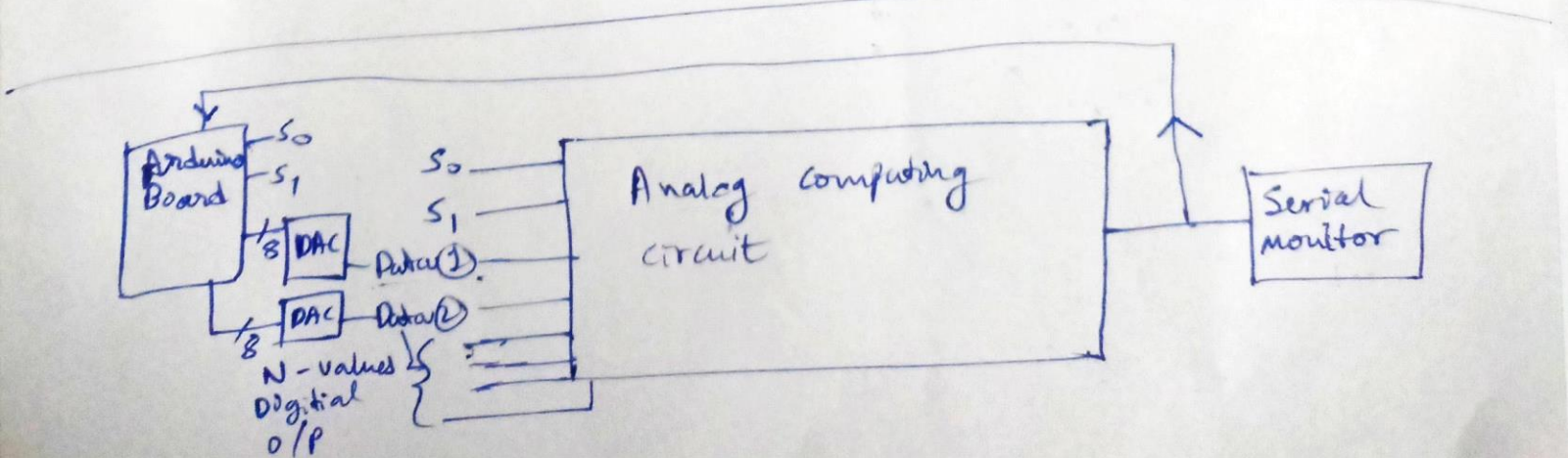
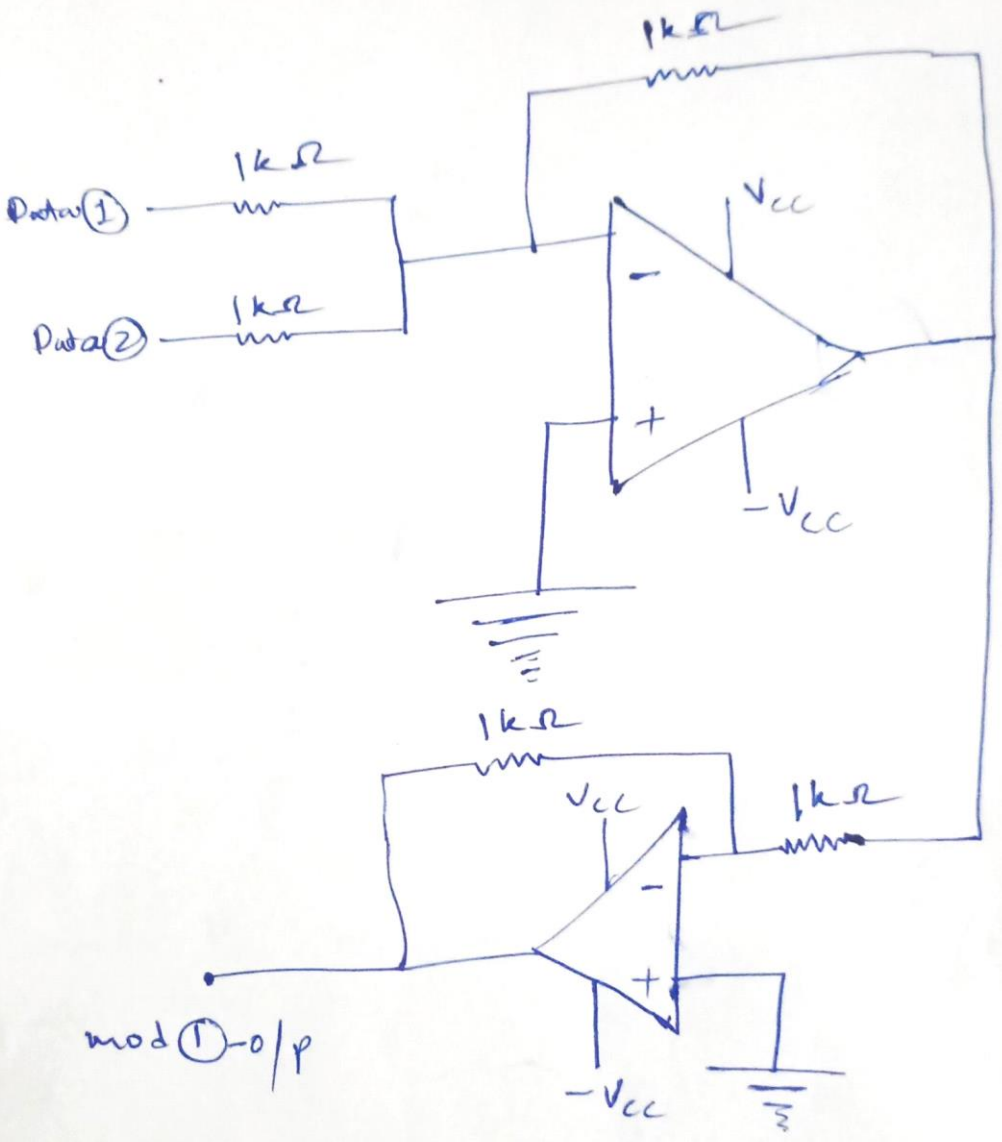
Final Core Analog computing circuit



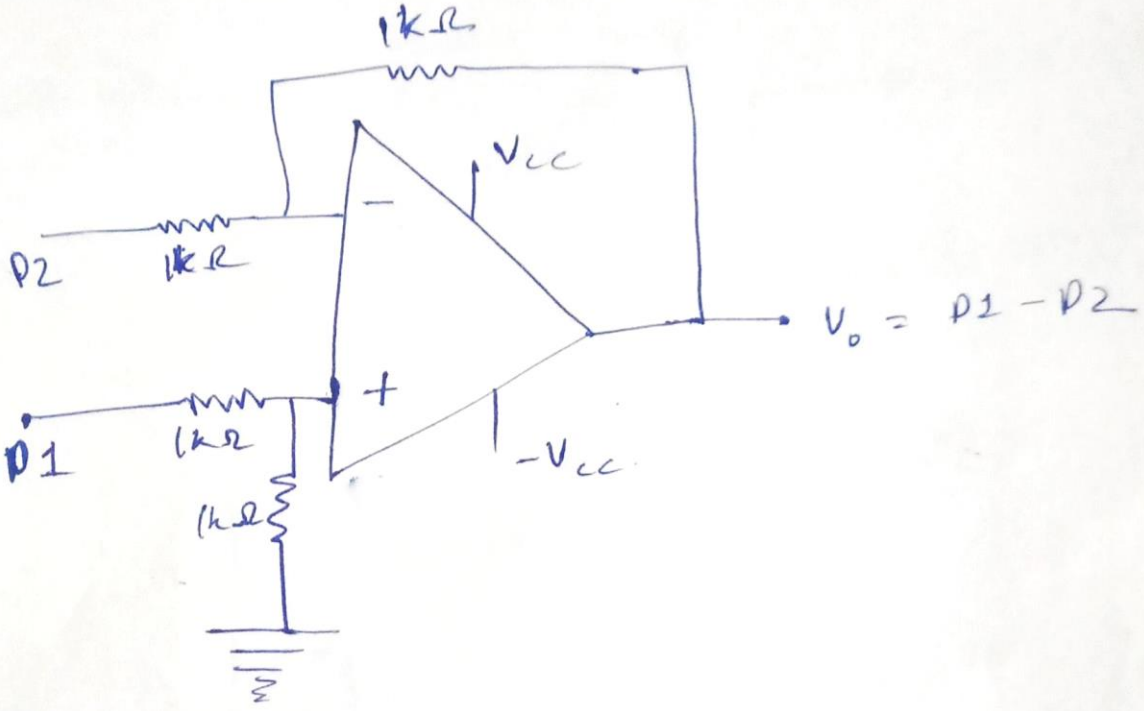


Module (1) { Analog Adder }

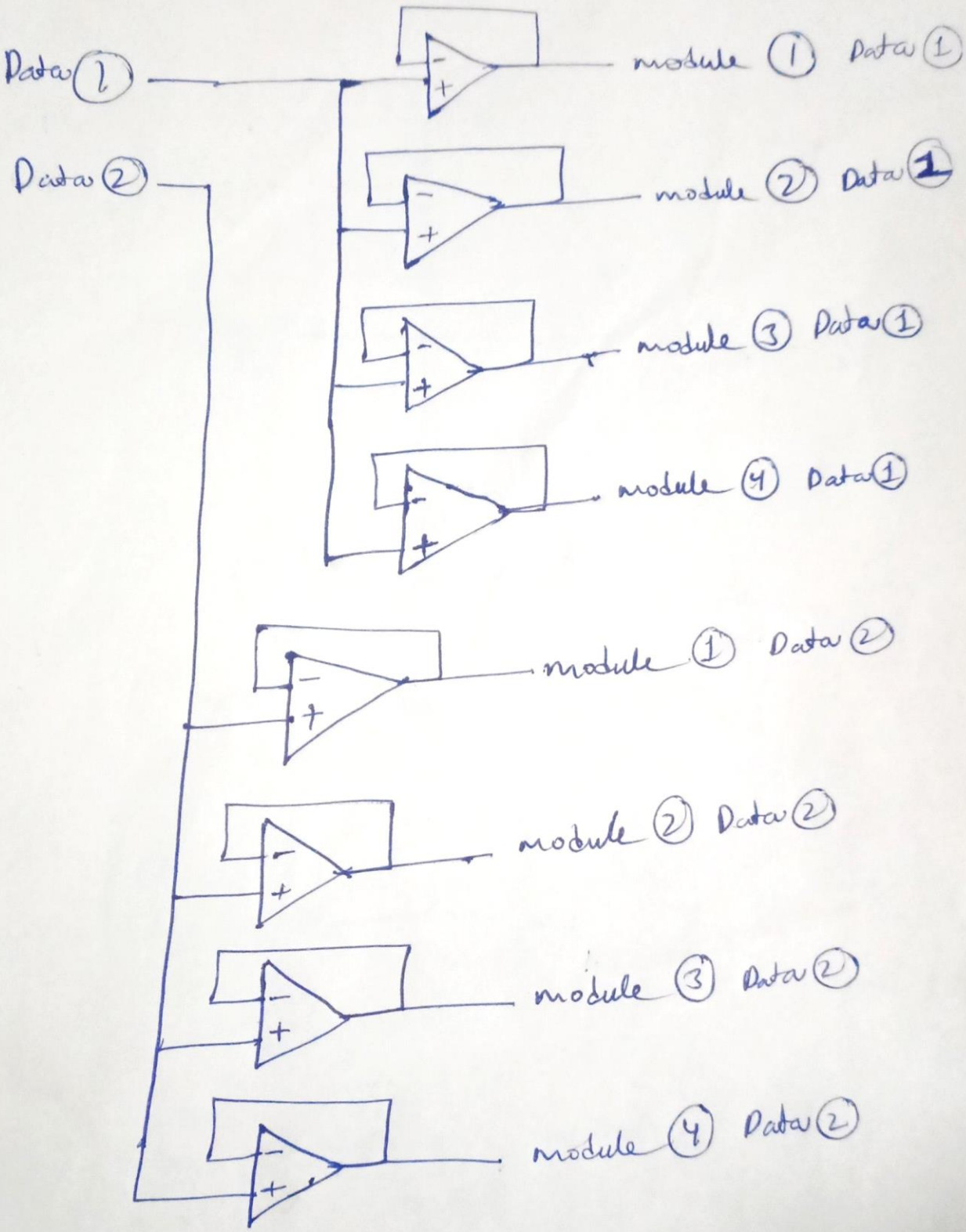


→ V_{cc} and $-V_{cc}$ are controlled by the help of module (6).

Module (2) { Analog Subtractor }

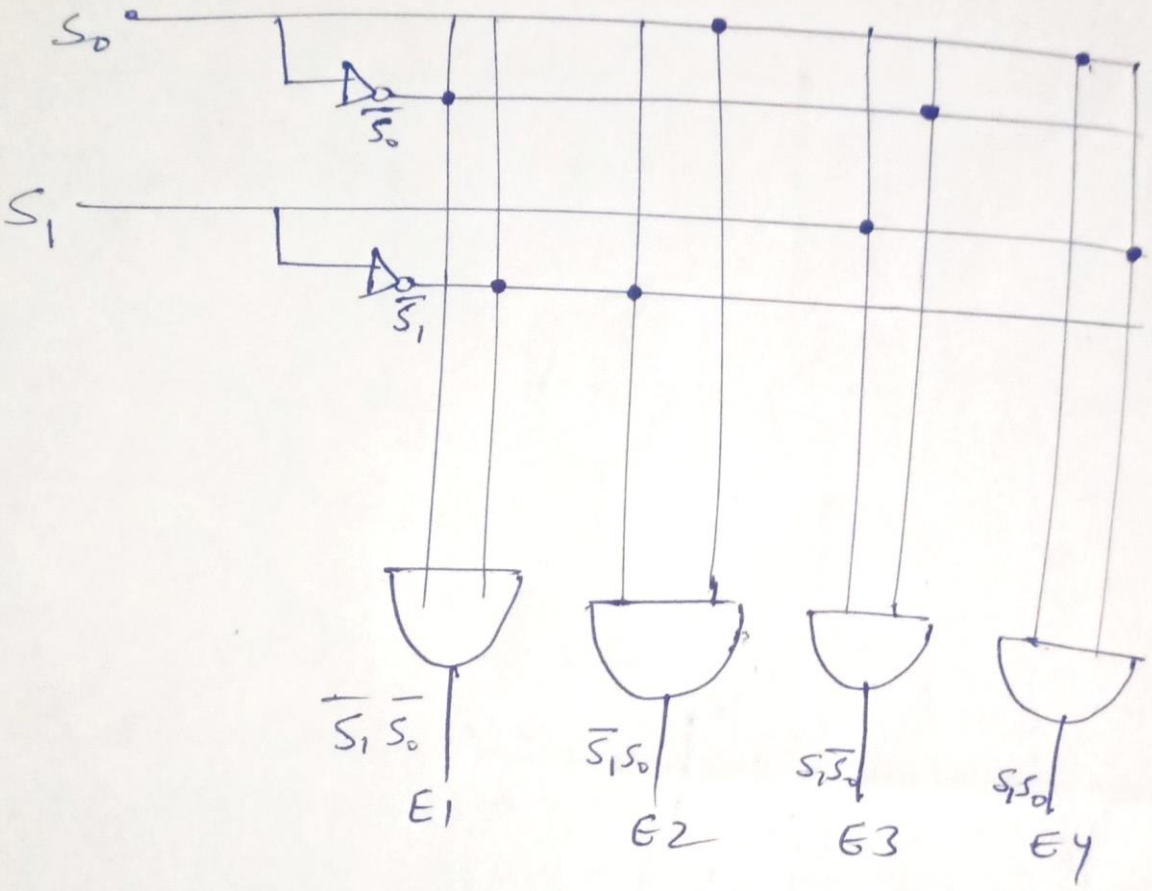


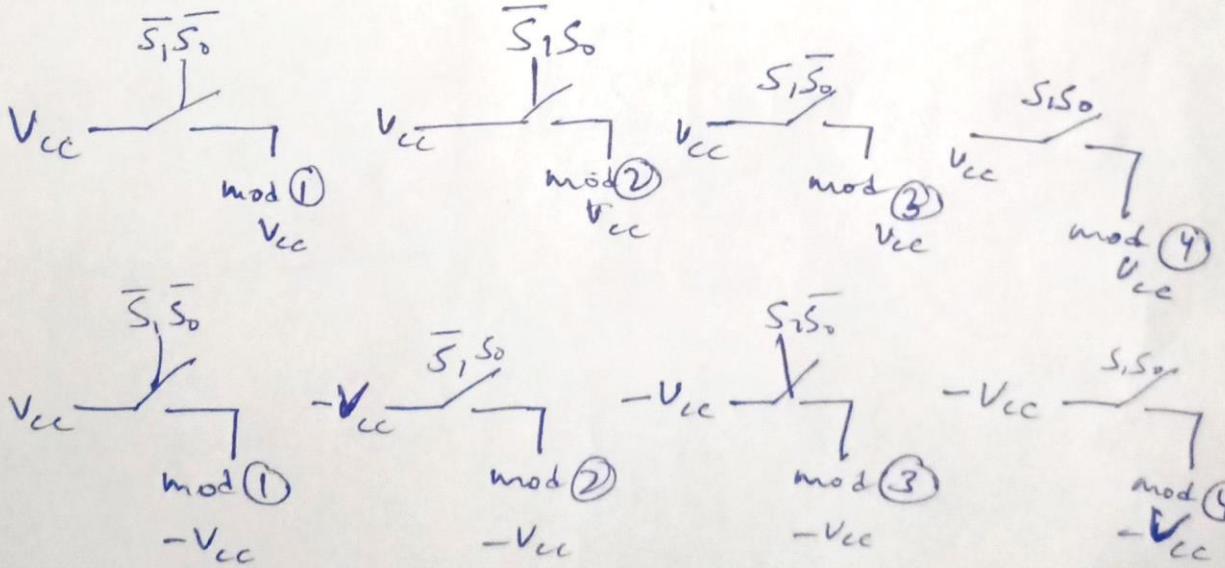
⟶ V_{cc} and $-V_{cc}$ are controlled with the help of module (6).



module (5)
{Data container}

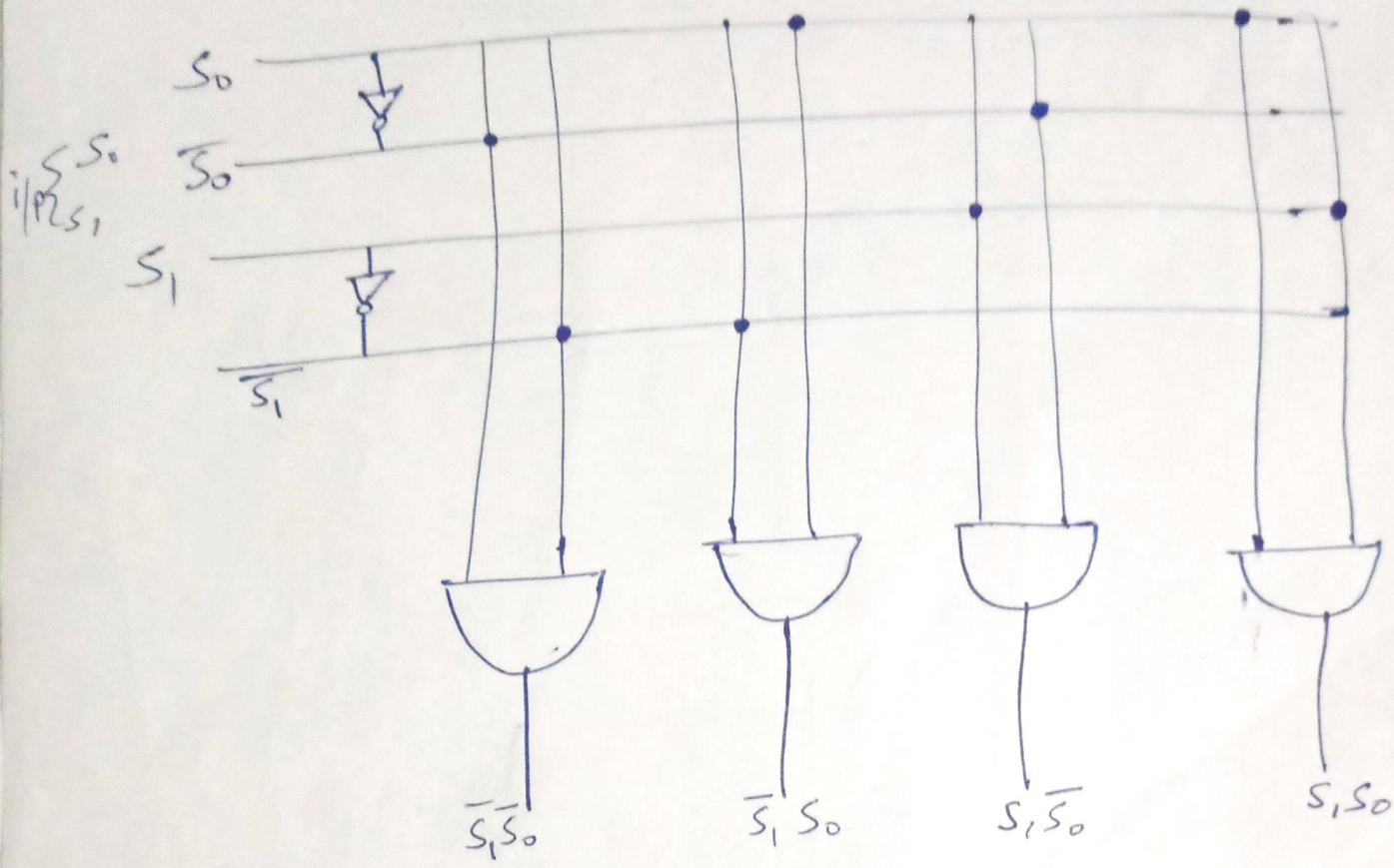
Module (6) {Operator Selector}

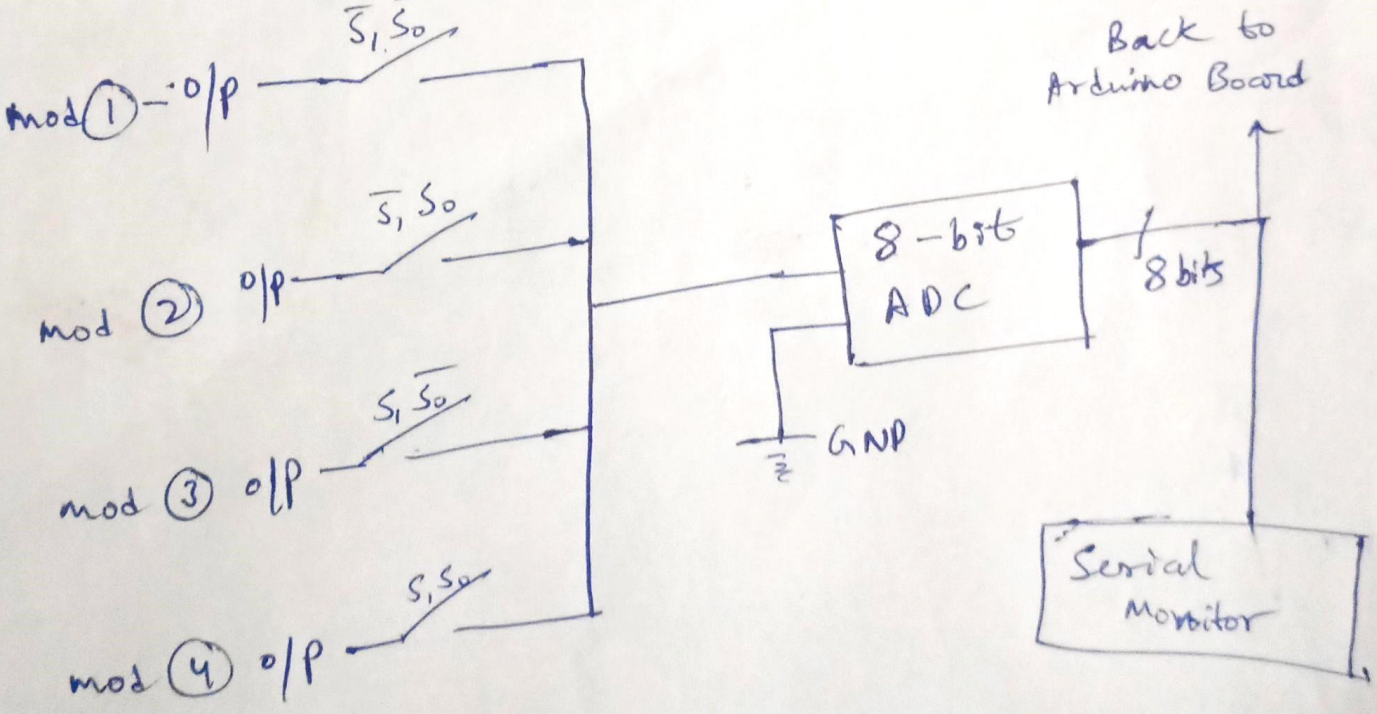




* Switches are implemented using CD 4066.
* Not gates are implemented using 74HC04.
* And gates are implemented using 74HC08.

module (7)


S_0
$\bar{S}_0$
i/p S_1
S_1
$\bar{S}_1$
$\bar{S}_1\bar{S}_0$
$\bar{S}_1 S_0$
$S_1\bar{S}_0$
$S_1 S_0$


$\bar{S}_1\bar{S}_0$
mod (1) o/p
$\bar{S}_1 S_0$
mod (2) o/p
$S_1\bar{S}_0$
mod (3) o/p
$S_1 S_0$
mod (4) o/p
8-bit ADC
8 bits
GND
Back to Arduino Board
Serial Monitor

module (8) { N - selector }

N - Value { 4-bits Digital } ——— $I_3 I_2 I_1 I_0$