



**Vidyavardhini's College of Engineering & Technology**  
**Department of Artificial Intelligence and Data Science**

---

Experiment No. 2

Basic gates using universal gates.

Name: Ketan Mahadik

Roll Number: 24

Date of Performance:

Date of Submission:



**Vidyavardhini's College of Engineering & Technology**  
**Department of Artificial Intelligence and Data Science**

---

\_\_\_\_\_tatio

Aim - To realize the gates using universal gates.

## Objective

- 1) To study the realization of basic gates using universal gates.
- 2) Understanding how to construct any combinational logic function using NAND or NOR gates only. Theory -

AND, OR, NOT is called basic gates as their logical operation cannot be simplified further. NAND and NOR are called universal gates as using only NAND or only NOR, any logic function can be implemented.

## Components required -

1. IC's 7400(NAND) 7402(NOR) 2.
- Bread Board.
3. Connecting wires.

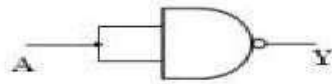
## Circuit Diagram -

# Vidyavardhini's College of Engineering & Technology

## Department of Artificial Intelligence and Data Science

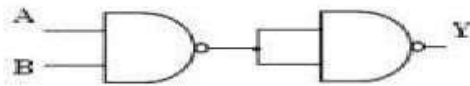
<sup>n</sup> using NANO gate;

(a) NOT gate:  $Y = A'$



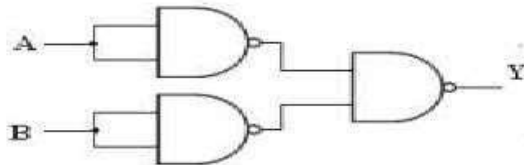
A	Y
0	1
1	0

(b) AND gate:  $Y = A \cdot B$



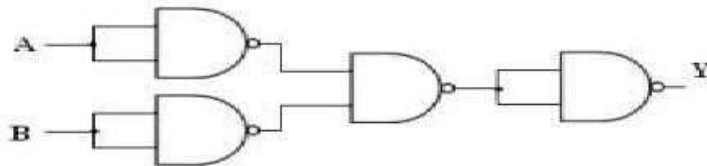
A	B	Y
0	0	0
0	1	0
1	0	0
1	1	1

(c) OR gate:  $Y = A + B$



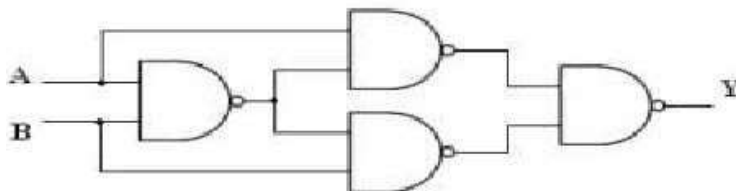
A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1

(d) NOR gate:  $Y = (A + B)'$



A	B	Y
0	0	1
0	1	0
1	0	0
1	1	0

(e) Ex-OR gate:  $Y = A \oplus B$



A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0

# Vidyavardhini's College of Engineering & Technology

Department of Artificial Intelligence and Data Science



**Vidyavardhini's College of Engineering & Technology**

Department of Artificial Intelligence and Data Science

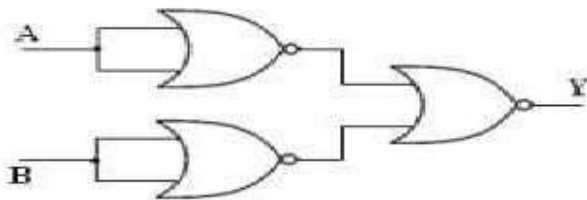
In-implementation using NOR gate:

(a) NOT gate:  $Y = A'$



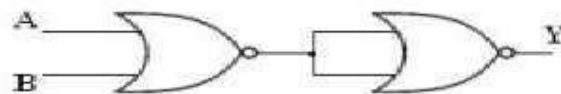
A	Y
0	1
1	0

(b) AND gate:  $Y = A \cdot B$



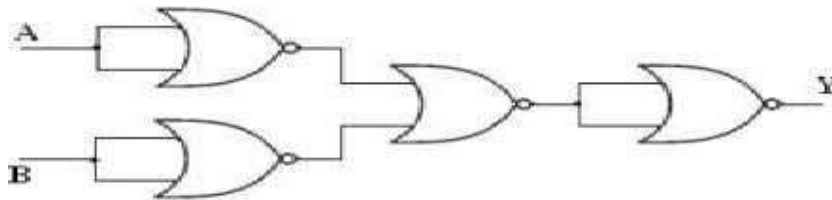
A	B	Y
0	0	0
0	1	0
1	0	0
1	1	1

(c) OR gate:  $Y = A + B$



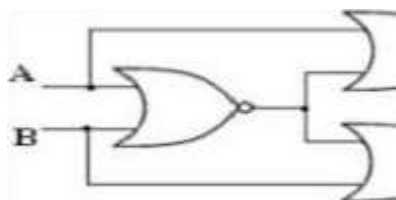
A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1

(d) NAND gate:  $Y = (AB)'$



A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

(e) Ex-NOR gate:  $Y = A \odot B = (A \oplus B)'$



A	B	Y
---	---	---

0	0	1
0	1	0
1	0	0
1	1	1

# Vidyavardhini's College of Engineering & Technology

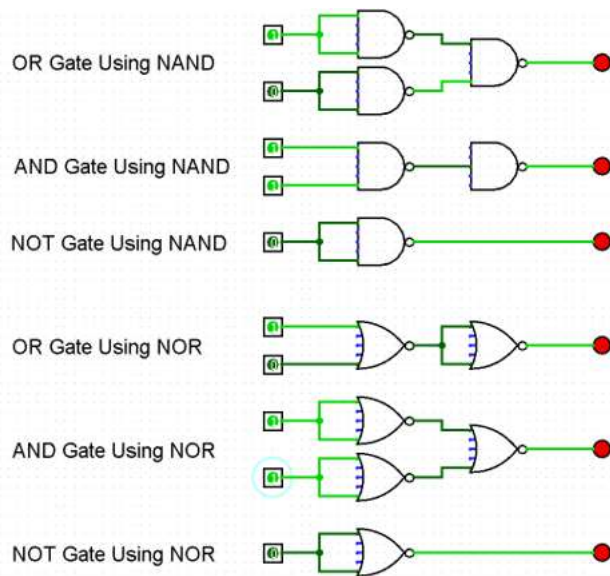
Department of Artificial Intelligence and Data Science



**Vidyavardhini's College of Engineering & Technology**

**Department of Artificial Intelligence and Data Science**

Screenshot:



Procedure: Connections are made as per the circuit diagrams. By applying the inputs, the outputs are observed and the operations are verified with the help of truth table.

Conclusion - The experiment aimed to implement various logic gates using universal gates. By utilizing universal gates such as NAND or NOR, we successfully demonstrated their ability to simulate the behavior of AND, OR, NOT, and other gates. This experiment showcased the versatility and fundamental importance of universal gates in digital logic design, emphasizing their role in simplifying circuitry and facilitating the construction of complex logic functions using a minimal set of components.

Vidyavardhini's College of Engineering & Technology  
Department of Artificial Intelligence and Data Science

CSL302: