

Sistemas Embarcados

- 05/07/2024

Field Programmable gate Array → (FPGA)

Arranjo de Porta programável em campo → Hardware Programável

PCB e CI com lógica digital?

microprocessadores e microcontroladores?

FPGA?

* Paradigma da Lógica Programável

12/07/2024

SAP - Simple AS Possible

FPGA Device : MAX10 10M50DAF484C7G

• New Project Wizard

Directory - Name of the project - Top-level Entity

• New

Block Diagram / Schematic Files

~~Verilog HDL Files~~
~~Hardware Description Language (HDL) Files~~
~~Programmer~~

• Pin Planner

• Netlist Viewers

● New [Simulação]
University Program VWF

Waveform Editor

Simulation Settings

1- Testbench Generation Command

... Renomear.VWF . . . Renomear.VWF

2- Model Sim Script

Renomear.VWF.Vt

Apagar "-novopt"

3- Renomear Arquivo.VWF