

iverilog installation

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Icarus Verilog for Windows

Icarus Verilog is a free compiler implementation for the IEEE-1364 Verilog hardware description language. Icarus is

In this page you will find easy to install Icarus Verilog packages compiled with the **MinGW** toolchain for the Windows

Download

You can find Icarus Verilog sources and binaries for most platforms at the **Icarus site FTP**. The sources available here

- **iverilog-v11-20210204-x64_setup.exe [44.1MB]**
- iverilog-v11-20201125-x64_setup.exe [48.1MB]
- iverilog-10.1.1-x64_setup.exe [9.77MB]
- iverilog-10.0-x86_setup.exe [11.1MB]
- iverilog-20130827_setup.exe (development snapshot) [11.2MB]
- iverilog-0.9.7_setup.exe (latest stable release) [10.5MB]
- iverilog-0.9.6_setup.exe [10.4MB]
- iverilog-0.8.6_setup.exe (latest release 0.8 series) [1.29MB] iverilog-0.8.6.7z [800kB]
- iverilog-0.7-20040706_setup.exe [1.09MB] iverilog-0.7-20040706.7z [588kB]

Resources

Here are some pointers to interesting Verilog related resources.

- **Verilog Resources**
- **GTKWave Electronic Waveform Viewer**
- **GTKWave for Windows**
- **IVI, a graphical frontend for Icarus**
- **Eclipse Verilog Editor**
- **Getting started with Icarus Verilog on Windows**
- **Verilog syntax highlighting for UltraEdit.**

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Setup - Icarus Verilog version v11-20210204

License Agreement

Please read the following important information before continuing.



Please read the following License Agreement. You must accept the terms of this agreement before continuing with the installation.

GNU GENERAL PUBLIC LICENSE
Version 2, June 1991

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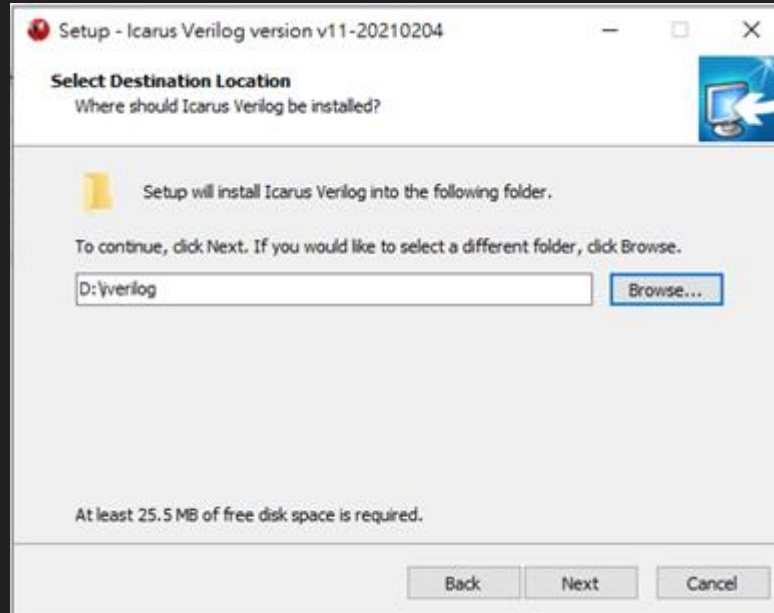
- ☒ I accept the agreement
- ☐ I do not accept the agreement

Next

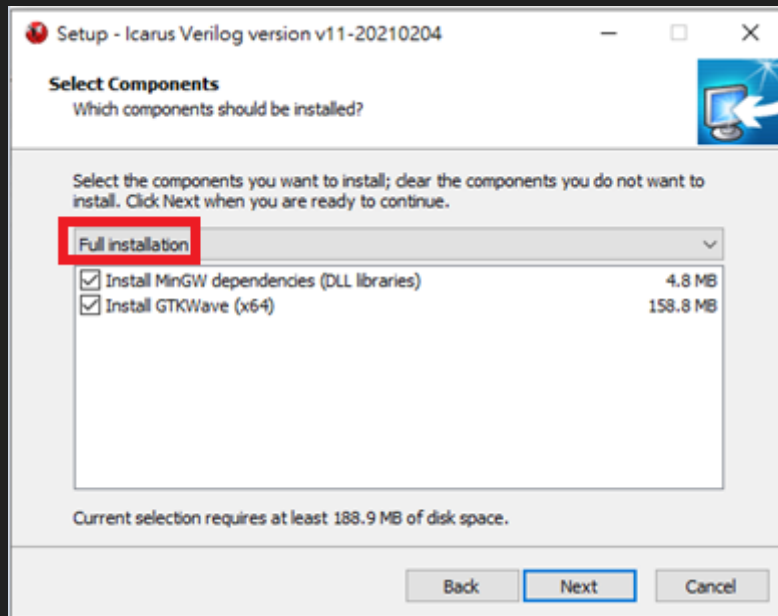
Cancel

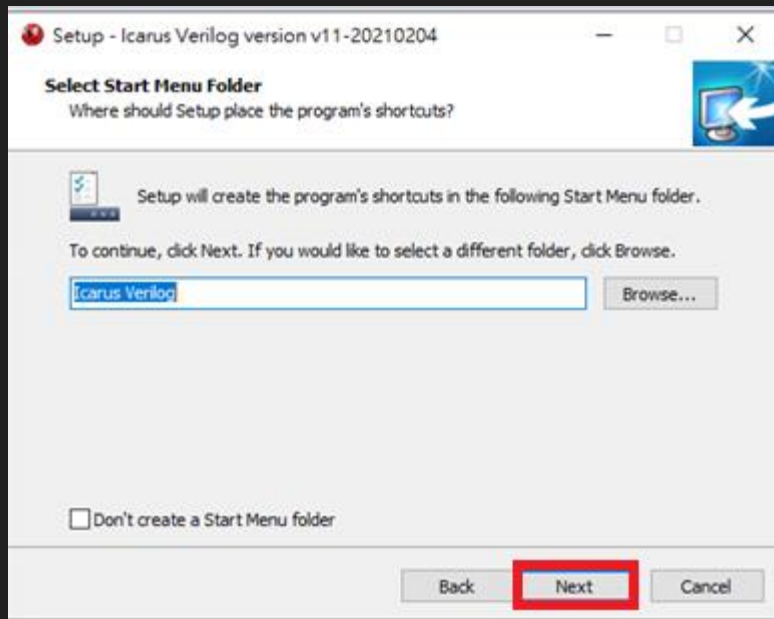
Choose the path you want

Ex: D:\



Full installation

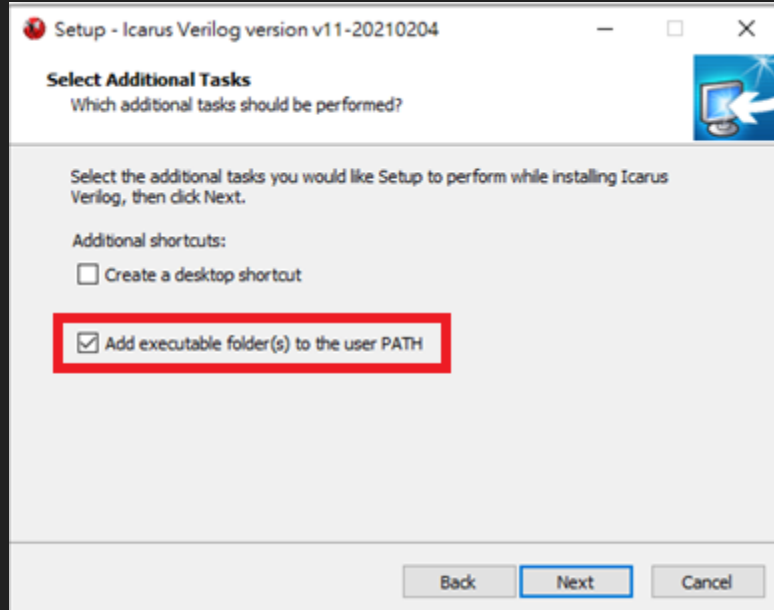




Important

Select → ADD executable folder(s) to user PATH

This will help you set up the Environment Variable



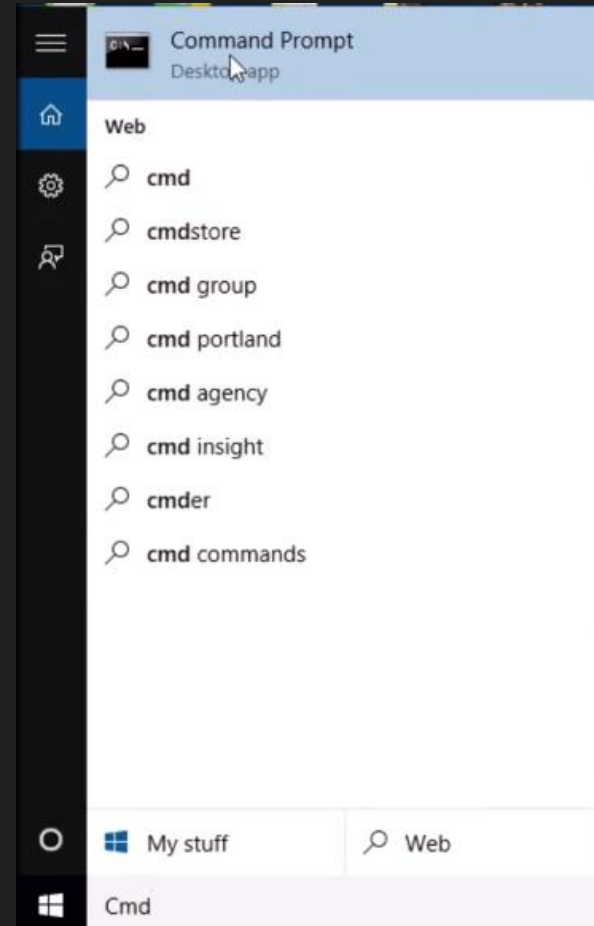
Restart your computer

After your installation is complete, please restart your computer so that the environment variables will take effect.

Run your verilog program

Windows cmd

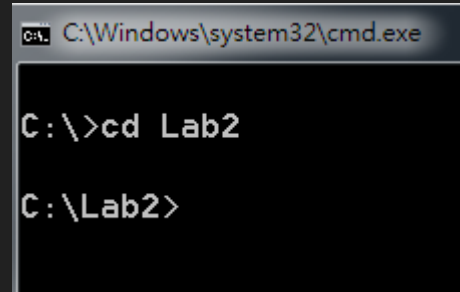
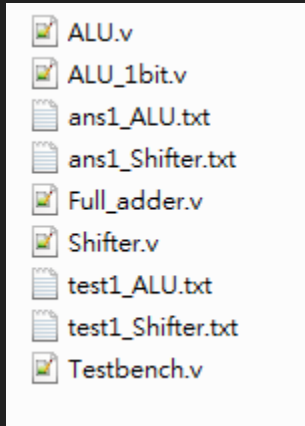
Search cmd at search bar



Change the directory in cmd

Ex: The path of my code is at C:\Lab2

(put all your code, test and ans data in same folder)

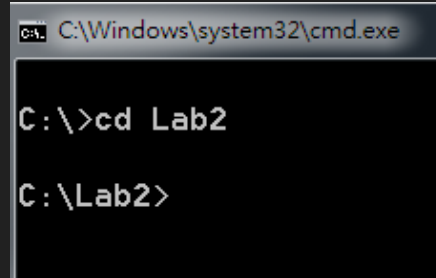


How to change the directory (folder) in CMD

change the directory

\$ cd xxx

Ex: cd Lab2

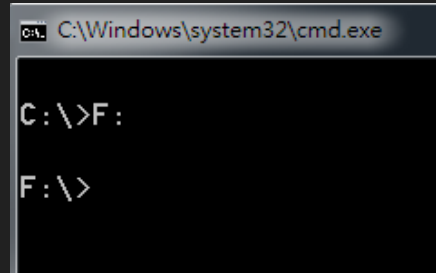


```
C:\Windows\system32\cmd.exe  
C:\>cd Lab2  
C:\Lab2>
```

change the drive

Ex: go to F:

\$ F:



```
C:\Windows\system32\cmd.exe  
C:\>F:  
F:\>
```

view the contents of a directory

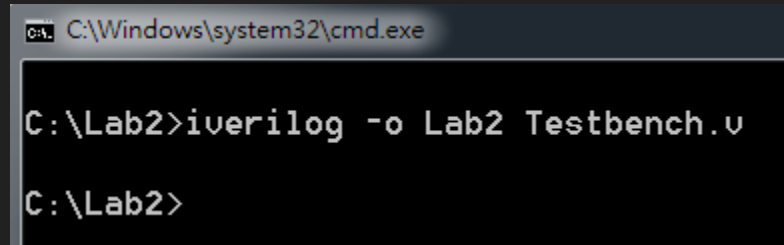
\$ dir

Compile your code

\$ iverilog -o xxxx Testbench.v

xxxx is the output file name

Ex: Lab2



```
C:\Windows\system32\cmd.exe

C:\Lab2>iverilog -o Lab2 Testbench.v

C:\Lab2>
```

Run your code

\$ vvp xxxx

execute the file xxxx you compiled

Ex: Lab2

C:\Windows\system32\cmd.exe

```
C:\Lab2>iverilog -o Lab2 Testbench.v
```

```
C:\Lab2>vvp Lab2
```

```
UCD info: dumpfile ALU_lab2.vcd opened for output.
```

```
ALU test data #1 is wrong
```

```
Shifter test data #1 is wrong
```

```
ALU test data #2 is wrong
```

```
Shifter test data #2 is wrong
```

```
ALU test data #3 is wrong
```

```
Shifter test data #3 is wrong
```

```
ALU test data #4 is wrong
```

```
Shifter test data #4 is wrong
```

```
Correctness = 0/8
```

```
Testbench.v:59: $finish called at 50000 (1ps)
```

```
C:\Lab2>
```

GTKWave

The code in Testbench.v will save the wave information in ALU_lab2.vcd

You can use gtkwave to check your wave if you need

```
$ gtkwave ALU_lab2.vcd &
```

