

ECE 437L Midterm Report

FirstName LastName

TA's Name

Due Date

1 Executive Overview

Text for your executive overview

2 Processor Design

Text for discussing your designs

3 Processor Debug

Fix the processor!!!

4 Results

| | |
|------------|-------------------------|
| Throughput | ??/??=??instructions/ns |
| Latency | ??*??=??ns/instruction |

Table 1: Processor Specs

Text for your results

5 Conclusion

Text for your conclusion

6 Contributions

Text for individual contributions