XME0712		PE300	Product Name	JM1		Product Name	PE300	XME0712	
Xilinx			FPGA vendor			FPGA vendor		Xilinx	
Artix-7			FPGA family			FPGA family		Artix-7	
35T/75T/100T/200T			FPGA device(s)			FPGA device(s)			
CLG484			FPGA package			FPGA package		FGG484	
R1.1		R1.1	Board revision			Board revision	R1.1	R1.1	
			Board status	JM1		Board status			
12 Dec 2019		12 Dec 2019	Last update	Pin Numbers		Last update	12 Dec 2019	12 Dec 2019	
Signal Name	Pin Number	Signal Name	Pin Type	Outside	Inside	Pin Type	Signal Name	Signal Name	Pin Number
B15_L10_P	M21	HDMI_RX_SPDIF	IO_P	1	2	IO_P	HDMI_RX_INT	B15_L9_P	K21
B15_L10_N	L21	HDMI_RX_MCLK	IO_N	3	4	IO_N	ETH1_nRST	B15_L9_N	K22
B15_L21_P	K17	HDMI_RX_LRCLK	IO_P	5	6	IO_P	ETH1_RGMII_MDC	B15_L7_P	J22
B15_L21_N	J17	HDMI_RX_SCLK	IO_N	7	8	IO_N	ETH1_RGMII_MDIO	B15_L7_N	H22
GND		GND	GND	9	10	GND	GND	GND	
VCCIO15		VIOTB(3V3)	VIN_IO	11	12	IO_P	ETH1_RXCTL	B15_L3_P	J14
VCCIO15		VIOTB(3V3)	VIN_IO	13	14	IO_N	ETH1_RXD0	B15_L3_N	H14
B15_L23_P	L16	PL_LED2	IO_P	15	16	IO_P	ETH1_RXD1	B15_L5_P	J15
B15_L23_N	K16	PL_LED1	IO_N	17	18	IO_N	ETH1_RXD2	B15_L5_N	H15
GND		GND	GND	19	20	GND	GND	GND	
B15_L11_P_SRCC	J20	GPIO2_9P	CLK_P	21	22	CLK_P	ETH1_RXCK	B15_L12_P_MRCC	J19
B15_L11_N_SRCC	J21	GPIO2_9N	CLK_N	23	24	CLK_N	ETH1_RXD3	B15_L12_N_MRCC	H19
B15_L8_P	H20	PL_KEY1	IO_P	25	26	IO_P	ETH1_TXD0	B15_L6_P	H17
B15_L8_N	G20	PL_KEY2	IO_N	27	28	IO_N	ETH1_TXD1	B15_L6_N	H18
GND		GND	GND	29	30	GND	GND	GND	
B15_L4_P	G17	GPIO2_6P	IO_P	31	32	IO_P	ETH1_TXCK	B15_L2_P	G15
B15_L4_N	G18	GPIO2_6N	IO_N	33	34	IO_N	ETH1_TXD2	B15_L2_N	G16
B16_L22_P	E22	GPIO2_5P	IO_P	35	36	IO_P	ETH1_TXD3	B15_L1_P	H13
B16_L22_N	D22	GPIO2_5N	IO_N	37	38	IO_N	ETH1_TXCTL	B15_L1_N	G13
GND		GND	GND	39	40	GND	GND	GND	
B16_L20_P	C22	GPIO2_4P	CLK_P	41	42	CLK_P	GPIO1_1P	B16_L24_P	G21

B16_L20_N	B22	GPIO2_4N	CLK_N	43	44	CLK_N	GPIO1_1N	B16_L24_N	G22
B16_L21_P	B21	GPIO2_3P	IO_P	45	46	IO_P	GPIO1_2P	B16_L3_P	C14
B16_L21_N	A21	GPIO2_3N	IO_N	47	48	IO_N	GPIO1_2N	B16_L3_N	C15
GND		GND	GND	49	50	GND	GND	GND	
B16_L23_P	E21	GPIO2_2P	IO_P	51	52	IO_P	GPIO1_3P	B16_L16_P	B20
B16_L23_N	D21	GPIO2_2N	IO_N	53	54	IO_N	GPIO1_3N	B16_L16_N	A20
B16_L19_P	D20	GPIO2_1P	IO_P	55	56	IO_P	GPIO1_4P	B16_L17_P	A18
B16_L19_N	C20	GPIO2_1N	IO_N	57	58	IO_N	GPIO1_4N	B16_L17_N	A19
GND		GND	GND	59	60	GND	GND	GND	
B16_L13_P_MRCC	C18	GPIO2_0P	CLK_P	61	62	CLK_P	GPIO1_5P	B16_L14_P_SRCC	E19
B16_L13_N_MRCC	C19	GPIO2_0N	CLK_N	63	64	CLK_N	GPIO1_5N	B16_L14_N_SRCC	D19
B16_L9_P	A15	GPIO1_17P	IO_P	65	66	IO_P	GPIO1_6P	B16_L18_P	F19
B16_L9_N	A16	GPIO1_17N	IO_N	67	68	IO_N	GPIO1_6N	B16_L18_N	F20
GND		GND	GND	69	70	GND	GND	GND	
B16_L10_P	A13	GPIO1_16P	IO_P	71	72	IO_P	GPIO1_7P	B16_L15_P	F18
B16_L10_N	A14	GPIO1_16N	IO_N	73	74	IO_N	GPIO1_7N	B16_L15_N	E18
B16_L6_P	D14	GPIO1_15P	IO_P	75	76	IO_P	GPIO1_8P	B16_L8_P	C13
B16_L6_N	D15	GPIO1_15N	IO_N	77	78	IO_N	GPIO1_8N	B16_L8_N	B13
GND		GND	GND	79	80	GND	GND	GND	
B16_L11_P_SRCC	B17	GPIO1_0P	CLK_P	81	82	CLK_P	GPIO1_9P	B16_L12_P_MRCC	D17
B16_L11_N_SRCC	B18	GPIO1_0N	CLK_N	83	84	CLK_N	GPIO1_9N	B16_L12_N_MRCC	C17
B16_L7_P	B15	GPIO1_14P	IO_P	85	86	IO_P	GPIO1_10P	B16_L5_P	E16
B16_L7_N	B16	GPIO1_14N	IO_N	87	88	IO_N	GPIO1_10N	B16_L5_N	D16
GND		GND	GND	89	90	GND	GND	GND	
VCCIO16		VIOTA(Selectable	VIN_IO	91	92	IO_P	GPIO1_11P	B16_L1_P	F13
VCCIO16		VIOTA(Selectable	VIN_IO	93	94	IO_N	GPIO1_11N	B16_L1_N	F14
B16_L4_P	E13	GPIO1_13P	IO_P	95	96	IO_P	GPIO1_12P	B16_L2_P	F16
B16_L4_N	E14	GPIO1_13N	IO_N	97	98	IO_N	GPIO1_12N	B16_L2_N	E17
GND		GND	GND	99	100	GND	GND	GND	

XME0712		P300	Product Name	JM2 Pin Numbers		Product Name	PE300	XME0712	
Xilinx			FPGA vendor			FPGA vendor		Xilinx	
Artix-7			FPGA family			FPGA family		Artix-7	
XC7A100T			FPGA device(s)			FPGA device(s)		XC7A100T	
FGG484			FPGA package			FPGA package		FGG484	
R1.1		R1.1	Board revision			Board revision	R1.1	R1.1	
			Board status	JM2		Board status			
12 Dec 2019		12 Dec 2019	Last update	Pin Numbers		Last update	12 Dec 2019	12 Dec 2019	
Signal Name	Pin Number	Signal Name	Pin Type	Inside	Outside	Pin Type	Signal Name		
MGT_CLK0_N	E6	PCIE_CLK_N	MGT_CLK_N	1	2	MGT_CLK_N	-	-	
MGT_CLK0_P	F6	PCIE_CLK_P	MGT_CLK_P	3	4	MGT_CLK_P	-	-	
GND		GND	GND	5	6	GND	GND	GND	
MGT_TX0_N	A4	PCIE_TX3_N	MGT_TX_N	7	8	MGT_RX_N	PCIE_RX3_N	MGT_RX0_N	A8
MGT_TX0_P	B4	PCIE_TX3_P	MGT_TX_P	9	10	MGT_RX_P	PCIE_RX3_P	MGT_RX0_P	B8
GND		GND	GND	11	12	GND	GND	GND	
MGT_TX1_N	C5	PCIE_TX2_N	MGT_TX_N	13	14	MGT_RX_N	PCIE_RX2_N	MGT_RX1_N	C11
MGT_TX1_P	D5	PCIE_TX2_P	MGT_TX_P	15	16	MGT_RX_P	PCIE_RX2_P	MGT_RX1_P	D11
GND		GND	GND	17	18	GND	GND	GND	
MGT_TX2_N	A6	PCIE_TX1_N	MGT_TX_N	19	20	MGT_RX_N	PCIE_RX1_N	MGT_RX2_N	A10
MGT_TX2_P	B6	PCIE_TX1_P	MGT_TX_P	21	22	MGT_RX_P	PCIE_RX1_P	MGT_RX2_P	B10
GND		GND	GND	23	24	GND	GND	GND	
MGT_TX3_N	C7	PCIE_TX0_N	MGT_TX_N	25	26	MGT_RX_N	PCIE_RX0_N	MGT_RX3_N	C9
MGT_TX3_P	D7	PCIE_TX0_P	MGT_TX_P	27	28	MGT_RX_P	PCIE_RX0_P	MGT_RX3_P	D9
GND		GND	GND	29	30	GND	GND	GND	
B15_L16_P	M18	HDMI_RX_RST	IO_P	31	32	IO_P	HDMI_TX_D23	B15_L15_P	N22
B15_L16_N	L18	RX_HPD_CTRL	IO_N	33	34	IO_N	HDMI_TX_D22	B15_L15_N	M22
B15_L18_P	N20	HDMI_TX_INT	IO_P	35	36	IO_P	HDMI_TX_D21	B15_L17_P	N18
B15_L18_N	M20	PCIE_PERSTn	IO_N	37	38	IO_N	HDMI_TX_D20	B15_L17_N	N19
GND		GND	GND	39	40	GND	GND	GND	
B15_L13_P	K18	HDMI_SDA	CLK_P	41	42	CLK_P	HDMI_TX_D19	B15_L14_P_SRCC	L19

B15_L13_N	K19	HDMI_SCL	CLK_N	43	44	CLK_N	HDMI_TX_D18	B15_L14_N_SRCC	L20
B15_L22_P	L14	HDMI_RX_P5	IO_P	45	46	IO_P	HDMI_TX_D17	B15_L19_P	K13
B15_L22_N	L15	HDMI_RX_P6	IO_N	47	48	IO_N	HDMI_TX_D16	B15_L19_N	K14
GND		GND	GND	49	50	GND	GND	GND	
B14_L23_P	N13	HDMI_RX_P7	IO_P	51	52	IO_P	HDMI_TX_D15	B15_L24_P	M15
B14_L23_N	N14	HDMI_RX_P8	IO_N	53	54	IO_N	HDMI_TX_D14	B15_L24_N	M16
B14_L22_P	P15	HDMI_RX_P9	IO_P	55	56	IO_P	HDMI_TX_D13	B15_L20_P	M13
B14_L22_N	R16	HDMI_RX_P10	IO_N	57	58	IO_N	HDMI_TX_D12	B15_L20_N	L13
GND		GND	GND	59	60	GND	GND	GND	
B14_L11_P_SRCC	U20	HDMI_RX_P11	CLK_P	61	62	CLK_P	HDMI_TX_CLK	B14_L12_P_MRCC	W19
B14_L11_N_SRCC	V20	HDMI_RX_P12	CLK_N	63	64	CLK_N	HDMI_TX_D11	B14_L12_N_MRCC	W20
B14_L5_P	P19	HDMI_RX_P13	IO_P	65	66	IO_P	HDMI_TX_D10	B14_L21_P	N17
B14_L5_N	R19	HDMI_RX_P14	IO_N	67	68	IO_N	HDMI_TX_D9	B14_L21_N	P17
GND		GND	GND	69	70	GND	GND	GND	
B14_L4_P	T21	HDMI_RX_P15	IO_P	71	72	IO_P	HDMI_TX_D8	B14_L20_P	R18
B14_L4_N	U21	HDMI_RX_P16	IO_N	73	74	IO_N	HDMI_TX_D7	B14_L20_N	T18
B14_L3_P	U22	HDMI_RX_P17	IO_P	75	76	IO_P	HDMI_TX_D6	B14_L7_P	W21
B14_L3_N	V22	HDMI_RX_P18	IO_N	77	78	IO_N	HDMI_TX_D5	B14_L7_N	W22
GND		GND	GND	79	80	GND	GND	GND	
B14_L13_P_MRCC	Y18	HDMI_RX_LLC	CLK_P	81	82	CLK_P	HDMI_TX_D4	B14_L14_P_SRCC	V18
B14_L13_N_MRCC	Y19	HDMI_RX_P19	CLK_N	83	84	CLK_N	HDMI_TX_D3	B14_L14_N_SRCC	V19
B14_L15_P	AA19	HDMI_RX_P20	IO_P	85	86	IO_P	HDMI_TX_D2	B14_L9_P	Y21
B14_L15_N	AB20	HDMI_RX_P21	IO_N	87	88	IO_N	HDMI_TX_D1	B14_L9_N	Y22
GND		GND	GND	89	90	GND	GND	GND	
-		VIOTC(3.3V)	VIN_IO	91	92	IO_P	HDMI_TX_D0	B14_L10_P	AB21
-		VIOTC(3.3V)	VIN_IO	93	94	IO_N	HDMI_TX_DE	B14_L10_N	AB22
B14_L17_P	AA18	HDMI_RX_P22	IO_P	95	96	IO_P	HDMI_TX_HS	B14_L8_P	AA20
B14_L17_N	AB18	HDMI_RX_P23	IO_N	97	98	IO_N	HDMI_TX_VS	B14_L8_N	AA21
GND		GND	GND	99	100	GND	GND	GND	

XME0712		PE300	Product Name	JM3 Pin Numbers		Product Name	PE300	XME0712	
Xilinx			FPGA vendor			FPGA vendor		Xilinx	
Artix-7			FPGA family			FPGA family		Artix-7	
XC7A100T			FPGA device(s)			FPGA device(s)		XC7A100T	
FGG484			FPGA package			FPGA package		FGG484	
R1.1		R1.1	Board revision			Board revision	R1.1	R1.1	
			Board status			Board status			
12 Dec 2019		12 Dec 2019	Last update			Last update	12 Dec 2019	12 Dec 2019	
		Signal Name	Pin Type	Inside	Outside	Pin Type	Signal Name		
VIN_MOD		VCC_12V	VIN_MOD	1	2	VIN_MOD	VCC_12V	VIN_MOD	
VIN_MOD		VCC_12V	VIN_MOD	3	4	VIN_MOD	VCC_12V	VIN_MOD	
VIN_MOD		VCC_12V	VIN_MOD	5	6	VIN_MOD	VCC_12V	VIN_MOD	
VIN_MOD		VCC_12V	VIN_MOD	7	8	VIN_MOD	VCC_12V	VIN_MOD	
-		-	NC	9	10	NC	-	-	
VCC_3V3		VCC_MOD_3V3	VREF_JTAG	11	12	PWR_EN	PWR_EN	PWR_EN	
VCC_3V3		VCC_MOD_3V3	VREF_JTAG	13	14	PG_MOD	PG_MOD	PG_MOD	
FPGA_TCK		FPGA_TCK	JTAG_TCK	15	16	Cold_nRST	nRSTIN	RESET_N	
FPGA_TDO		FPGA_TDO	JTAG_TDO	17	18	Warm_nRST	WARM_nRST	WARM_nRST	
GND		GND	GND	19	20	GND	GND	GND	
FPGA_TDI		FPGA_TDI	JTAG_TDI	21	22	IO_P	HDMI_RX_P0	B14_L18_P	U17
FPGA_TMS		FPGA_TMS	JTAG_TMS	23	24	IO_N	HDMI_RX_DE	B14_L18_N	U18
#N/A			IO_P	25	26	IO_P	HDMI_RX_HS	B14_L16_P	V17
#N/A			IO_N	27	28	IO_N	HDMI_RX_VS	B14_L16_N	W17
GND		GND	GND	29	30	GND	GND	GND	
B14_L24_P	P16	HDMI_RX_P1	IO_P	31	32	IO_P	GPIO2_13P	B13_L17_P	T16
B14_L24_N	R17	HDMI_RX_P2	IO_N	33	34	IO_N	GPIO2_13N	B13_L17_N	U16
B14_L19_P	P14	HDMI_RX_P3	IO_P	35	36	IO_P	GPIO2_12P	B13_L2_P	AB16
B14_L19_N	R14	HDMI_RX_P4	IO_N	37	38	IO_N	GPIO2_12N	B13_L2_N	AB17
GND		GND	GND	39	40	GND	GND	GND	
B13_L13_P_MRCC	V13	GPIO2_17P	IO_P	41	42	IO_P	GPIO2_11P	B13_L14_P_SRCC	U15

B13_L13_N_MRCC	V14	GPIO2_17N	IO_N	43	44	IO_N	GPIO2_11N	**B13_L14_N_SRCC**	V15
B13_L15_P	T14	GPIO2_16P	IO_P	45	46	IO_P	GPIO2_10P	B13_L1_P	Y16
B13_L15_N	T15	GPIO2_16N	IO_N	47	48	IO_N	GPIO2_10N	B13_L1_N	AA16
GND		**GND**	**GND**	49	50	**GND**	**GND**	**GND**	
B13_L5_P	Y13	GPIO2_15P	IO_P	51	52	IO_P	GPIO2_8P	B13_L4_P	AA15
B13_L5_N	AA14	GPIO2_15N	IO_N	53	54	IO_N	GPIO2_8N	B13_L4_N	AB15
B13_L16_P	W15	GPIO2_14P	IO_P	55	56	IO_P	GPIO2_7P	B13_L6_P	W14
B13_L16_N	W16	GPIO2_14N	IO_N	57	58	IO_N	GPIO2_7N	B13_L6_N	Y14
GND		**GND**	**GND**	59	60	**GND**	**GND**	**GND**	
#N/A		-	IO_P	61	62	IO_P	-	#N/A	
#N/A		-	IO_N	63	64	IO_N	-	#N/A	
#N/A		-	IO_P	65	66	Analog_in_P/IO_P	XADC_VP	XADC_VP	
#N/A		-	IO_N	67	68	Analog_in_N//IO_N	XADC_VN	XADC_VN	
GND		**GND**	**GND**	69	70	**GND**	**GND**	**GND**	
#N/A		-	IO	71	72	UART_RX	UART_RX	UART_RX	P20
#N/A		SD_CLK	SD_CLK	73	74	UART_TX	UART_TX	UART_TX	N15
#N/A		SD_CMD	SD_CMD	75	76	PHY_LED1	PHY0_LED1	PHY0_LED1	
#N/A		SD_DATA0	SD_DATA0	77	78	PHY_LED0	PHY0_LED0	PHY0_LED0	
GND		**GND**	**GND**	79	80	**GND**	**GND**	**GND**	
#N/A		SD_DATA1	SD_DATA1	81	82	ETH_MDI0_P	ETH0_MDI0_P	ETH0_MDI0_P	
#N/A		SD_DATA2	SD_DATA2	83	84	ETH_MDI0_N	ETH0_MDI0_N	ETH0_MDI0_N	
#N/A		SD_DATA3	SD_DATA3	85	86	ETH_MDI1_P	ETH0_MDI1_P	ETH0_MDI1_P	
#N/A		-	USB_CPEN	87	88	ETH_MDI1_N	ETH0_MDI1_N	ETH0_MDI1_N	
GND		**GND**	**GND**	89	90	**GND**	**GND**	**GND**	
#N/A		-	USB_DP	91	92	ETH_MDI2_P	ETH0_MDI2_P	ETH0_MDI2_P	
#N/A		-	USB_DN	93	94	ETH_MDI2_N	ETH0_MDI2_N	ETH0_MDI2_N	
#N/A		-	IO_P	95	96	ETH_MDI3_P	ETH0_MDI3_P	ETH0_MDI3_P	
#N/A		-	IO_N	97	98	ETH_MDI3_N	ETH0_MDI3_N	ETH0_MDI3_N	
GND		**GND**	**GND**	99	100	**GND**	**GND**	**GND**	