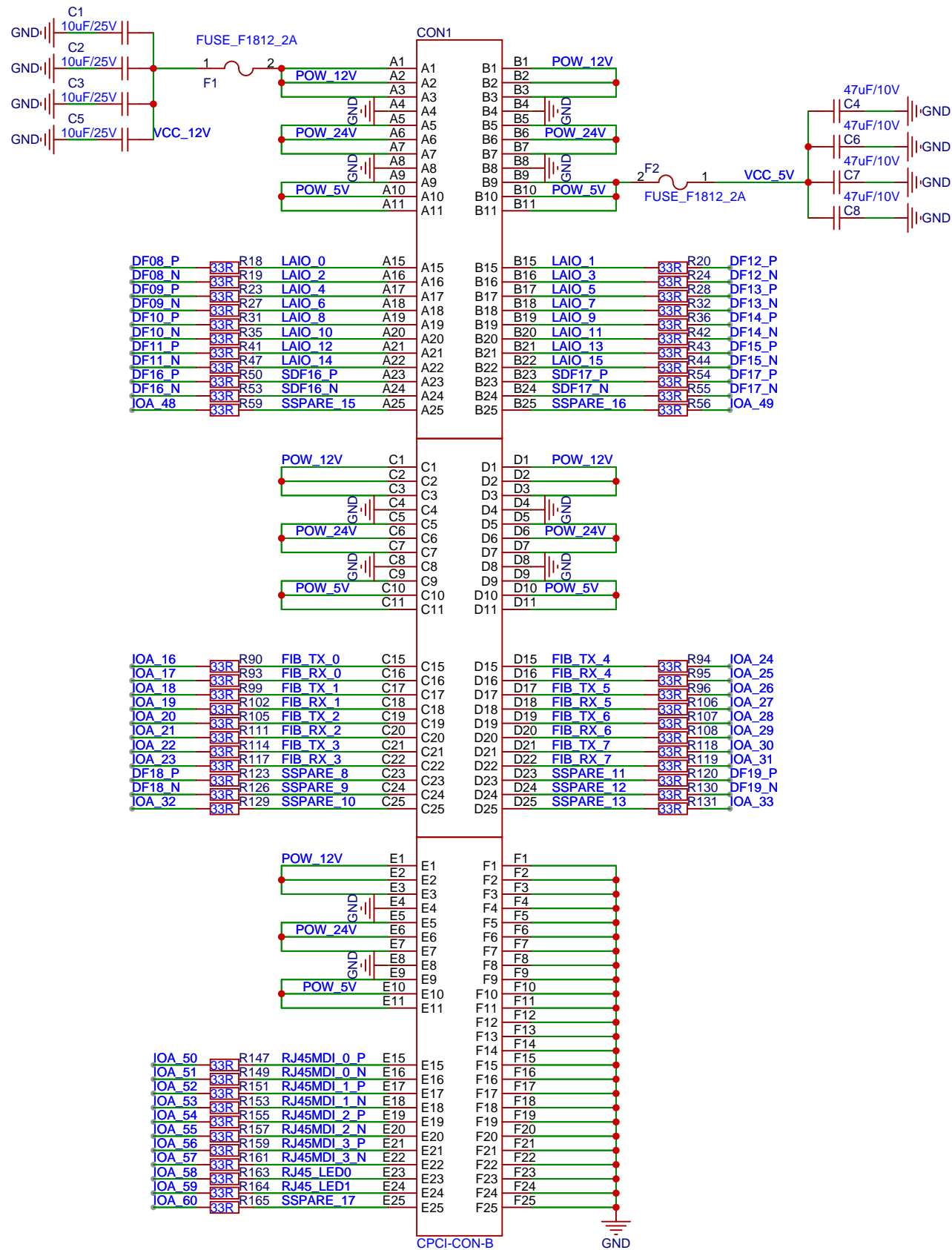
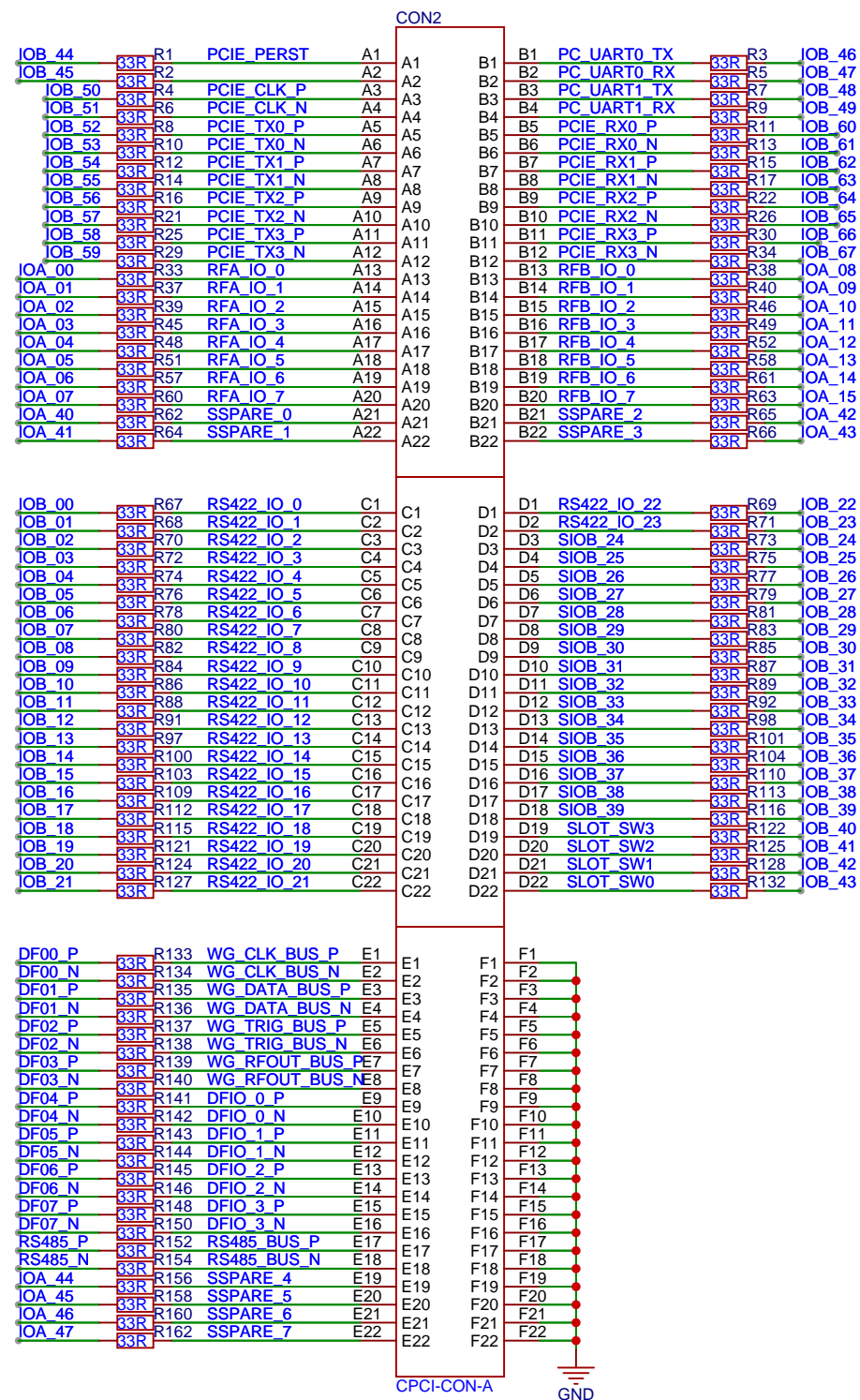

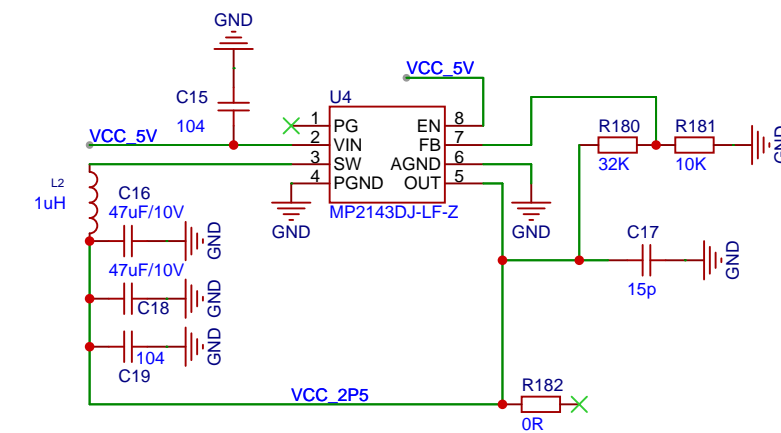
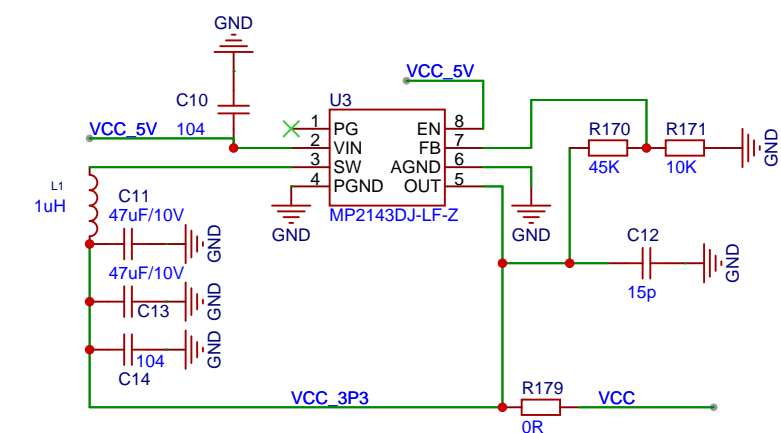
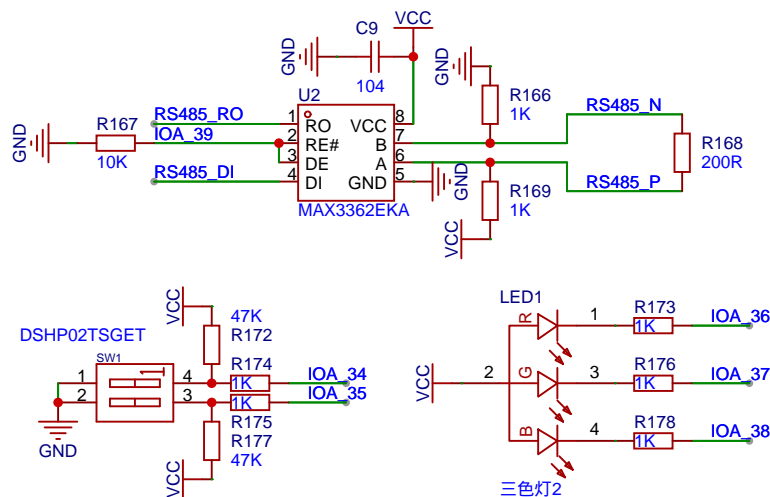


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				Create Date	2024-10-02
Page	P1			Part Number	
Drawn	KevinLiao	JS203-39-FPGA-B			
Reviewed					
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EasyEDA		V0.1	A4	JOSN	



Schematic	Schematic1			Update Date	2024-11-04
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Reviewed					
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		V0.1	A3	JOSN	



Schematic	Schematic1			Update Date	2024-11-04
				Create Date	2024-10-02
Page	P3			Part Number	
Drawn	KevinLiao	JS203-39-FPGA-B			
Reviewed					
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EasyEDA		V0.1	A4	JOSN	