



EK5133-G2 Series User Manual

Document Title

EK5133-G2 Series User Manual

Revision History

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0.0	Preliminary	July. 28, 2020	
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1. Introduction

This document describes how to use the EK5133-G2 Series. The software enables you to develop and debug firmware and prototype peripheral hardware for wireless applications based on the A5133 Series RF chip.

2. Software Install/Uninstall

The software is used to program the A5133 series device on the evaluation board. By using this software the designer of the radio system can easily evaluate the performance of RF IC in the early stage of the design process. It is further a useful tool during the generation of the configuration data and for finding optimized external component values.

2.1 Software Install/Uninstall

The software is written to run under Win 7 or Win 10 operating system. There should be a USB port in the target PC for the device control.

To install EK5133-G2 Series:

- 1) Start Windows, if you have not already done so.
- 2) Place the CD-ROM disk into the CD-ROM drive.
- 3) Select "Install EK5133-G2 Series" item to setup program.

Notes: Administrator privilege is required for installing the EK5133-G2 Series program on Win 7, Win10. If the user doesn't have the administrative right, this program may be not work while controlling the device with the USB port.

To uninstall EK5133-G2 Series:

- 1 Click the [Start] button under Windows.
- 2 Select "Control Panel" in Setup.
- 3 Double-click Add/Remove Programs.
- 4 Click Install/Uninstall. Then select EK5133-G2 Series from the list of programs that can be automatically removed.
- 5 Click the [Remove...] button to uninstall EK5133-G2 Series.

2.2. Driver Install

This software should be install PL2303 USB to Bridge (HX) install shield driver. The Wizard will guide you to complete the installation.

3. Hardware Description

3.1 Evaluation board

The Evaluation board provides two main functions. First, the I/O pin function of A5133 series can be set and measured on the evaluation board. Another, user can use EK5133-G2 Series software installed on PC to evaluate the A5133 The RF module is plugged into connector J2 on the evaluation board. The connector J1 should be connected to PC's USB port by USB cable. This evaluation board is shown in the Figure 1.

The following describe the function of key parts on the evaluation board (Figure 1)

1. USB B Type female connector (J1): It should be connected to PC's USB port by USB cable
2. RF Module connector: Plug RF module into RF module connector on the evaluation board.
3. Jumper J21: REGIPA. Please see below table 3.6 for detail description.
4. Jumper J10,J15: PA & LNA On/Off . Please see below table 3.5 for detail description.
5. I/O pin connector (J3): Please see below table 3.4 for detail description.
6. Jumper J7, J22: RF module supply voltage, I/O driving select. Select system +3.3V on board or extern power.
7. Jumper J8: EK Board power supply select. Select USB power supply or J6 Jack power supply.
8. Power supply (J6): Power supply is fed to the evaluation board through J6 and input range is DC +5.5V to +7V

Fig. 1 Topside of evaluation board

3.2 The pin definition of A01/A02

Pin No.	Pin name	Comment
1	VREG_IN	Chip's Regulator input
2	GND	GND
3	CKO	Multi pin CKO
4	GIO2	Multi pin IGPIO 2
5	GIO1	Multi pin IGPIO 1
6	SDIO	SPI data input/output
7	SCK	SPI clock
8	SCS	SPI chip select
9	GND	GND
10	XCLK	External crystal source input

3.3 The pin definition of F07

Pin No.	Pin name	Comment
1	GND	GND
2	REGIPA	Control A7700 Rx gain select
3	CKO	Multi pin clock output
4	GIO2	Multi pin IGPIO 2
5	GIO1	Multi pin IGPIO 1
6	SDIO	SPI data input/output
7	SCK	SPI clock
8	SCS	SPI chip select
9	REGI	Chip's Regulator input
10	GND	GND

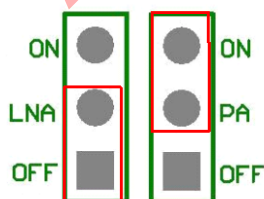
3.4 The pin definition of J3

Pin No.	Pin name	Comment
1	GIO1	Multi pin GPIO1
2	GIO2	Multi pin GPIO2
3	CKO	Multi pin CKO
4	XO	External crystal source input
5	GND	GND
6	-	NC
7	-	NC
8	GND	GND
9	GND	GND

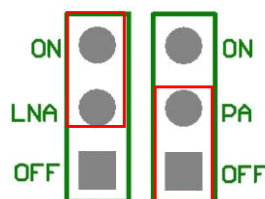
3.5 PA/LNA setting

The PA/LNA pin is control TRX switch select.

a) PA On setting



b) LNA On setting



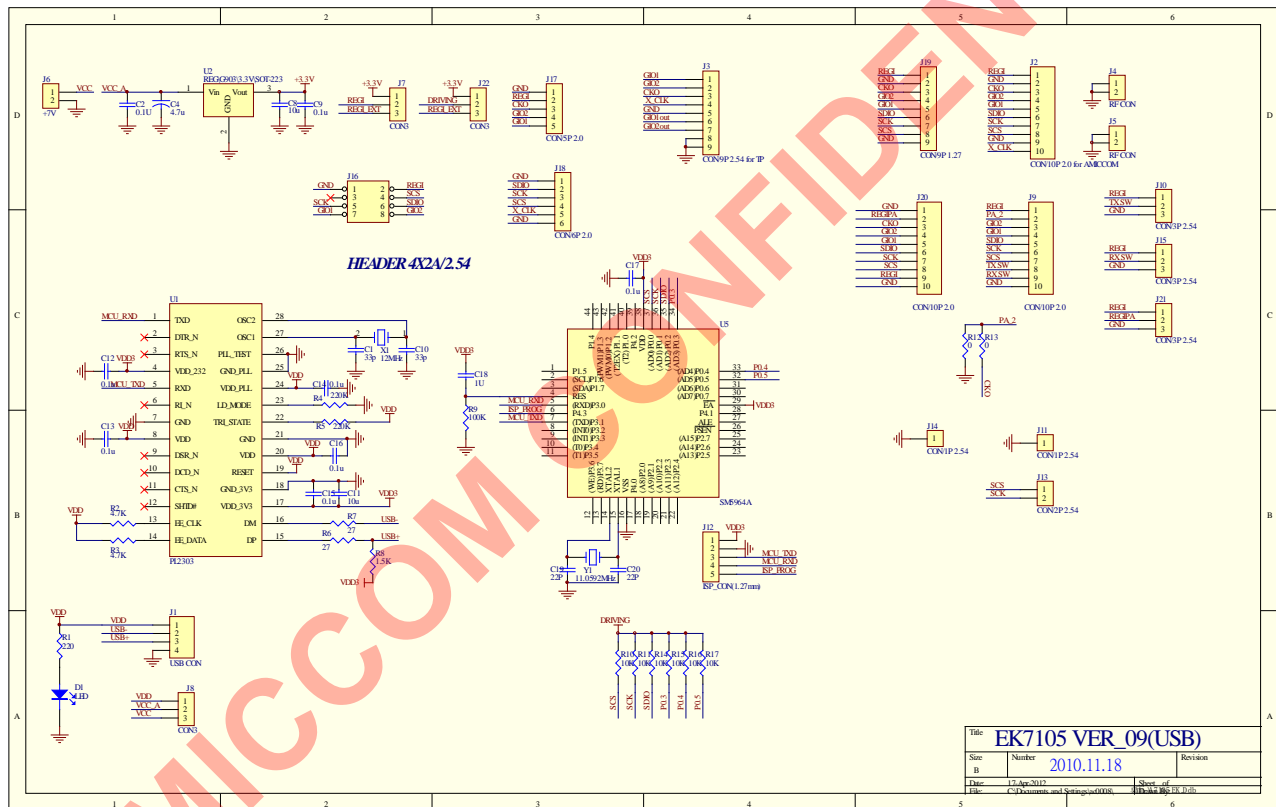
3.6 REGIPA setting

The REGIPA pin is control A7700 RX gain select.

- b) High gain mode setting b) Low gain mode setting



3.7 EV Board Schematic



4. Software Introduction

4.1 Using the EK5133-G2 Series software

The EK5133-G2 Series program Main Screen appears whenever you execute the program. The screen is shown below. Refer to the A5133 datasheet for detailed information on the register settings.

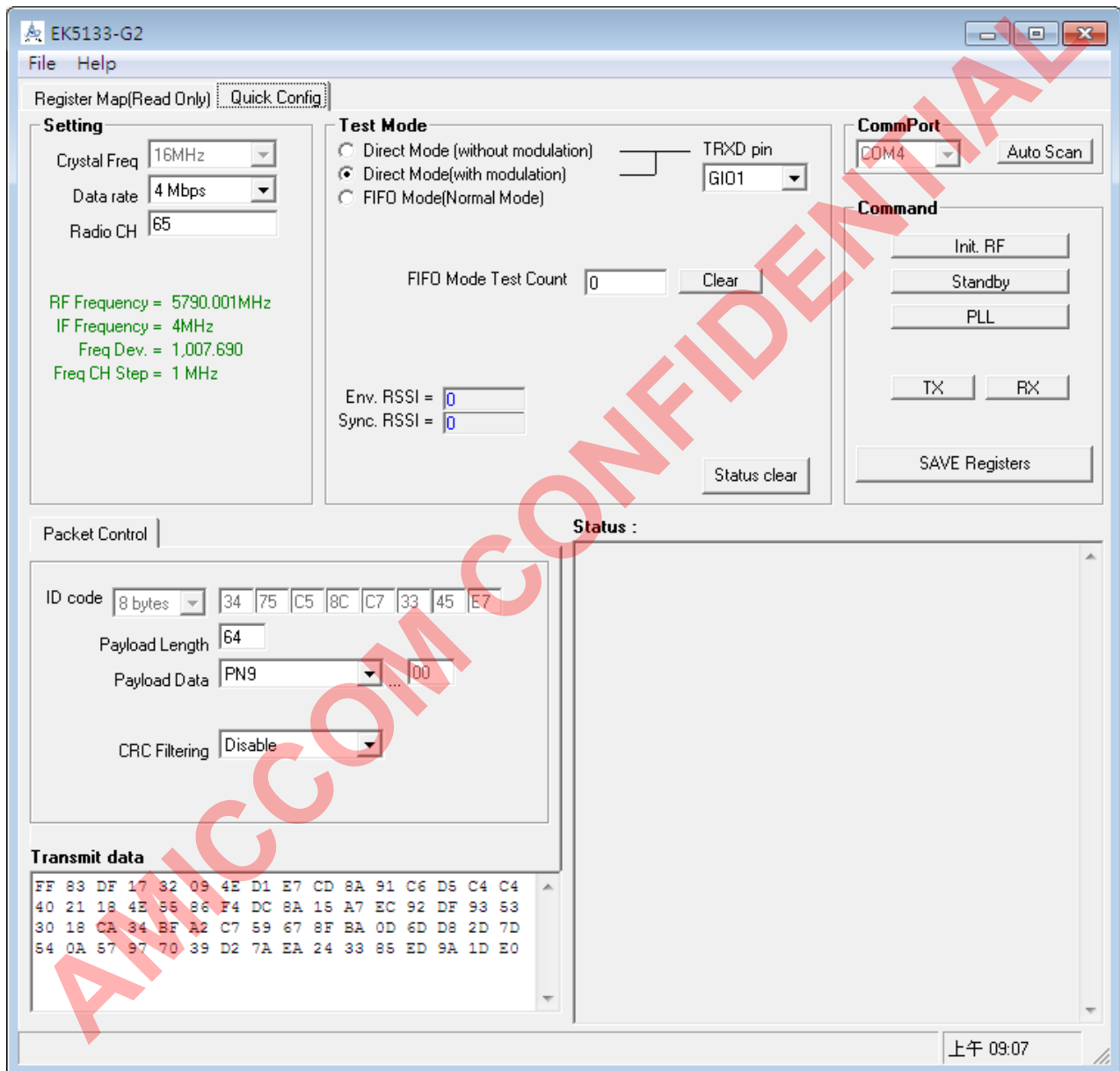
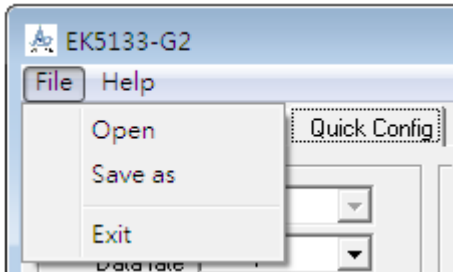


Fig. 2 EK5133-G2 Series program main screen

4.2 Pull-down menu

4.2.1 File menu options



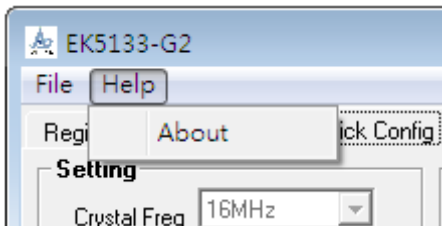
The File menu offers the following commands:

Open Reserved.

Save As Displays a file selection dialog box that asks you for the name of a CFG-file in which to save the entered system parameters.

Exit Exits EK5133-G2 Series program

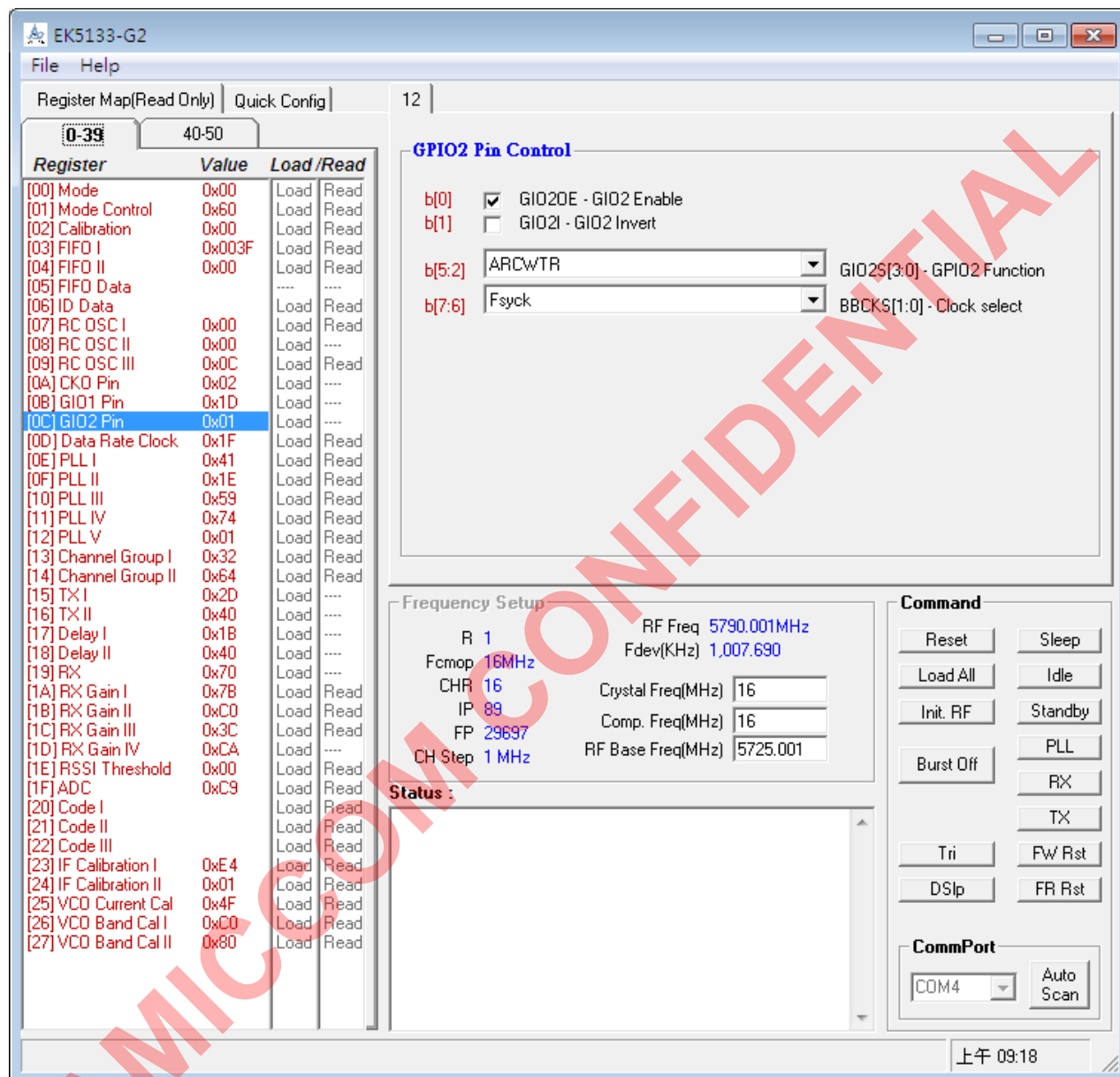
4.2.2 Help menu options



The help menu provides access to useful information about the product.

About brings up a message box with the software revision and copyright information.

4.3 Register Map (Read Only) Tab



Register

Register	Value	Load	Read
[00] Mode	0x00	Load	Read
[01] Mode Control	0x60	Load	Read
[02] Calibration	0x00	Load	Read
[03] FIFO I	0x003F	Load	Read
[04] FIFO II	0x00	Load	Read
[05] FIFO Data	----	----	----
[06] ID Data	----	Load	Read
[07] RC OSC I	0x00	Load	Read
[08] RC OSC II	0x00	Load	----
[09] RC OSC III	0x0C	Load	Read
[0A] CKO Pin	0x02	Load	----
[0B] GIO1 Pin	0x1D	Load	----
[0C] GIO2 Pin	0x01	Load	----
[0D] Data Rate Clock	0x1F	Load	Read
[0E] PLL I	0x41	Load	Read
[0F] PLL II	0x1E	Load	Read
[10] PLL III	0x59	Load	Read
[11] PLL IV	0x74	Load	Read
[12] PLL V	0x01	Load	Read
[13] Channel Group I	0x32	Load	Read
[14] Channel Group II	0x64	Load	Read
[15] TX I	0x2D	Load	----
[16] TX II	0x40	Load	----
[17] Delay I	0x1B	Load	----
[18] Delay II	0x40	Load	----
[19] RX	0x70	Load	----
[1A] RX Gain I	0x7B	Load	Read
[1B] RX Gain II	0xC0	Load	Read
[1C] RX Gain III	0x3C	Load	Read
[1D] RX Gain IV	0xCA	Load	----
[1E] RSSI Threshold	0x00	Load	Read
[1F] ADC	0xC9	Load	Read
[20] Code I	----	Load	Read
[21] Code II	----	Load	Read
[22] Code III	----	Load	Read
[23] IF Calibration I	0xE4	Load	Read
[24] IF Calibration II	0x01	Load	Read
[25] VCO Current Cal	0x4F	Load	Read
[26] VCO Band Cal I	0xC0	Load	Read
[27] VCO Band Cal II	0x80	Load	Read

GPIO2 Pin Control

b[0] ☒ GIO2OE - GIO2 Enable
b[1] ☐ GIO2I - GIO2 Invert
b[5:2] GIO2S[3:0] - GPIO2 Function
b[7:6] BBCKS[1:0] - Clock select

Frequency Setup

R 1 RF Freq 5790.001MHz
Fcmop 16MHz Fdev(KHz) 1,007.690
CHR 16 Crystal Freq(MHz) 16
IP 89 Comp. Freq(MHz) 16
FP 29697 RF Base Freq(MHz) 5725.001
CH Step 1 MHz

Command

Reset Sleep
Load All Idle
Init. RF Standby
Burst Off PLL
RX
TX
Tri FW Rst
DSip FR Rst

CommPort

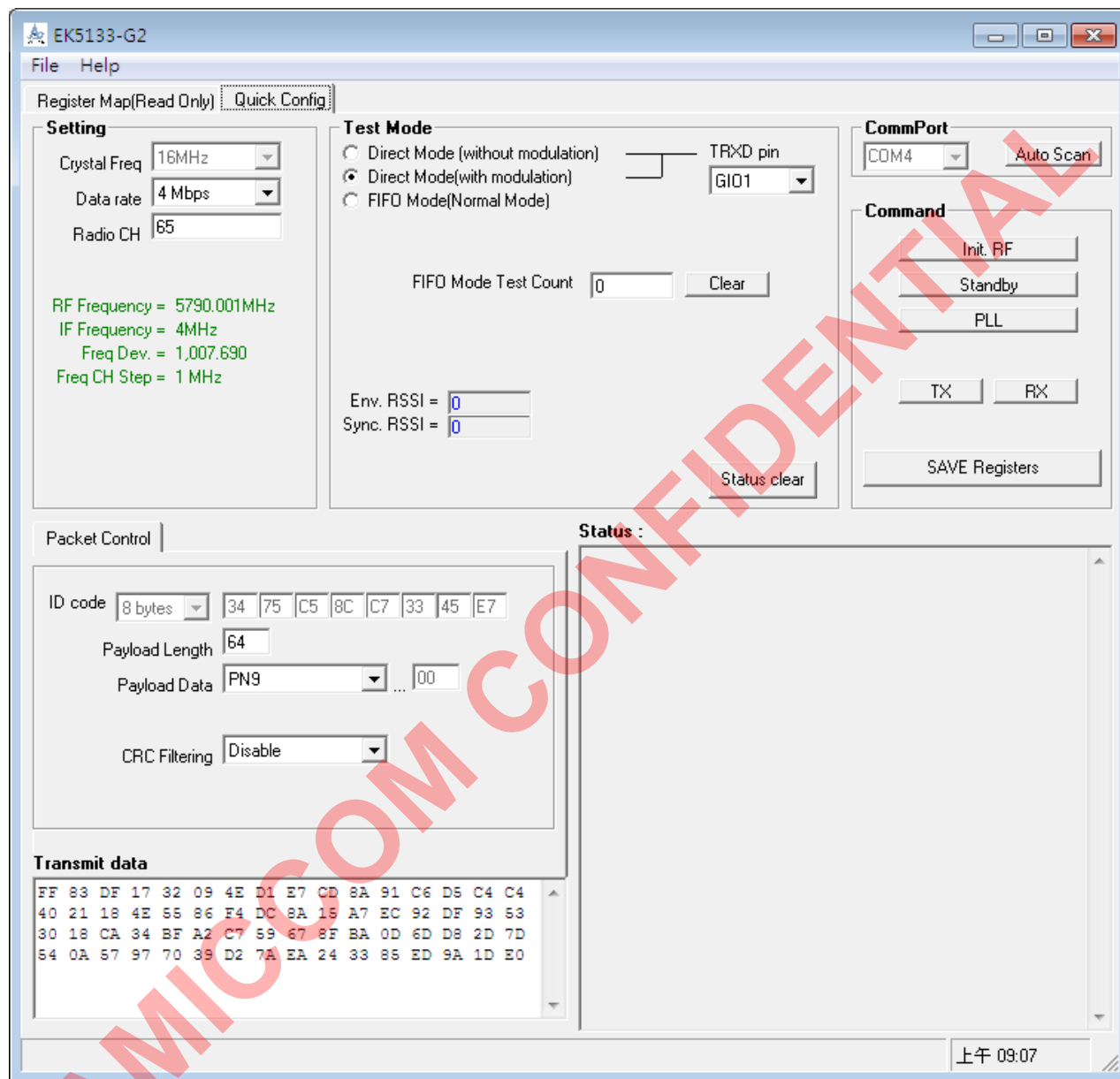
COM4 Auto Scan

上午 09:18

Register list

The register tab only shows the value that the EK5133-G2 Series is sending to the device, you can't change any parameter or button in the tab. When you change the different parameter value in Quick Config.Tab, the specified register in register Tab will change automatically.

4.4 Quick config. Tab



Setting

Crystal Freq

The crystal frequency of your module board is 16 MHz.

Do not change the crystal frequency parameter when using this module unless the crystal is being replaced.

Data rate

The data rate can be set between 4Mbps, 2Mbps, 1Mbps.

Radio CH

The A5133 series can operate at frequencies between 5725~5850 MHz ISM band. Enter the desired Channel number to set RF frequency.

Test Mode

The test mode can be set in direct mode (without modulation), direct mode(With modulation) or FIFO mode.
Note: Select "Direct mode (with modulation)" option, Program set GIO1 or GIO2 pin by TRXD pin option to transmit/receive data.

Comm Port

Pressing "Auto Scan" button, the program can scan available comm. port. If device detected, the button back color becomes orange.

Command

Pressing "Init RF" button, the program can initial A5133 series RF module automatically. If initial RF succeeds, the button back color becomes orange.

Pressing "Standby" button, the program can set the A5133 series RF module in Standby mode. The button back color becomes green.

Pressing "PLL" button, the program can set the A5133 series RF module in PLL mode. The button back color becomes green.

Pressing "TX" button, the program can set the A5133 series RF module in TX mode. The button back color becomes green.

Pressing "RX" button, the program can set the A5133 series RF module in RX mode. The button back color becomes green.

Save Registers

After completing the test, you may press "Save Register" button to save all register values in *.txt file.

Status

Status Info is shown on the text screen. The user can view message.

Packet Setting

TX Payload Length: The payload length default set 64 bytes.

Payload Data:

User can be set TX FIFO data pattern. In "Payload data" option, you can set in one of four modes.

Choosing "Fill PN9", it will display PN9 code in the Transmit data field.

Choosing "Fix Hex", it will display the fixed value in the Transmit data field.

Choosing "Customized Hex", you need enter 1~64 bytes value (Hex value) in the Transmit data field.

Choosing "Sequence pattern", the program can automatically update value with a sequence pattern to transmit a packet.

5. Setup Configuration install

Installation procedures

Step 1: Connect USB cable between PC and EK5133 Board. Plug A5133 series RF module on EK5133's socket.

Step 2: Enable EK5133-G2 Series program.

Step 3: Select "Quick config." Tab

Step 4: Pressing "Auto Scan" button to scan available Comm. Port.

Step 5: Pressing "Init. RF" button to initial RF module. If initial success, the button back color becomes orange and RF module setup into Standby state with "Standby" button back color becomes green.

Step 6: Or press "PLL" button to set RF module into PLL state.

Setup TX:

Step 1: Press "TX" button to set RF module into TX state.

Setup RX:

Step 1: Press "RX" button to set RF module into RX state.

6. References

[1] Refer to the A5133 series datasheet for detailed information on the register settings