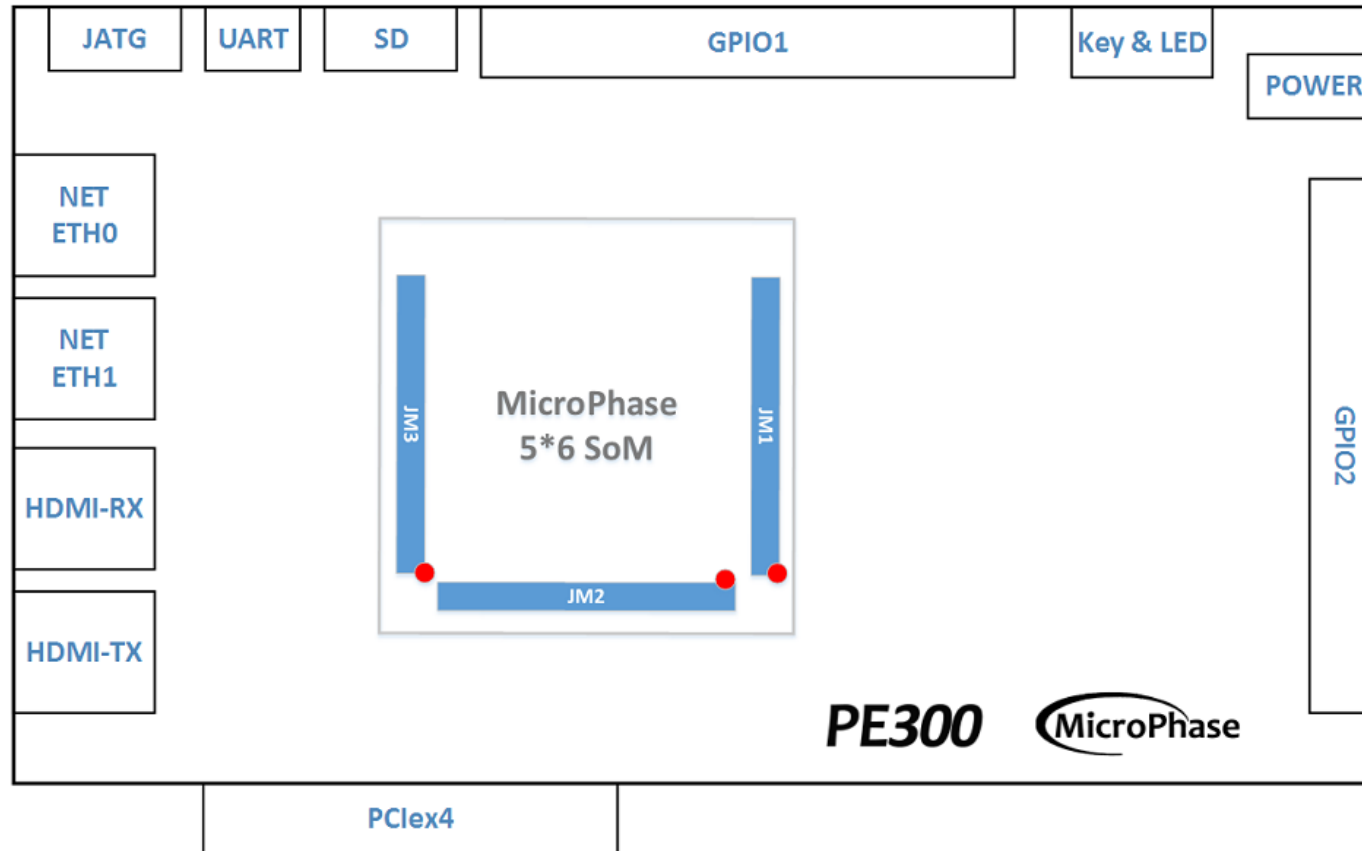


PE300 Block Diagram

REV	DATE	PAGES	DESCRIPTION
1.0	06/03/2019	All	Rev 1.0 Release
1.1	11/01/2019	All	Rev 1.1 Release

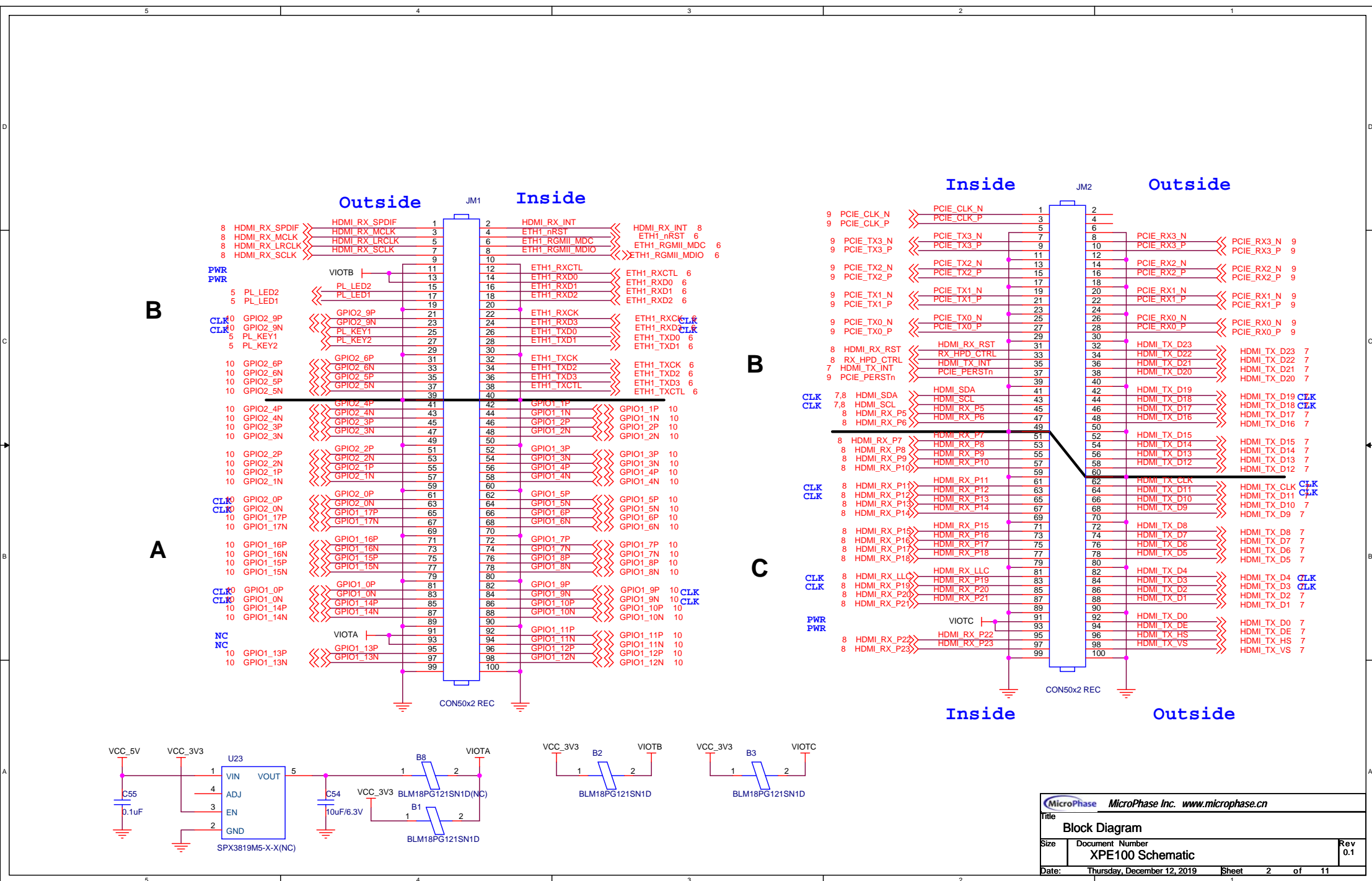


PAGE	DESCRIPTION
1	Title, Notes, Block Diagram, Revision History
2	B2B Connector: JM1, JM2
3	B2B Connector: JM3, JTAG
4	USB-UART, SD
5	ETH0, KEY, LED
6	ETH1
7	HDMI TX
8	HDMI RX
9	PCIe
10	GPIO Connector
11	Power
12	
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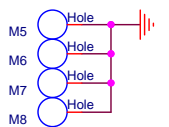
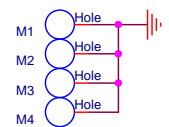
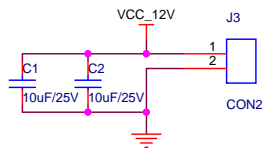
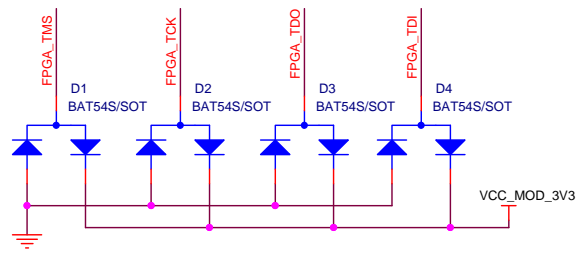
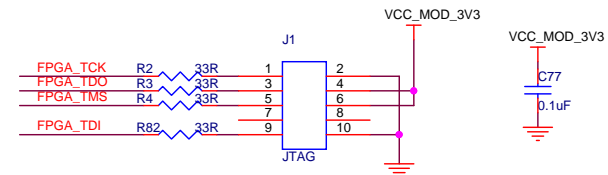
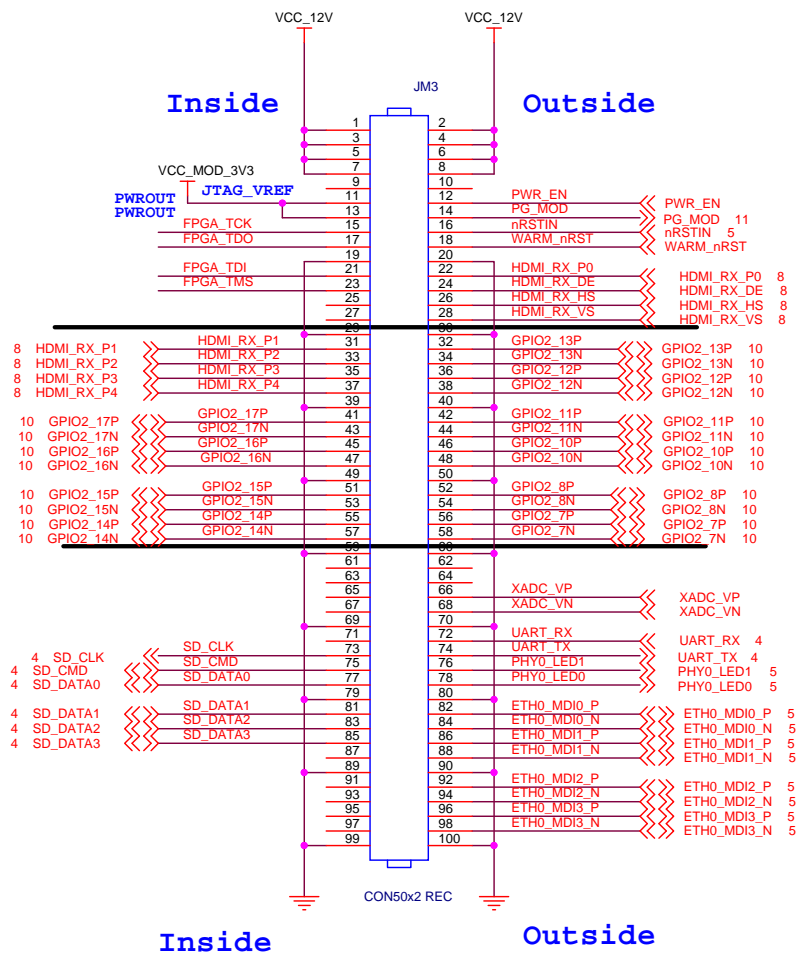
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Title Block Diagram		
Size	Document Number XPE100 Schematic	Rev 0.1
Date:	Thursday, December 12, 2019	Sheet 1 of 11

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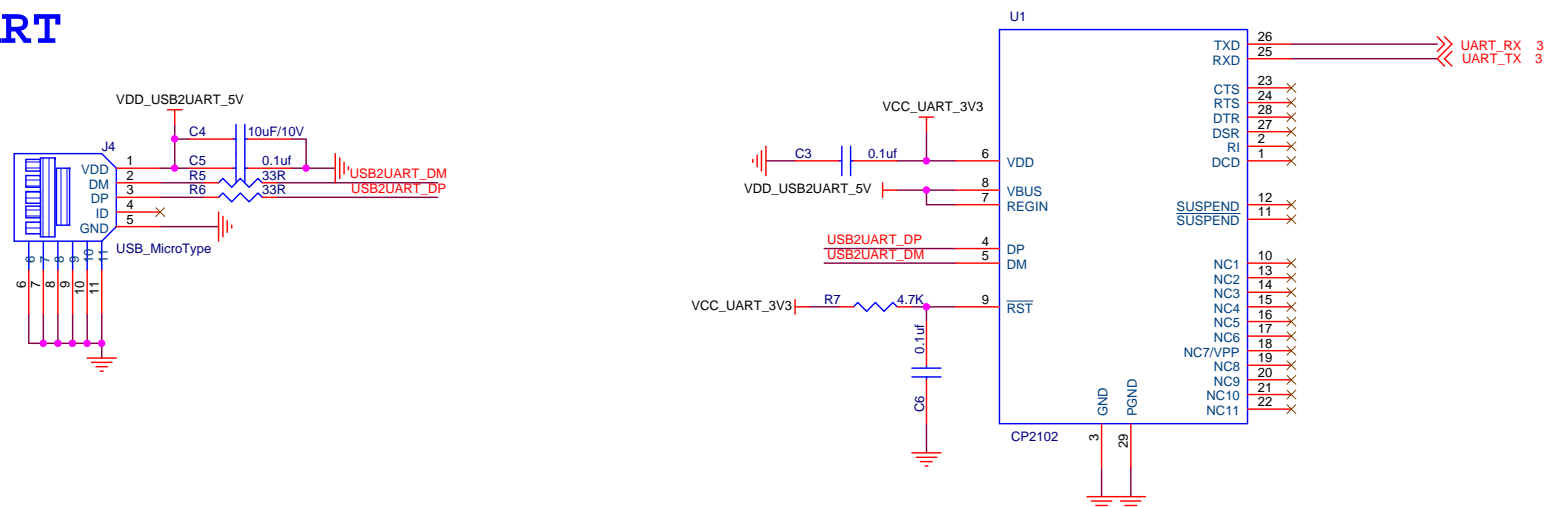
JTAG has not been assigned



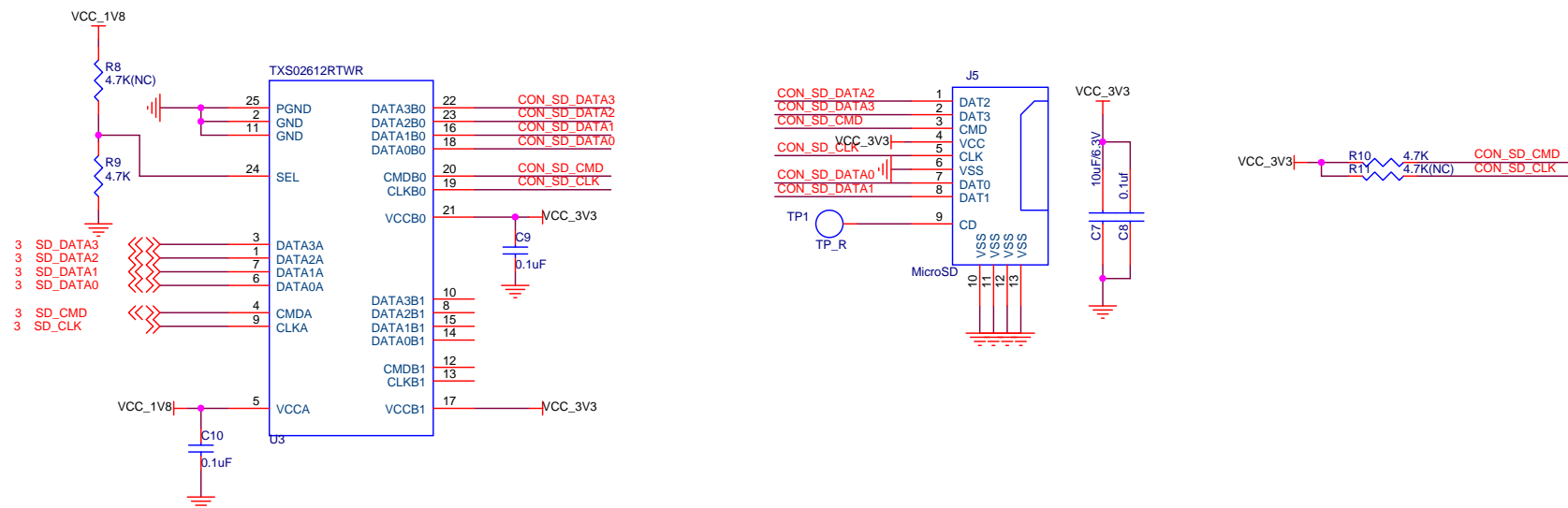
Mount.Hole 3.2mm

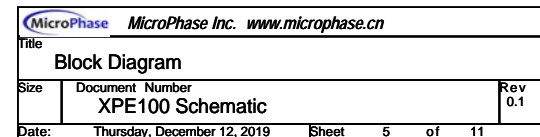
Mount.Hole Core Board Fixed

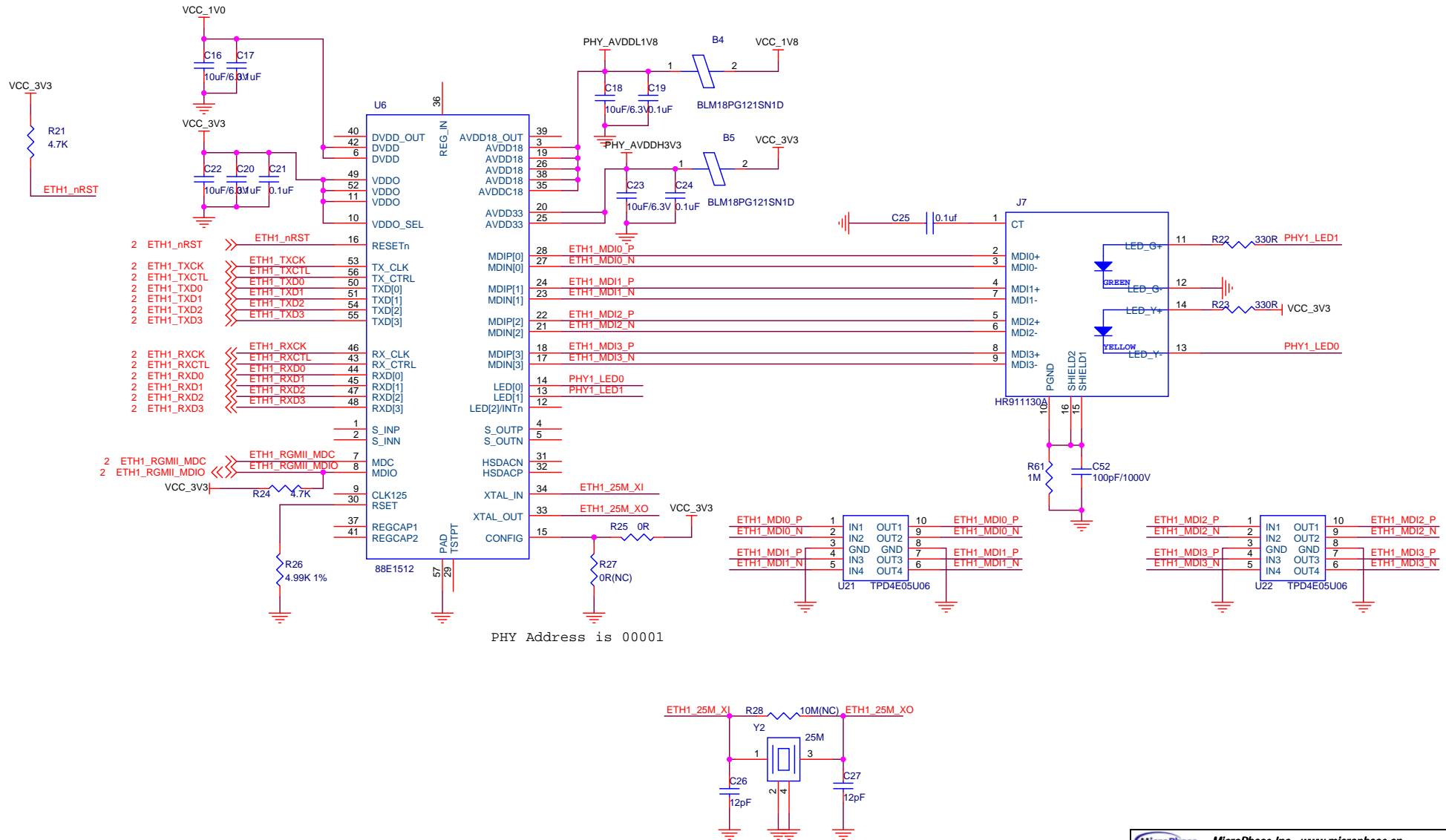
USB2UART

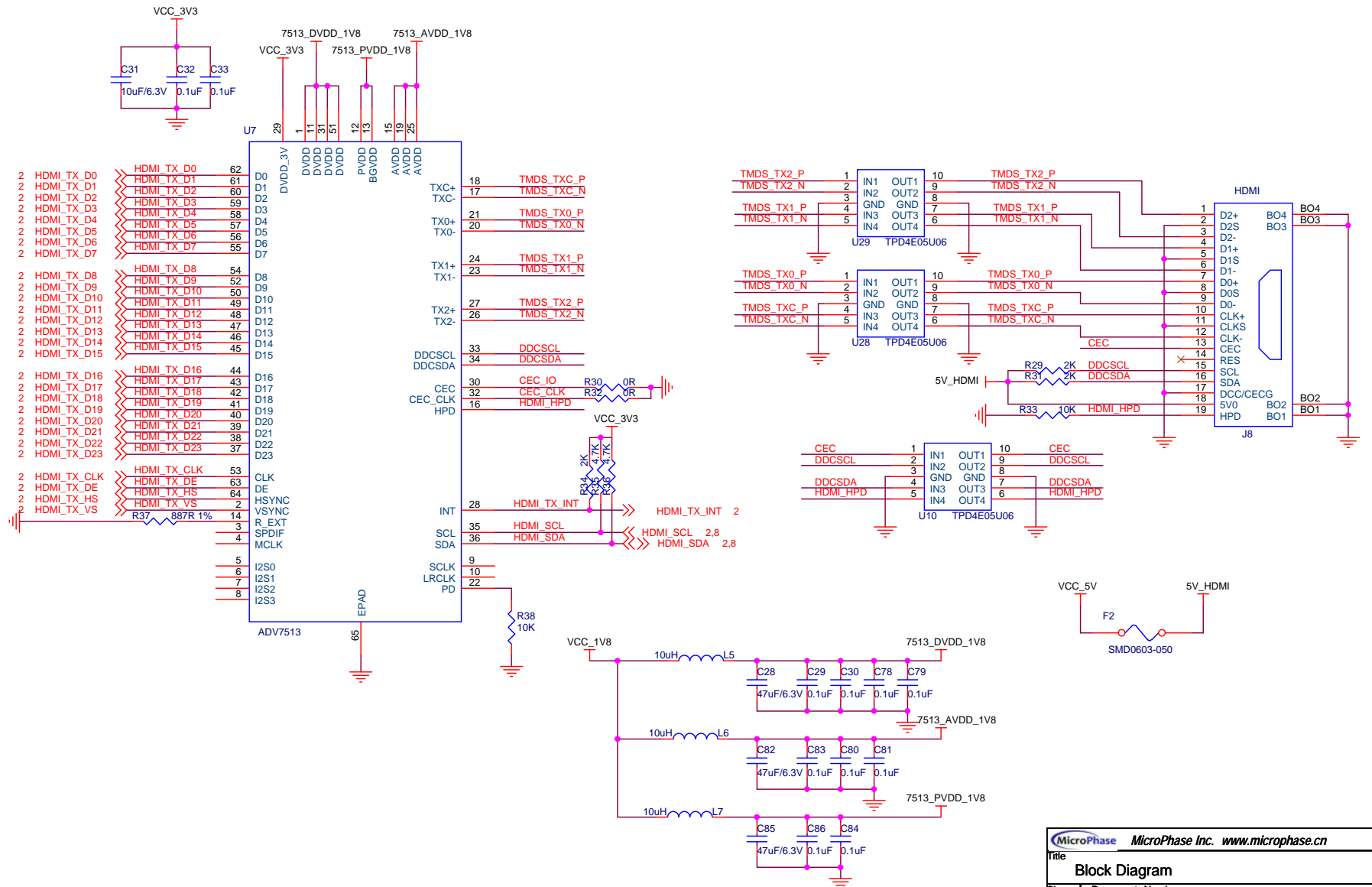


SD CARD(Only Support ZYNQ SoC Module)



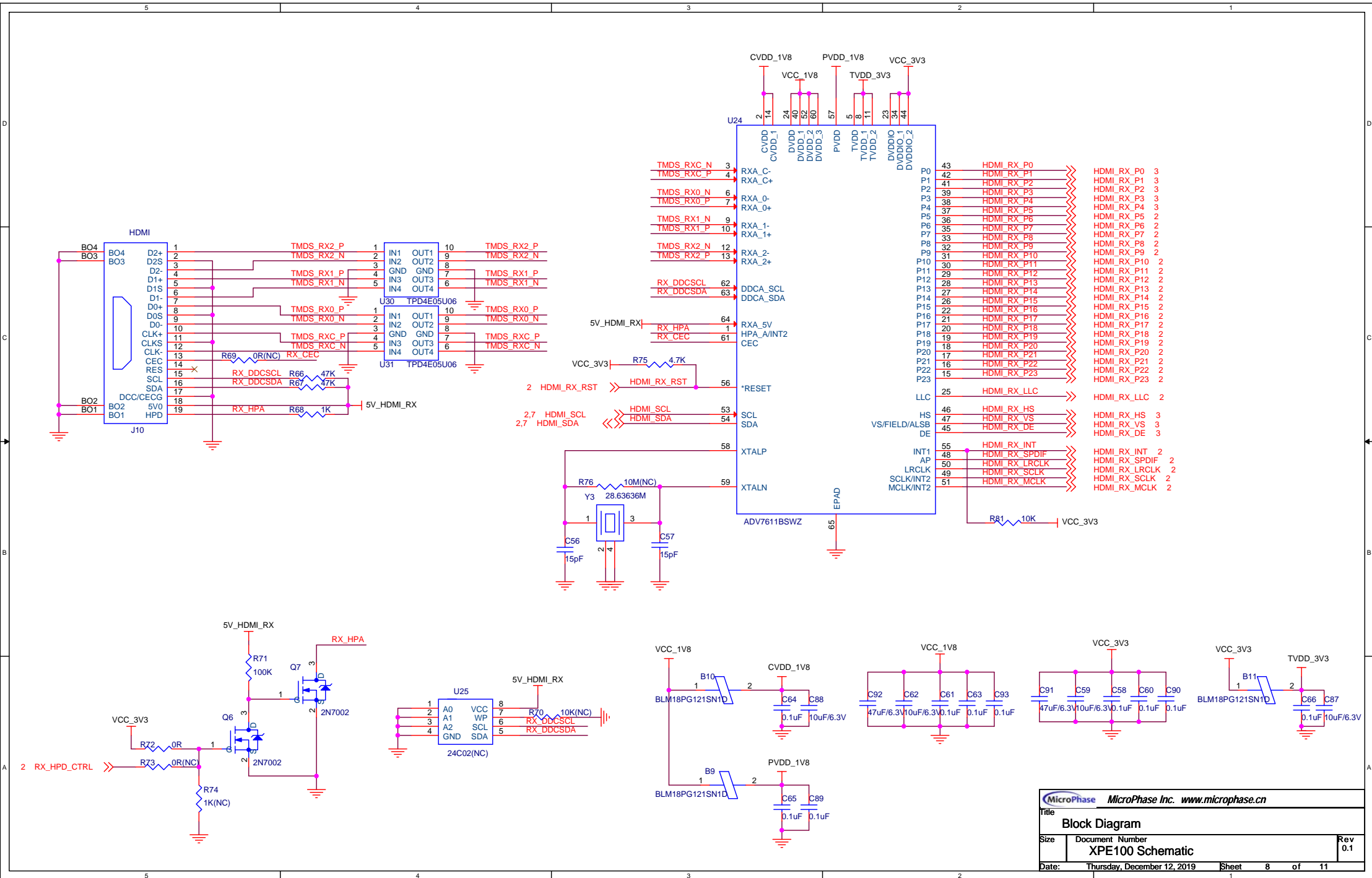


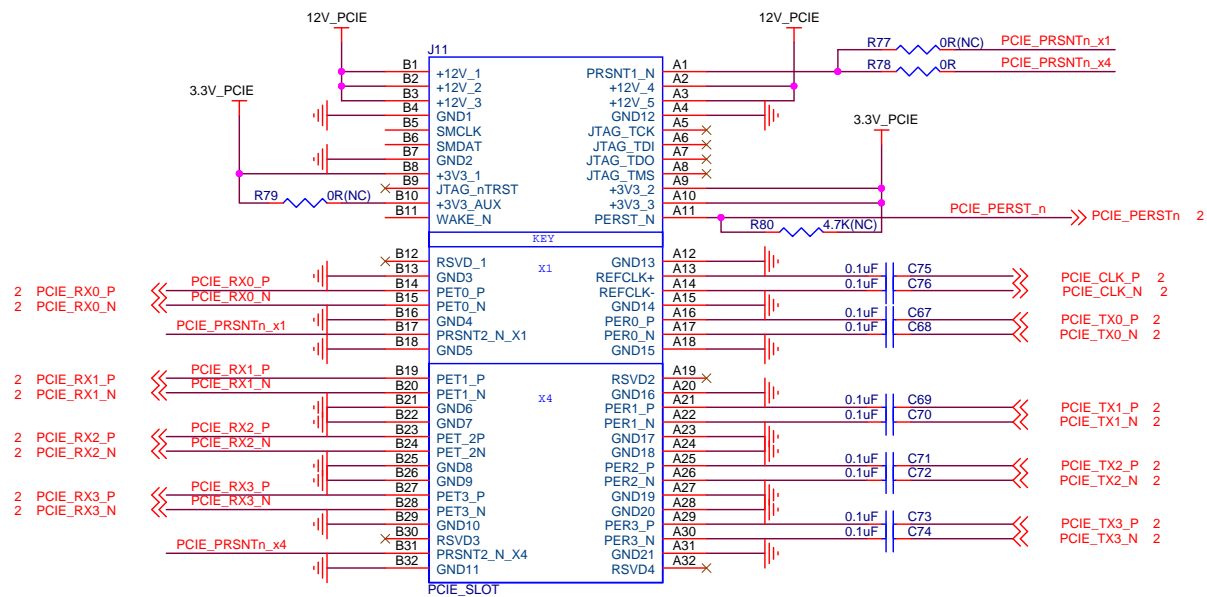


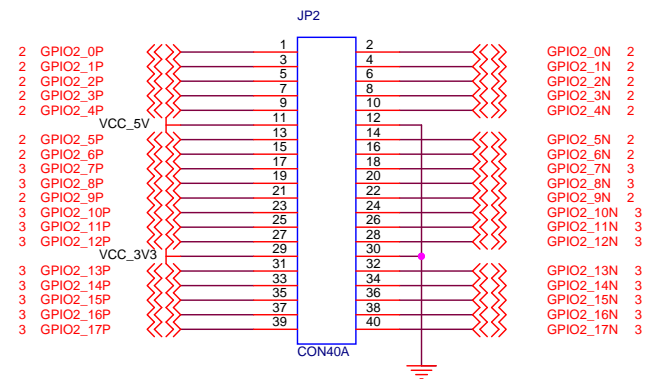
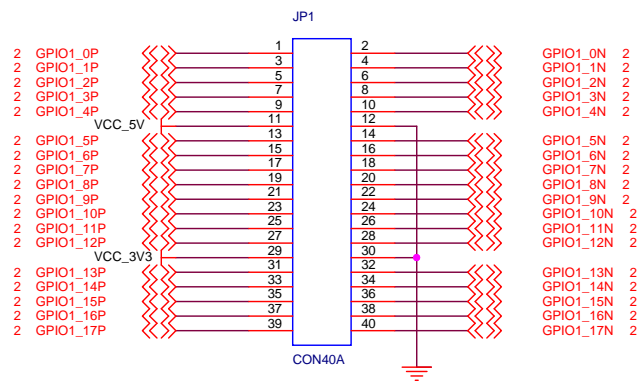


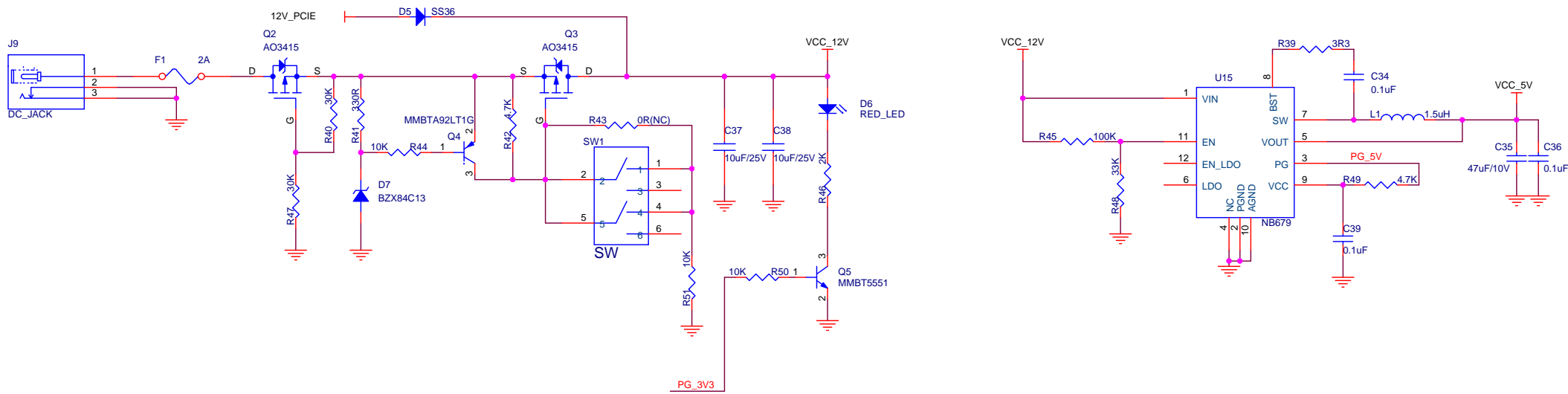
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Title		
Block Diagram		
Size	Document Number	Rev
	XPE100 Schematic	0.1
Date:	Thursday, December 12, 2019	Sheet 7 of 11










Vref = 0.6V VOUT = 1.0V

Vref = 0.6V VOUT = 1.8V

3 PG_MOD

Vref = 0.6V VOUT = 3.3V

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Title			
Block Diagram			
Size	Document Number		Rev
	XPE100 Schematic		0.1
Date:	Thursday, December 12, 2019	Sheet 11 of 11	