Shibao Su

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Lab 3 Part 2

A skewed-associative cache is a type of cache that uses simple hashing functions to map an address to distinct sets in each way of the cache. It is a type of cache memory system that combines aspects of both direct-mapped (single-level) and fully associative cache designs. The two-way skewed-associative cache has the same hardware complexity as a two-way set-associative cache. The skewed-associative cache provides a low miss rate without the increase the size or associativity and causes fewer requests to the main memory than a single level cache system. In summary, the skewed-associative cache minimized cache misses and reduced main memory requests, all within the constraints of hardware complexity making it a better choice than single level cache.