真理大學 107 學年度第1 學期 期末複習考 試題 第1頁/共4頁

考試科目	計算機結構	■大學 □碩士]大學進學]碩士在職班	資訊	L工程學系3年B班	命題老師	林熙	中
考試日期		-		案紙□是 大小■A4[試卷別■單一□A≯	&□B 卷	印刷份數	60
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- 1. To enhance data reliability, a memory system uses Hamming error correction code to detect and correct one bit-error. (16 points)
 - (a) Suppose 1010101 is the word to be written into memory. What is the resulting bit-sting stored in memory.
 - (b) Now consider the read operation of the memory system. What is the correct data word embedded in the bit-string 00110001100 retrieved from memory? Will any error be detected during this read operation?

- 2. By adding one extra parity bit, Hamming error correction code is capable of detecting a double-bit error and correcting a single-bit error. (For simplicity, assume that the extra parity bit is placed as the left-most bit.)
 - (a) Suppose 1010101 is the word to be written into memory. What is the resulting bit-sting stored in memory.
 - (b) Now consider the read operation of the memory system. What is the correct data word embedded in the bit-string 000110001100 retrieved from memory? Will any error be detected during this read operation?

(8 points)

3. Answer the following questions concisely and precisely?	(8 point
(a) Explain the technique of "CAV" used in magnetic(b) Explain the technique of "CLV" used in optical dis	
4. Consider a magnetic disk drive with 4 surfaces, 256 trais 1KB and the drive rotates at 3000rpm. Its average se Successive tracks in a cylinder can be read in turn without can not be read simultaneously.	eek time is 4ms, while track-to-track access time is 2m
(a) What is the disk capacity?	
(b) What is the average access time?	
(c) Estimate the time required to transfer a 800KB file	
(d) Estimate the time required to transfer a 800KB file(e) What is burst transfer rate (the highest speed at w	· ·
(e) What is suffer transfer rate (the inglices speed as in	, and add can be transcribed).
5. Find the sum of 01111001 and 00010111 assuming number	bers are represented in (8 point
(a) sign-magnitude representation;	· · ·
(b) twos complement.	
(a)	(b)

How to expend the bit length of an integer in two complement representation? Explain why it wor	ks. (8 points)
Given $x = 10011$ and $y = 11101$ in two complement notation, do the following arithmetic.	
 (a) x - y. (b) x × y. (Use the Booth algorithm and you should describe how registers are changed.) 	(4 points)
(c) x / y . (Use the algorithm described in Sect.10.3 and describe how registers are changed.)	(8 points
(a) $x-y$.	
(b) $x \times y$.	

Consider a reduced 16-bit floating point format, where the left most bit is a sign bit, the following 9 bits represe he exponent and the other 6 bits at the right-hand side are for the significand. For simplicity, we assume the alues are normalized and the exponent is coded in biased representation. Please answer the following question (16 point) (a) Express (-121/16) ₁₀ in this format. (b) What is the equivalent decimal value for (1100 0011 100) ₂ ? (c) What is the smallest positive number (in decimal) that can be coded with this format? (d) What is the smallest number (in decimal) that can be coded with this format?	(c) x / y .						
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