

SafeTI™ Hercules™ Diagnostic Library Release version 2.3.2



The table below lists the memory requirements when integrating the library built in release mode.

when integrating the library built in release mode.				
MEMORY STATISTICS (IN BYTES) for the				
SafeTI Hercules Diag	gnostic Libr	ary		
RM48/TMS5	70LS31			
Program	Data	Total		
20032	505	20537		
RM46/TMS570I	S12x/11x			
Program Data Total				
20364	505	20869		
RM42/TMS570I	S04x/03x			
Program	Data	Total		
18040	503	18543		
RM57L/TMS5	RM57L/TMS570LC43x			
Program	Program Data Total			
15352 486 15838				

MEMORY STATISTICS (IN BYTES) for the			
TPS65381 c	driver		
RM4 devices with	VFP suppo	rt	
Program Data Total			
11304	252	11556	
RM4 devices withou	it VFP supp	ort	
Program Data Total			
11304	252	11556	
TMS570LS devices wi	ith VFP sup	port	
Program	Data	Total	
11316	252	11568	
TMS570LS devices without VFP support			
Program Data Total			
11316 252 11568			



Profiling data for SafeTI™ Hercules™ Diagnostic Library API.

Notes: Measurements were made on an RM42 and TMS570LS04 device, with HCLK = 80 MHz.

Some tests do not generate an ESM event, and hence the time to handler is not available. Fault injection timings depend on the handling done by application, and hence is not profiled. Please refer to the API user's guide for details on fault injection.

Timings are indicative, appropriate care should be taken by application when using the data provided here depending on application scenario.

Safett Diagnostic Library Test	CofeTi Disamentia Liberary Test	Timing Dangerous auto (ma)
SRAM_ECC_IBIT_FAULT_INJECTION SRAM_ECC_ERROR_FORCING_2BIT SRAM_ECC_ERROR_FORCING_2BIT SRAM_ECC_ERROR_FORCING_2BIT SRAM_PA_ADDR_CTRL_SELF_TEST 223.6 SRAM_ECC_ERROR_PROFILING NA ADC_SELFIEST_ALL S3.475 NA ADC_SELFIEST_ALL S3.475 ADC_SELFIEST_ALL S3.475 ADC_SELFIEST_ALL S3.475 ADC_SRAM_PARITY_TEST 7.175 FEE_ECC_SYN_REPORT_MODE 11.3375 FEE_ECC_SYN_REPORT_MODE 7.925 FEE_ECC_MALFUNCTION_MODE1 8.0375 FEE_ECC_MALFUNCTION_MODE1 8.0375 FEE_ECC_CADR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_1BIT 12.5375 FEE_ECC_TEST_MODE_1BIT FEE_ECC_TEST_MODE_2BIT 11.725 FEE_ECC_TEST_MODE_2BIT FEE_ECC_TEST_MODE_2BIT FILASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT PLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT NA FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT NA PLASH_ECC_TEST_MODE_2BIT NA PLASH_ECC_TEST_MODE_2BIT NA PLASH_ECC_TEST_MODE_2BIT NA PLASH_ECC_TEST_MODE_2BIT NA PLASH_ECC_TEST_MODE_2BIT NA PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	,	
SRAM_ECC_ERROR_FORCING_2BIT 219.375 SRAM_ECC_2BIT_FAULT_INJECT NA SRAM_PAR_ADDR_CTRL_SELF_TEST 223.6 SRAM_PAR_ADDR_CTRL_SELF_TEST 223.6 SRAM_ECC_ERROR_PROFILING NA SRAM_ECC_ERROR_PROFILING NA SRAM_ECC_ERROR_PROFILING NA SRAM_ENDELOCK_DIAGNOSTICS 419.6 SRAM_LIVELOCK_DIAGNOSTICS NA ADC_SELFTEST_ALL 53.475 ADC_SRAM_PARITY_TEST 7.175 FEE_ECC_DATA_CORR_MODE 11.3375 FEE_ECC_DATA_CORR_MODE 7.925 FEE_ECC_MALFUNCTION_MODE 7.925 FEE_ECC_MALFUNCTION_MODE 10.8375 FEE_ECC_MALFUNCTION_MODE 10.8375 FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_1BIT 11.725 FEE_ECC_TEST_MODE_1BIT_FAULT_INJECT NA FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST 218.8375 FLASH_ECC_TEST_MODE_1BIT 11.55		
SRAM_ECC_2BIT_FAULT_INJECT NA SRAM_PAR_ADDR_CTRL_SELF_TEST 223.6 SRAM_ECC_ERROR_PROFILING NA SRAM_ECC_ERROR_PROFILING NA SRAM_ECC_ERROR_PROFILING NA SRAM_ECC_ERROR_PROFILING NA SRAM_ECC_ERROR_PROFILING NA SRAM_LIVELOCK_DIAGNOSTICS NA ADC_SELFTEST_ALL 53.475 ADC_SRAM_PARITY_TEST 7.175 FEE_ECC_DATA_CORR_MODE 11.3375 FEE_ECC_SYN_REPORT_MODE 7.925 FEE_ECC_SYN_REPORT_MODE 7.925 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_TEST_MODE_1BIT 12.5375 FEE_ECC_TEST_MODE_1BIT 12.5375 FEE_ECC_TEST_MODE_1BIT 12.5375 FEE_ECC_TEST_MODE_2BIT 11.725 FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST 218.8375 FLASH_		
SRAM_PAR_ADDR_CTRL_SELF_TEST 223.6 SRAM_ECC_ERROR_PROFILING NA SRAM_ECC_ERROR_PROFILING_FAULT_INJECT NA SRAM_LECL_DERROR_PROFILING_FAULT_INJECT NA SRAM_LIVELOCK_DIAGNOSTICS NA ADC_SELFTEST_ALL 53.475 ADC_SRAM_PARITY_TEST 7.175 FEE_ECC_DATA_CORR_MODE 11.3375 FEE_ECC_SYN_REPORT_MODE 7.925 FEE_ECC_MALFUNCTION_MODE1 8.0375 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_1BIT 12.5375 FEE_ECC_TEST_MODE_1BIT 12.5375 FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST 218.8375 FLASH_ADDRESS_PARITY_SELF_TEST NA FLASH_ECC_TEST_MODE_2BIT 11.55 FLASH_ECC_TEST_MODE_2BIT 11.55 FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT		
SRAM_ECC_ERROR_PROFILING SRAM_ECC_ERROR_PROFILING_FAULT_INJECT NA SRAM_ENDECODE_DIAGNOSTICS ADC_SELFTEST_ALL S3.475 ADC_SELFTEST_ALL S3.475 ADC_SRAM_PARITY_TEST ADC_SRAM_PARITY_TEST FEE_ECC_DATA_CORR_MODE FEE_ECC_DATA_CORR_MODE FEE_ECC_MALFUNCTION_MODE1 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_TEST_MODE_IBIT FEE_ECC_TEST_MODE_IBIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FIASH_ECC_ADDR_TAG_REG_MODE NA FELASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_PARITY_SAULT_INJECT NA FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_BIT 11.55 FLASH_BCC_TEST_MODE_BIT 11.55 FLASH_BCC_TEST_MODE_BIT NA FLASH_ECC_TEST_MODE_BIT NA FLASH_ECC_TEST_MODE_BIT NA FLASH_ECC_TEST_MODE_BIT NA PLASH_ECC_TEST_MODE_BIT NA PLASH_ECC_TEST_MODE_BIT NA PLASH_ECC_TEST_MODE_BIT NA PLASH_ECC_TEST_MODE_BIT NA PSCON_ERROR_ENCING NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING		
SRAM_ECC_ERROR_PROFILING_FAULT_INJECT SRAM_RADECODE_DIAGNOSTICS ADC_SELFTEST_ALL ADC_SRAM_PARITY_TEST ADC_SRAM_PARITY_TEST ADC_SRAM_PARITY_TEST FEE_ECC_DATA_CORR_MODE FEE_ECC_SYN_REPORT_MODE FEE_ECC_SYN_REPORT_MODE FEE_ECC_MALFUNCTION_MODE1 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_BIIT FEE_ECC_TEST_MODE_BIIT FEE_ECC_TEST_MODE_BIIT FEE_ECC_TEST_MODE_BIIT FEE_ECC_TEST_MODE_BIIT FIL725 FEE_ECC_TEST_MODE_BIIT FIL725 FEE_ECC_TEST_MODE_BIIT FIL725 FEE_ECC_TEST_MODE_BIIT FIL725 FEE_ECC_TEST_MODE_BIIT_FAULT_INJECT NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST PLASH_ADDRESS_PARITY_SELF_TEST PLASH_ADDRESS_PARITY_SELF_TEST PLASH_ADDRESS_PARITY_SELF_TEST PLASH_ADDRESS_PARITY_SELF_TEST PLASH_ADDRESS_PARITY_SELF_TEST NA FLASH_ECC_TEST_MODE_BIIT FLASH_ECC_TEST_MODE_BIIT 11.55 FLASH_ECC_TEST_MODE_BIIT 11.55 FLASH_ECC_TEST_MODE_BIIT NA FLASH_ECC_TEST_MODE_BIIT NA FLASH_ECC_TEST_MODE_BIIT NA PLASH_ECC_TEST_MODE_BIIT NA PROON_ERROR_FORCING NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA		
SRAM_RADECODE_DIAGNOSTICS		
SRAM_LIVELOCK_DIAGNOSTICS ADC_SELFTEST_ALL 53.475 ADC_SRAM_PARITY_TEST FEE_ECC_DATA_CORR_MODE FEE_ECC_SYN_REPORT_MODE FEE_ECC_SYN_REPORT_MODE FEE_ECC_MALFUNCTION_MODE1 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_TAST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_SBIT FEL_ECC_TEST_MODE_SBIT FEL_ECC_TEST_MODE_SBIT FEL_ECC_TEST_MODE_SBIT FEL_ECC_TEST_MODE_SBIT FEL_ECC_TEST_MODE_SBIT FILASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_BIT FLASH_ECC_TEST_MODE_BIT FLASH_ECC_TEST_MODE_BIT ALSS FLASH_HARD_ERROR_LIVELOCK NA PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	SRAM_ECC_ERROR_PROFILING_FAULT_INJECT	
ADC_SELFTEST_ALL ADC_SRAM_PARITY_TEST ADC_SRAM_PARITY_TEST FEE_ECC_DATA_CORR_MODE FIE_ECC_SYN_REPORT_MODE FEE_ECC_MALFUNCTION_MODE1 FEE_ECC_MALFUNCTION_MODE1 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_IBIT FEE_ECC_TEST_MODE_IBIT FEE_ECC_TEST_MODE_ZBIT FEE_ECC_TEST_MODE_ZBIT FELASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_CCC_TEST_MODE_IBIT FLASH_ECC_TEST_MODE_ZBIT FLASH_CCC_TEST_MODE_ZBIT FLASH_CCC_TEST_MODE_ZBIT_FAULT_INJECT NA PLASH_CCC_TEST_MODE_ZBIT_FAULT_INJECT NA PSCON_SELF_TEST_CONG NA PSCON_SELF_TEST_CONG NA PSCON_SELF_TEST_CONG NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	SRAM_RADECODE_DIAGNOSTICS	419.6
ADC_SRAM_PARITY_TEST 7.175 FEE_ECC_DATA_CORR_MODE 11.3375 FEE_ECC_SYN_REPORT_MODE 7.925 FEE_ECC_MALFUNCTION_MODE1 8.0375 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_TAST_MODE_BIST 12.5375 FEE_ECC_TEST_MODE_BIST 12.5375 FEE_ECC_TEST_MODE_BIST_FAULT_INJECT NA FEE_ECC_TEST_MODE_ZBIT 11.725 FEE_ECC_TEST_MODE_ZBIT_FAULT_INJECT NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST 218.8375 FLASH_ADDRESS_PARITY_SELF_TEST 218.8375 FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_ZBIT 11.55 FLASH_ECC_TEST_MODE_ZBIT 11.55 FLASH_ECC_TEST_MODE_ZBIT 31.55 FLASH_ECC_TEST_MODE_ZBIT 32.75 PBIST_EXECUTE NA PBIST_EXECUTE NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	SRAM_LIVELOCK_DIAGNOSTICS	NA
FEE_ECC_DATA_CORR_MODE FEE_ECC_SYN_REPORT_MODE FEE_ECC_SYN_REPORT_MODE FEE_ECC_MALFUNCTION_MODE1 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FEE_ECC_TEST_MODE_BIT FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_BIT FLASH_ECC_TEST_MODE_BIT FLASH_ECC_TEST_MODE_BIT FLASH_ECC_TEST_MODE_BIT FLASH_ECC_TEST_MODE_BIT FLASH_ECC_TEST_MODE_BIT PLASH_ECC_TEST_MODE_BIT NA FLASH_ECC_TEST_MODE_BIT PBIST_EXECUTE NA PBIST_EXECUTE NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	ADC_SELFTEST_ALL	53.475
FEE_ECC_SYN_REPORT_MODE FEE_ECC_MALFUNCTION_MODE1 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_IBIT 12.5375 FEE_ECC_TEST_MODE_IBIT_FAULT_INJECT NA FEE_ECC_TEST_MODE_ZBIT 11.725 FEE_ECC_TEST_MODE_ZBIT_FAULT_INJECT NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_PARITY_SELF_TEST PLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ECC_TEST_MODE_IBIT 11.55 FLASH_ECC_TEST_MODE_ZBIT NA FLASH_ECC_TEST_MODE_ZBIT A18.3 FLASH_ECC_TEST_MODE_ZBIT PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	ADC_SRAM_PARITY_TEST	7.175
FEE_ECC_MALFUNCTION_MODE1 FEE_ECC_MALFUNCTION_MODE2 10.8375 FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_IBIT 12.5375 FEE_ECC_TEST_MODE_IBIT FEE_ECC_TEST_MODE_IBIT FEE_ECC_TEST_MODE_IBIT FEE_ECC_TEST_MODE_IBIT 11.725 FEE_ECC_TEST_MODE_ZBIT FEE_ECC_TEST_MODE_ZBIT NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_SELF_ITEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_IBIT FLASH_ECC_TEST_MODE_IBIT FLASH_ECC_TEST_MODE_ZBIT FLASH_ECC_TEST_MODE_ZBIT PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	FEE_ECC_DATA_CORR_MODE	11.3375
FEE_ECC_MALFUNCTION_MODE2 FEE_ECC_ADDR_TAG_REG_MODE NA FEE_ECC_TEST_MODE_1BIT FEE_ECC_TEST_MODE_1BIT FEE_ECC_TEST_MODE_1BIT_FAULT_INJECT NA FEE_ECC_TEST_MODE_2BIT FEE_ECC_TEST_MODE_2BIT FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	FEE_ECC_SYN_REPORT_MODE	7.925
FEE_ECC_ADDR_TAG_REG_MODE FEE_ECC_TEST_MODE_1BIT FEE_ECC_TEST_MODE_1BIT_FAULT_INJECT NA FEE_ECC_TEST_MODE_2BIT FEE_ECC_TEST_MODE_2BIT FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	FEE_ECC_MALFUNCTION_MODE1	8.0375
FEE_ECC_TEST_MODE_1BIT 12.5375 FEE_ECC_TEST_MODE_1BIT_FAULT_INJECT NA FEE_ECC_TEST_MODE_2BIT 11.725 FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST 218.8375 FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT 11.55 FLASH_ECC_TEST_MODE_2BIT 418.3 FLASH_HARD_ERROR_LIVELOCK NA FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT 3.275 PBIST_EXECUTE NA PBIST_EXECUTE NA STC_RUN NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FEE_ECC_MALFUNCTION_MODE2	10.8375
FEE_ECC_TEST_MODE_1BIT_FAULT_INJECT FEE_ECC_TEST_MODE_2BIT FEE_ECC_TEST_MODE_2BIT FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE NA STC_RUN NA STC_RUN NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	FEE_ECC_ADDR_TAG_REG_MODE	NA
FEE_ECC_TEST_MODE_2BIT 11.725 FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT NA FLASH_ECC_ADDR_TAG_REG_MODE NA FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT NA FLASH_ADDRESS_PARITY_SELF_TEST 218.8375 FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT 11.55 FLASH_ECC_TEST_MODE_2BIT 418.3 FLASH_HARD_ERROR_LIVELOCK NA FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT 3.275 PBIST_EXECUTE NA PBIST_EXECUTE NA STC_RUN NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FEE_ECC_TEST_MODE_1BIT	12.5375
FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT FLASH_ECC_ADDR_TAG_REG_MODE FLASH_ADDRESS_ECC_SELF_TEST NA FLASH_ADDRESS_ECC_FAULT_INJECT FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	FEE_ECC_TEST_MODE_1BIT_FAULT_INJECT	NA
FLASH_ECC_ADDR_TAG_REG_MODE FLASH_ADDRESS_ECC_SELF_TEST FLASH_ADDRESS_ECC_FAULT_INJECT FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_HARD_ERROR_LIVELOCK FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FEE_ECC_TEST_MODE_2BIT	11.725
FLASH_ADDRESS_ECC_SELF_TEST FLASH_ADDRESS_ECC_FAULT_INJECT FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_HARD_ERROR_LIVELOCK FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING	FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT	NA
FLASH_ADDRESS_ECC_FAULT_INJECT FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_HARD_ERROR_LIVELOCK FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE_OVERRIDE NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA	FLASH_ECC_ADDR_TAG_REG_MODE	NA
FLASH_ADDRESS_PARITY_SELF_TEST FLASH_ADDRESS_PARITY_FAULT_INJECT NA FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_HARD_ERROR_LIVELOCK NA FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE NA STC_RUN STC_RUN STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FLASH_ADDRESS_ECC_SELF_TEST	NA
FLASH_ADDRESS_PARITY_FAULT_INJECT FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_ECC_TEST_MODE_2BIT FLASH_HARD_ERROR_LIVELOCK NA FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE NA PBIST_EXECUTE_OVERRIDE NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FLASH_ADDRESS_ECC_FAULT_INJECT	NA
FLASH_ECC_TEST_MODE_1BIT FLASH_ECC_TEST_MODE_2BIT FLASH_HARD_ERROR_LIVELOCK FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE PBIST_EXECUTE NA PBIST_EXECUTE_OVERRIDE NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING PSCON_ERROR_FORCING PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA	FLASH_ADDRESS_PARITY_SELF_TEST	218.8375
FLASH_ECC_TEST_MODE_2BIT 418.3 FLASH_HARD_ERROR_LIVELOCK NA FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT 3.275 PBIST_EXECUTE NA PBIST_EXECUTE_OVERRIDE NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FLASH_ADDRESS_PARITY_FAULT_INJECT	NA
FLASH_ECC_TEST_MODE_2BIT 418.3 FLASH_HARD_ERROR_LIVELOCK NA FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT 3.275 PBIST_EXECUTE NA PBIST_EXECUTE_OVERRIDE NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FLASH ECC TEST MODE 1BIT	11.55
FLASH_HARD_ERROR_LIVELOCK FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE NA STC_RUN STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING PSCON_ERROR_FORCING PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FLASH ECC TEST MODE 2BIT	418.3
FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT PBIST_EXECUTE NA PBIST_EXECUTE_OVERRIDE NA STC_RUN STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING PSCON_ERROR_FORCING PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA	FLASH HARD ERROR LIVELOCK	NA
PBIST_EXECUTE NA PBIST_EXECUTE_OVERRIDE NA STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING FAULT_INJECT NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	FLASH ECC TEST MODE 2BIT FAULT INJECT	3.275
STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING_FAULT_INJECT NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA		NA
STC_RUN NA STC_COMPARE_SELFCHECK NA PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING_FAULT_INJECT NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING NA	PBIST EXECUTE OVERRIDE	NA
STC_COMPARE_SELFCHECK PSCON_SELF_TEST NA PSCON_ERROR_FORCING PSCON_ERROR_FORCING_FAULT_INJECT PSCON_SELF_TEST_ERROR_FORCING PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA		
PSCON_SELF_TEST NA PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING_FAULT_INJECT NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA	-	
PSCON_ERROR_FORCING NA PSCON_ERROR_FORCING_FAULT_INJECT NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA		
PSCON_ERROR_FORCING_FAULT_INJECT NA PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA		
PSCON_SELF_TEST_ERROR_FORCING NA PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA		
PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT NA		



EFUSE_SELF_TEST_AUTOLOAD	NA
EFUSE_SELF_TEST_ECC	7.0125
EFUSE_SELF_TEST_STUCK_AT_ZERO	209.725
CCMR4F_SELF_TEST	NA
CCMR4F_ERROR_FORCING_TEST	NA
CCMR4F_ERROR_FORCING_TEST_FAULT_INJECT	NA
CCMR4F_SELF_TEST_ERROR_FORCING	NA
L2INTERCONNECT_RESERVED_ACCESS	7.3625
L2INTERCONNECT_UNPRIVELEGED_ACCESS	NA
L3INTERCONNECT_RESERVED_ACCESS	6.975
L3INTERCONNECT_UNPRIVELEGED_ACCESS	NA
VIM_SRAM_PARITY_TEST	9.5125
VIM_SOFTWARE_TEST	419.95
HET_SRAM_PARITY_TEST	10.2
HET_ANALOG_LOOPBACK_TEST	NA
HTU_SRAM_PARITY_TEST	9.475
MIBSPI_SRAM_PARITY_TEST	11.875
MIBSPI_ANALOG_LOOPBACK_TEST	33
SPI_ANALOG_LOOPBACK_TEST	24.1875
CAN_SRAM_PARITY_TEST	12.925
CAN_ANALOG_LOOPBACK_TEST	NA
GIO_ANALOG_LOOPBACK_TEST	5.0625
SCI_ANALOG_LOOPBACK_TEST	NA



Profiling information for PBIST algorithms.

RAM Group	PBIST Algorithm	Profiling measurement (us)
DDICT DAMCDOUR OF DRICT DOM	DDICTALCO TRIDLE DEAD CLOW DEAD	· · ·
PBIST_RAMGROUP_01_PBIST_ROM	PBISTALCO_TRIPLE_READ_SLOW_READ	239.2727273
PBIST_RAMGROUP_01_PBIST_ROM	PBISTALGO_TRIPLE_READ_FAST_READ	90.53181818
PBIST_RAMGROUP_02_STC_ROM	PBISTALGO_TRIPLE_READ_SLOW_READ	193.9863636
PBIST_RAMGROUP_02_STC_ROM	PBISTALGO_TRIPLE_READ_FAST_READ	75.06818182
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_MARCH13N_RED_2PORT	304.6590909
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_DOWN1A_RED_2PORT	80.75909091
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_MAPCOLUMN_2PORT	94.61363636
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_PRECHARGE_2PORT	72.85909091
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_DTXN2_2PORT	106.4772727
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_FLIP10_2PORT	66.14090909
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_IDDQ_2PORT	27.77727273
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_RETENTION_2PORT	27.77727273
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_IDDQ2_2PORT	27.77727273
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_RETENTION2_2PORT	27.77727273
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_IDDQROWSTRIPE_2PORT	28.17727273
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_IDDQROWSTRIPE2_2PORT	28.17727273
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_MARCH13N_RED_2PORT	303.0090909
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_DOWN1A_RED_2PORT	79.58636364
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_MAPCOLUMN_2PORT	94.61363636
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_PRECHARGE_2PORT	72.85909091
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_DTXN2_2PORT	106.4772727
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_PMOS_OPEN_2PORT	246.5590909
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_FLIP10_2PORT	66.14090909
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_IDDQ_2PORT	27.77727273
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_RETENTION_2PORT	27.77727273
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_IDDQ2_2PORT	27.77727273
PBIST RAMGROUP 04 DCAN2	PBISTALGO RETENTION2 2PORT	27.77727273
PBIST RAMGROUP 04 DCAN2	PBISTALGO IDDQROWSTRIPE 2PORT	28.17727273
PBIST_RAMGROUP_04_DCAN2	PBISTALGO IDDQROWSTRIPE2 2PORT	28.17727273
PBIST RAMGROUP 05 DCAN3	PBISTALGO MARCH13N RED 2PORT	303.0090909
PBIST RAMGROUP 05 DCAN3	PBISTALGO DOWN1A RED 2PORT	79.58636364
PBIST RAMGROUP 05 DCAN3	PBISTALGO MAPCOLUMN 2PORT	94.61363636
PBIST RAMGROUP 05 DCAN3	PBISTALGO_PRECHARGE_2PORT	72.85909091
PBIST_RAMGROUP_05_DCAN3	PBISTALGO DTXN2 2PORT	106.4772727
PBIST RAMGROUP 05 DCAN3	PBISTALGO_PMOS_OPEN_2PORT	246.5590909
PBIST RAMGROUP 05 DCAN3	PBISTALGO FLIP10 2PORT	66.14090909
PBIST RAMGROUP 05 DCAN3	PBISTALGO IDDQ 2PORT	27.77727273
PBIST RAMGROUP 05 DCAN3	PBISTALGO_RETENTION_2PORT	27.77727273
PBIST RAMGROUP 05 DCAN3	PBISTALGO IDDQ2 2PORT	27.77727273
PBIST RAMGROUP 05 DCAN3	PBISTALGO RETENTION2 2PORT	27.77727273
PBIST RAMGROUP 05 DCAN3	PBISTALGO IDDQROWSTRIPE 2PORT	28.17727273
PBIST_RAMGROUP_05_DCAN3	PBISTALGO IDDQROWSTRIPE2 2PORT	28.17727273
PBIST RAMGROUP 07 MIBSPI1	PBISTALGO_IDDQROWSTRIFL2_2FORT PBISTALGO MARCH13N RED 2PORT	361.1454545
		86.30454545
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_DOWN1A_RED_2PORT	80.30434343



PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_MAPCOLUMN_2PORT	106.8681818
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_PRECHARGE_2PORT	78.00454545
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_DTXN2_2PORT	160.2590909
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_PMOS_OPEN_2PORT	346.6
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_FLIP10_2PORT	76.81363636
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQ_2PORT	25.40454545
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_RETENTION_2PORT	25.80454545
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQ2_2PORT	25.40454545
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_RETENTION2_2PORT	25.80454545
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQROWSTRIPE_2PORT	25.80454545
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQROWSTRIPE2_2PORT	25.80454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_MARCH13N_RED_2PORT	361.1454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_DOWN1A_RED_2PORT	86.30454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_MAPCOLUMN_2PORT	106.8681818
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_PRECHARGE_2PORT	78.00454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_DTXN2_2PORT	160.2590909
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_PMOS_OPEN_2PORT	346.6045455
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_FLIP10_2PORT	76.81363636
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQ_2PORT	25.40454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_RETENTION_2PORT	25.80454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQ2_2PORT	25.40454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_RETENTION2_2PORT	25.80454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQROWSTRIPE_2PORT	25.80454545
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQROWSTRIPE2_2PORT	25.80454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_MARCH13N_RED_2PORT	361.1454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_DOWN1A_RED_2PORT	86.30454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_MAPCOLUMN_2PORT	106.8681818
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_PRECHARGE_2PORT	78.00454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_DTXN2_2PORT	160.2590909
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_PMOS_OPEN_2PORT	346.6045455
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_FLIP10_2PORT	76.81363636
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQ_2PORT	25.40454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_RETENTION_2PORT	25.80454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQ2_2PORT	25.40454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_RETENTION2_2PORT	25.80454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQROWSTRIPE_2PORT	25.80454545
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQROWSTRIPE2_2PORT	25.80454545
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_MARCH13N_RED_2PORT	70.09090909
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_DOWN1A_RED_2PORT	26.98636364
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_MAPCOLUMN_2PORT	29.75909091
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_PRECHARGE_2PORT	25.80454545
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_DTXN2_2PORT	36.08636364
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_PMOS_OPEN_2PORT	54.75909091
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_FLIP10_2PORT	22.64090909
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_IDDQ_2PORT	16.31363636
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_RETENTION_2PORT	16.31363636
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_IDDQ2_2PORT	16.31363636
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_RETENTION2_2PORT	16.31363636
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_IDDQROWSTRIPE_2PORT	16.31363636
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO IDDQROWSTRIPE2 2PORT	16.31363636



PBIST_RAMGROUP_12_DMA	PBISTALGO_MARCH13N_RED_2PORT	150.7090909
PBIST_RAMGROUP_12_DMA	PBISTALGO_DOWN1A_RED_2PORT	49.07727273
PBIST_RAMGROUP_12_DMA	PBISTALGO_MAPCOLUMN_2PORT	55.01363636
PBIST_RAMGROUP_12_DMA	PBISTALGO_PRECHARGE_2PORT	46.31363636
PBIST_RAMGROUP_12_DMA	PBISTALGO_DTXN2_2PORT	58.96818182
PBIST_RAMGROUP_12_DMA	PBISTALGO_PMOS_OPEN_SLICE1_2POR	78.40909091
PBIST_RAMGROUP_12_DMA	PBISTALGO_PMOS_OPEN_SLICE2_1POR	41.26818182
PBIST_RAMGROUP_12_DMA	PBISTALGO_FLIP10_2PORT	38.00454545
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQ_2PORT	23.37727273
PBIST_RAMGROUP_12_DMA	PBISTALGO_RETENTION_2PORT	23.76818182
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQ2_2PORT	23.37727273
PBIST_RAMGROUP_12_DMA	PBISTALGO_RETENTION2_2PORT	23.76818182
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQROWSTRIPE_2PORT	23.76818182
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQROWSTRIPE2_2PORT	23.76818182
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_MARCH13N_RED_2PORT	412.9454545
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_DOWN1A_RED_2PORT	111.2227273
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_MAPCOLUMN_2PORT	131.3863636
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PRECHARGE_2PORT	102.9136364
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_DTXN2_2PORT	144.4409091
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PMOS_OPEN_SLICE1_2POR	239.1227273
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PMOS_OPEN_SLICE2_1POR	71.03181818
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_FLIP10_2PORT	87.09545455
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQ_2PORT	38.45909091
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_RETENTION_2PORT	38.85
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQ2_2PORT	38.45909091
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_RETENTION2_2PORT	38.85
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQROWSTRIPE_2PORT	39.25
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQROWSTRIPE2_2PORT	39.25
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_MARCH13N_RED_2PORT	132.9636364
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_DOWN1A_RED_2PORT	47.95
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_MAPCOLUMN_2PORT	52.69545455
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_PRECHARGE_2PORT	46.36818182
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_DTXN2_2PORT	54.27727273
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_PMOS_OPEN_2PORT	75.32272727
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_FLIP10_2PORT	34.89545455
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_IDDQ_2PORT	25.01363636
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_RETENTION_2PORT	25.40454545
PBIST_RAMGROUP_14_HETTU1	PBISTALGO IDDQ2 2PORT	25.01363636
PBIST_RAMGROUP_14_HETTU1	FBISTALGO_IDDQ2_2FORT	23.01303030
DOIST DAMAGRAUS ::::==:::	PBISTALGO_RETENTION2_2PORT	25.40454545
PBIST_RAMGROUP_14_HETTU1	- '-	
PBIST_RAMGROUP_14_HETTU1 PBIST_RAMGROUP_14_HETTU1	PBISTALGO_RETENTION2_2PORT	25.40454545
	PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT	25.40454545 25.40454545
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT	25.40454545 25.40454545 25.40454545
PBIST_RAMGROUP_14_HETTU1 PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT	25.40454545 25.40454545 25.40454545 70.09090909
PBIST_RAMGROUP_14_HETTU1 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT	25.40454545 25.40454545 25.40454545 70.09090909 26.98636364
PBIST_RAMGROUP_14_HETTU1 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT	25.40454545 25.40454545 25.40454545 70.09090909 26.98636364 29.75909091
PBIST_RAMGROUP_14_HETTU1 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT	25.40454545 25.40454545 25.40454545 70.09090909 26.98636364 29.75909091 25.80454545
PBIST_RAMGROUP_14_HETTU1 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT	25.40454545 25.40454545 25.40454545 70.09090909 26.98636364 29.75909091 25.80454545 36.08636364
PBIST_RAMGROUP_14_HETTU1 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2 PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_2PORT	25.40454545 25.40454545 25.40454545 70.09090909 26.98636364 29.75909091 25.80454545 36.08636364 54.75909091



PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQ2_2PORT	16.31363636
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT	16.31363636
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQROWSTRIPE_2PORT	16.31363636
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQROWSTRIPE2_2PORT	16.31363636
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_MARCH13N_RED_2PORT	412.9454545
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_DOWN1A_RED_2PORT	111.2227273
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_MAPCOLUMN_2PORT	131.3863636
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PRECHARGE_2PORT	102.9136364
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_DTXN2_2PORT	144.4409091
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PMOS_OPEN_SLICE1_2POR	239.1227273
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PMOS_OPEN_SLICE2_1POR	71.03181818
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_FLIP10_2PORT	87.09545455
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQ_2PORT	38.45909091
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_RETENTION_2PORT	38.85
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQ2_2PORT	38.45909091
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_RETENTION2_2PORT	38.85
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQROWSTRIPE_2PORT	39.25
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQROWSTRIPE2_2PORT	39.25
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_MARCH13N_RED_2PORT	132.9636364
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_DOWN1A_RED_2PORT	47.95
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_MAPCOLUMN_2PORT	52.69545455
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_PRECHARGE_2PORT	46.36818182
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_DTXN2_2PORT	54.27727273
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_PMOS_OPEN_2PORT	75.32272727
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_FLIP10_2PORT	34.89545455
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_IDDQ_2PORT	25.01363636
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_RETENTION_2PORT	25.40454545
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_IDDQ2_2PORT	25.01363636
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_RETENTION2_2PORT	25.40454545
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_IDDQROWSTRIPE_2PORT	25.40454545
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_IDDQROWSTRIPE2_2PORT	25.40454545
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_MARCH13N_RED_2PORT	125.8272727
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_DOWN1A_RED_2PORT	40.41363636
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_MAPCOLUMN_2PORT	45.95
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_PRECHARGE_2PORT	38.44090909
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_DTXN2_2PORT	49.90454545
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_PMOS_OPEN_SLICE1_2POR	71.40454545
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_PMOS_OPEN_SLICE2_1POR	31.92272727
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_FLIP10_2PORT	33.29545455
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_IDDQ_2PORT	19.85
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT	19.85
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_IDDQ2_2PORT	19.85
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION2_2PORT	19.85
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_IDDQROWSTRIPE_2PORT	20.25
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_IDDQROWSTRIPE2_2PORT	20.25
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_MARCH13N_RED_2PORT	104.4727273
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_DOWN1A_RED_2PORT	36.45909091
PBIST_RAMGROUP_24_ETHERNET2		
I DIST TO CONTROL 24 ETTIER TELEVINETZ	PBISTALGO MAPCOLUMN 2PORT	41.60454545
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT	35.27727273



PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_PMOS_OPEN_2PORT	71.35
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_FLIP10_2PORT	29.34090909
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQ_2PORT	19.45909091
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_RETENTION_2PORT	19.85
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQ2_2PORT	19.45909091
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_RETENTION2_2PORT	19.85
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQROWSTRIPE_2PORT	19.85
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQROWSTRIPE2_2PORT	19.85
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_MAPCOLUMN_1PORT	318.5272727
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_PRECHARGE_1PORT	206.3363636
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_DTXN2_1PORT	838.8545455
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_PMOS_OPEN_1PORT	1918.645455
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_FLIP10_1PORT	239.1363636
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_IDDQ_1PORT	34.50909091
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_RETENTION_1PORT	34.50909091
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_IDDQ2_1PORT	34.50909091
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_RETENTION2_1PORT	34.50909091
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_IDDQROWSTRIPE_1PORT	34.50909091
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_IDDQROWSTRIPE2_1PORT	34.50909091



Profiling data for SafeTI™ Hercules™ Diagnostic Library API.

Notes: Measurements were made on an RM46 and TMS570LS12 device, with HCLK = 220 MHz.

Some tests do not generate an ESM event, and hence the time to handler is not available. Fault injection timings depend on the handling done by application, and hence is not profiled. Please refer to the API user's guide for details on fault injection.

Timings are indicative, appropriate care should be taken by application when using the data provided here depending on application scenario.

SafeTI Diagnostic Library Test	Timing Measurements (us)
SRAM_ECC_ERROR_FORCING_1BIT	6.990909091
SRAM_ECC_1BIT_FAULT_INJECTION	NA
SRAM_ECC_ERROR_FORCING_2BIT	155.4590909
SRAM_ECC_2BIT_FAULT_INJECT	NA
SRAM_PAR_ADDR_CTRL_SELF_TEST	158.1863636
SRAM_ECC_ERROR_PROFILING	NA
SRAM_ECC_ERROR_PROFILING_FAULT_INJECT	NA
SRAM_RADECODE_DIAGNOSTICS	302.1909091
SRAM_LIVELOCK_DIAGNOSTICS	NA
ADC_SELFTEST_ALL	36.53181818
ADC_SRAM_PARITY_TEST	2.940909091
FEE_ECC_DATA_CORR_MODE	4.831818182
FEE_ECC_SYN_REPORT_MODE	3.459090909
FEE_ECC_MALFUNCTION_MODE1	3.522727273
FEE_ECC_MALFUNCTION_MODE2	4.681818182
FEE_ECC_ADDR_TAG_REG_MODE	NA
FEE_ECC_TEST_MODE_1BIT	5.227272727
FEE_ECC_TEST_MODE_1BIT_FAULT_INJECT	NA
FEE_ECC_TEST_MODE_2BIT	5.081818182
FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT	NA
FLASH_ECC_ADDR_TAG_REG_MODE	NA
FLASH_ADDRESS_ECC_SELF_TEST	NA
FLASH_ADDRESS_ECC_FAULT_INJECT	NA
FLASH_ADDRESS_PARITY_SELF_TEST	155.5590909
FLASH_ADDRESS_PARITY_FAULT_INJECT	NA
FLASH_ECC_TEST_MODE_1BIT	4.8818182
FLASH_ECC_TEST_MODE_2BIT	301.6227273
FLASH_HARD_ERROR_LIVELOCK	NA
FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT	1.5
PBIST_EXECUTE	NA
PBIST_EXECUTE_OVERRIDE	NA
STC_RUN	NA
STC_COMPARE_SELFCHECK	NA
PSCON_SELF_TEST	1.85454545
PSCON_ERROR_FORCING	2.263636364
PSCON_ERROR_FORCING_FAULT_INJECT	4.718181818
PSCON_SELF_TEST_ERROR_FORCING	2.186363636
PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT	NA
PSCON_PMA_TEST	NA



TEXAS IT INTO THAT ION Copyright © 2016 Texas Instruments Incorporated. All rights reserved.

	1
EFUSE_SELF_TEST_AUTOLOAD	NA
EFUSE_SELF_TEST_ECC	5.236363636
EFUSE_SELF_TEST_STUCK_AT_ZERO	151.1318182
CCMR4F_SELF_TEST	NA
CCMR4F_ERROR_FORCING_TEST	NA
CCMR4F_ERROR_FORCING_TEST_FAULT_INJECT	NA
CCMR4F_SELF_TEST_ERROR_FORCING	NA
L2INTERCONNECT_RESERVED_ACCESS	3.640909091
L2INTERCONNECT_UNPRIVELEGED_ACCESS	NA
L3INTERCONNECT_RESERVED_ACCESS	3.781818182
L3INTERCONNECT_UNPRIVELEGED_ACCESS	NA
VIM_SRAM_PARITY_TEST	4.190909091
VIM_SOFTWARE_TEST	302.6363636
DMA_SRAM_PARITY_TEST	4.536363636
DMA_SOFTWARE_TEST	34.12727273
HET_SRAM_PARITY_TEST	4.9181818
HET_ANALOG_LOOPBACK_TEST	41.40454545
HTU_SRAM_PARITY_TEST	4.145454545
MIBSPI_SRAM_PARITY_TEST	5.309090909
MIBSPI_ANALOG_LOOPBACK_TEST	23.31363636
SPI_ANALOG_LOOPBACK_TEST	19.63636364
CAN_SRAM_PARITY_TEST	5.522727273
CAN_ANALOG_LOOPBACK_TEST	NA
GIO_ANALOG_LOOPBACK_TEST	2.404545455
SCI_ANALOG_LOOPBACK_TEST	1091.240909

TEXAS IT INTO THATION COPYRIGHT © 2016 Texas Instruments Incorporated. All rights reserved. TI Information

Profiling information for PBIST algorithms.

RAM Group	PBIST Algorithm	Profiling measurement (us)
PBIST_RAMGROUP_01_PBIST_ROM	PBISTALGO_TRIPLE_READ_SLOW_READ	240.8136
PBIST_RAMGROUP_01_PBIST_ROM	PBISTALGO TRIPLE READ FAST READ	91.8800
PBIST_RAMGROUP_02_STC_ROM	PBISTALGO TRIPLE READ SLOW READ	195.2591
PBIST RAMGROUP 02 STC ROM	PBISTALGO TRIPLE READ FAST READ	76.5727
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_MARCH13N_RED_2PORT	305.3900
PBIST RAMGROUP 03 DCAN1	PBISTALGO DOWN1A RED 2PORT	81.3386
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_MAPCOLUMN_2PORT	96.3577
PBIST RAMGROUP 03 DCAN1	PBISTALGO PRECHARGE 2PORT	74.1455
PBIST_RAMGROUP_03_DCAN1	PBISTALGO DTXN2 2PORT	108.1873
PBIST RAMGROUP 03 DCAN1	PBISTALGO FLIP10 2PORT	67.6727
PBIST RAMGROUP 03 DCAN1	PBISTALGO IDDQ 2PORT	29.2455
PBIST RAMGROUP 03 DCAN1	PBISTALGO RETENTION 2PORT	29.2600
PBIST RAMGROUP 03 DCAN1	PBISTALGO IDDQ2 2PORT	29.2036
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_RETENTION2_2PORT	29.2455
PBIST RAMGROUP 03 DCAN1	PBISTALGO IDDQROWSTRIPE 2PORT	29.6455
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_IDDQROWSTRIPE2_2PORT	29.6455
PBIST RAMGROUP 04 DCAN2	PBISTALGO MARCH13N RED 2PORT	304.5245
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_DOWN1A_RED_2PORT	81.0273
PBIST RAMGROUP 04 DCAN2	PBISTALGO MAPCOLUMN 2PORT	96.4000
PBIST_RAMGROUP 04 DCAN2	PBISTALGO PRECHARGE 2PORT	74.1455
PBIST RAMGROUP 04 DCAN2	PBISTALGO DTXN2 2PORT	108.1673
PBIST_RAMGROUP 04 DCAN2	PBISTALGO PMOS OPEN 2PORT	247.6591
PBIST RAMGROUP 04 DCAN2	PBISTALGO_FINES_OF EN_2FORT	67.6727
PBIST RAMGROUP 04 DCAN2	PBISTALGO_IDDQ_2PORT	29.2455
PBIST RAMGROUP 04 DCAN2	PBISTALGO RETENTION 2PORT	29.2455
PBIST_RAMGROUP_04_DCAN2	PBISTALGO IDDQ2 2PORT	29.1827
PBIST RAMGROUP 04 DCAN2	PBISTALGO_IDDQ2_2FORT PBISTALGO RETENTION2 2PORT	29.2455
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_RETENTION2_2FORT PBISTALGO IDDQROWSTRIPE 2PORT	29.6455
PBIST RAMGROUP 04 DCAN2	PBISTALGO IDDQROWSTRIPE2 2PORT	29.6600
PBIST RAMGROUP 05 DCAN3	PBISTALGO_IDDQROWSTRIFL2_2FORT PBISTALGO_MARCH13N_RED_2PORT	304.5491
PBIST RAMGROUP 05 DCAN3	PBISTALGO_MARCHISN_RED_2FORT	81.0273
PBIST RAMGROUP 05 DCAN3	PBISTALGO_DOWNTA_KED_ZFOKT PBISTALGO MAPCOLUMN 2PORT	96.4000
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_MAPCOLOMIN_2PORT PBISTALGO PRECHARGE 2PORT	74.1455
		108.1673
PBIST_RAMGROUP_05_DCAN3	PBISTALCO PMOS OPEN 3PORT	247.8345
PBIST_RAMGROUP_05_DCAN3	PBISTALCO FURMS OPEN_2PORT	
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_FLIP10_2PORT	67.6727
PBIST_RAMGROUP_05_DCAN3	PBISTALCO DETENTION ADOPT	29.2455
PBIST_RAMGROUP_05_DCAN3	PBISTALCO IDDO2 2PORT	29.2455
PBIST_RAMGROUP_05_DCAN3	PBISTALCO PETENTIONS SPORT	29.2600
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_RETENTION2_2PORT	29.2455
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_IDDQROWSTRIPE_2PORT	29.6455
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_IDDQROWSTRIPE2_2PORT	29.6455
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_MARCH13N_RED_2PORT	362.7627
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_DOWN1A_RED_2PORT	87.9000
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_MAPCOLUMN_2PORT	108.5364



PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_PRECHARGE_2PORT	79.4168
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_DTXN2_2PORT	161.9505
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_PMOS_OPEN_2PORT	348.1818
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_FLIP10_2PORT	78.1909
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQ_2PORT	26.8182
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_RETENTION_2PORT	16.1727
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQ2_2PORT	26.8286
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_RETENTION2_2PORT	16.1727
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQROWSTRIPE_2PORT	27.2141
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQROWSTRIPE2_2PORT	27.2182
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_MARCH13N_RED_2PORT	362.7555
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_DOWN1A_RED_2PORT	87.9000
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_MAPCOLUMN_2PORT	108.5364
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_PRECHARGE_2PORT	79.4091
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_DTXN2_2PORT	161.9582
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_PMOS_OPEN_2PORT	348.1818
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_FLIP10_2PORT	78.1909
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQ_2PORT	26.8182
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_RETENTION_2PORT	16.1077
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQ2_2PORT	26.8323
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_RETENTION2_2PORT	16.1000
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQROWSTRIPE_2PORT	27.2182
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQROWSTRIPE2_2PORT	27.2182
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_MARCH13N_RED_2PORT	362.7555
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_DOWN1A_RED_2PORT	87.9000
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_MAPCOLUMN_2PORT	108.5364
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_PRECHARGE_2PORT	79.4091
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_DTXN2_2PORT	161.9509
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_PMOS_OPEN_2PORT	348.1818
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_FLIP10_2PORT	78.1909
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQ_2PORT	26.8182
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_RETENTION_2PORT	16.1000
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQ2_2PORT	26.8400
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_RETENTION2_2PORT	16.1000
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQROWSTRIPE_2PORT	27.2182
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQROWSTRIPE2_2PORT	27.2259
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_MARCH13N_RED_2PORT	71.4677
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_DOWN1A_RED_2PORT	28.4364
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_MAPCOLUMN_2PORT	31.2636
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_PRECHARGE_2PORT	27.6273
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_DTXN2_2PORT	37.6709
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_PMOS_OPEN_2PORT	56.2709
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_FLIP10_2PORT	24.3909
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_IDDQ_2PORT	17.9182
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_RETENTION_2PORT	17.9182
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_IDDQ2_2PORT	17.9327
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_RETENTION2_2PORT	17.9182
PBIST RAMGROUP 11 MIBADC1	PBISTALGO IDDQROWSTRIPE 2PORT	17.9182
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_IDDQROWSTRIPE2_2PORT	17.9182
PBIST RAMGROUP 12 DMA	PBISTALGO_MARCH13N_RED_2PORT	152.2245



PBIST_RAMGROUP_12_DMA	PBISTALGO_DOWN1A_RED_2PORT	50.6273
PBIST_RAMGROUP_12_DMA	PBISTALGO_MAPCOLUMN_2PORT	56.2909
PBIST_RAMGROUP_12_DMA	PBISTALGO_PRECHARGE_2PORT	47.8077
PBIST_RAMGROUP_12_DMA	PBISTALGO_DTXN2_2PORT	60.3523
PBIST_RAMGROUP_12_DMA	PBISTALGO_PMOS_OPEN_SLICE1_2POR	79.7500
PBIST_RAMGROUP_12_DMA	PBISTALGO_PMOS_OPEN_SLICE2_1POR	42.5609
PBIST_RAMGROUP_12_DMA	PBISTALGO_FLIP10_2PORT	39.3000
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQ_2PORT	25.1455
PBIST_RAMGROUP_12_DMA	PBISTALGO_RETENTION_2PORT	25.1455
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQ2_2PORT	25.0800
PBIST_RAMGROUP_12_DMA	PBISTALGO_RETENTION2_2PORT	25.1455
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQROWSTRIPE_2PORT	25.1455
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQROWSTRIPE2_2PORT	25.1455
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_MARCH13N_RED_2PORT	414.1300
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_DOWN1A_RED_2PORT	112.5818
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_MAPCOLUMN_2PORT	132.8091
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PRECHARGE_2PORT	104.4909
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_DTXN2_2PORT	146.0564
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PMOS_OPEN_SLICE1_2POR	240.5682
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PMOS_OPEN_SLICE2_1POR	72.5273
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_FLIP10_2PORT	88.3091
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQ_2PORT	40.1636
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_RETENTION_2PORT	40.5805
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQ2_2PORT	40.0977
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_RETENTION2_2PORT	40.5727
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQROWSTRIPE_2PORT	40.9727
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQROWSTRIPE2_2PORT	40.9727
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_MARCH13N_RED_2PORT	134.5918
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_DOWN1A_RED_2PORT	49.4727
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_MAPCOLUMN_2PORT	54.3273
PBIST RAMGROUP 14 HETTU1	PBISTALGO PRECHARGE 2PORT	47.8545
PBIST_RAMGROUP_14_HETTU1	PBISTALGO DTXN2 2PORT	55.8800
PBIST RAMGROUP 14 HETTU1	PBISTALGO PMOS OPEN 2PORT	76.7636
PBIST_RAMGROUP_14_HETTU1	PBISTALGO FLIP10 2PORT	36.5273
PBIST_RAMGROUP_14_HETTU1	PBISTALGO IDDQ 2PORT	26.4091
PBIST RAMGROUP 14 HETTU1	PBISTALGO_RETENTION_2PORT	26.8182
PBIST_RAMGROUP_14_HETTU1	PBISTALGO IDDQ2 2PORT	26.4336
PBIST RAMGROUP 14 HETTU1	PBISTALGO RETENTION2 2PORT	26.8182
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_IDDQROWSTRIPE_2PORT	26.8182
PBIST RAMGROUP 14 HETTU1	PBISTALGO IDDQROWSTRIPE2 2PORT	26.8259
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_MARCH13N_RED_2PORT	71.4677
PBIST RAMGROUP 18 MIBADC2	PBISTALGO DOWN1A RED 2PORT	28.4364
PBIST RAMGROUP 18 MIBADC2	PBISTALGO MAPCOLUMN 2PORT	31.2636
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO PRECHARGE 2PORT	27.6273
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_DTXN2_2PORT	37.6709
PBIST RAMGROUP 18 MIBADC2	PBISTALGO PMOS OPEN 2PORT	56.2545
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_FLIP10_2PORT	24.3909
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO IDDQ 2PORT	17.9182
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO RETENTION 2PORT	17.9182
PBIST RAMGROUP 18 MIBADC2	PBISTALGO IDDQ2 2PORT	17.9327



PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT	17.9182
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQROWSTRIPE_2PORT	17.9182
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQROWSTRIPE2_2PORT	17.9182
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_MARCH13N_RED_2PORT	414.1382
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_DOWN1A_RED_2PORT	112.5818
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_MAPCOLUMN_2PORT	132.8091
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PRECHARGE_2PORT	104.4986
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_DTXN2_2PORT	146.0886
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PMOS_OPEN_SLICE1_2POR	240.5759
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PMOS_OPEN_SLICE2_1POR	72.5432
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_FLIP10_2PORT	88.3091
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQ_2PORT	40.1636
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_RETENTION_2PORT	40.5727
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQ2_2PORT	40.0982
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_RETENTION2_2PORT	40.5727
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQROWSTRIPE_2PORT	40.9727
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQROWSTRIPE2_2PORT	40.9727
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_MARCH13N_RED_2PORT	134.5918
PBIST RAMGROUP 20 HETTU2	PBISTALGO_DOWN1A_RED_2PORT	49.4727
PBIST_RAMGROUP_20_HETTU2	PBISTALGO MAPCOLUMN 2PORT	54.3273
PBIST RAMGROUP 20 HETTU2	PBISTALGO PRECHARGE 2PORT	47.8545
PBIST RAMGROUP 20 HETTU2	PBISTALGO DTXN2 2PORT	55.8473
PBIST RAMGROUP 20 HETTU2	PBISTALGO PMOS OPEN 2PORT	76.7636
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_FLIP10_2PORT	36.5273
PBIST RAMGROUP 20 HETTU2	PBISTALGO IDDQ 2PORT	26.4091
PBIST RAMGROUP 20 HETTU2	PBISTALGO_RETENTION_2PORT	26.8259
PBIST RAMGROUP 20 HETTU2	PBISTALGO IDDQ2 2PORT	26.4250
PBIST RAMGROUP 20 HETTU2	PBISTALGO_RETENTION2_2PORT	26.8182
PBIST RAMGROUP 20 HETTU2	PBISTALGO IDDQROWSTRIPE 2PORT	26.8182
PBIST RAMGROUP 20 HETTU2	PBISTALGO_IDDQROWSTRIPE2_2PORT	26.8182
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO MARCH13N RED 2PORT	129.3300
PBIST RAMGROUP 23 ETHERNET1	PBISTALGO DOWN1A RED 2PORT	43.0000
PBIST RAMGROUP 23 ETHERNET1	PBISTALGO MAPCOLUMN 2PORT	48.2545
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_PRECHARGE_2PORT	40.5727
PBIST RAMGROUP 23 ETHERNET1	PBISTALGO_DTXN2_2PORT	52.2345
PBIST RAMGROUP 23 ETHERNET1	PBISTALGO PMOS OPEN SLICE1 2POR	73.4864
PBIST RAMGROUP 23 ETHERNET1	PBISTALGO PMOS OPEN SLICE2 1POR	34.1000
PBIST RAMGROUP 23 ETHERNET1	PBISTALGO FLIP10 2PORT	35.3091
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_IDDQ_2PORT	21.9636
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO RETENTION 2PORT	21.9636
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_IDDQ2_2PORT	21.8655
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO RETENTION2 2PORT	21.9636
PBIST RAMGROUP 23 ETHERNET1	PBISTALGO_IDDQROWSTRIPE_2PORT	21.9636
PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO IDDQROWSTRIPE2 2PORT	21.9714
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQROWSTRIFE2_2FORT PBISTALGO MARCH13N RED 2PORT	107.9168
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO DOWN1A RED 2PORT	38.9545
PBIST_RAMGROUP 24 ETHERNET2	PBISTALGO_DOWNTA_KLD_ZFORT PBISTALGO MAPCOLUMN 2PORT	43.8091
PBIST_RAMGROUP_24_ETHERNET2 PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_MAPCOLOMIN_2PORT PBISTALGO PRECHARGE 2PORT	37.7364
PBIST_RAMGROUP_24_ETHERNET2 PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_PRECHARGE_2FORT PBISTALGO_DTXN2_2PORT	52.6436
	PBISTALGO_DTAN2_2PORT PBISTALGO PMOS OPEN 2PORT	73.2973
PBIST_RAMGROUP_24_ETHERNET2	LEDISTATOO_LINIOS_OLEIN_SLOKI	/3.43/3



PBISTALGO_FLIP10_2PORT	31.2636
PBISTALGO_IDDQ_2PORT	21.5545
PBISTALGO_RETENTION_2PORT	21.5545
PBISTALGO_IDDQ2_2PORT	21.5709
PBISTALGO_RETENTION2_2PORT	21.5545
PBISTALGO_IDDQROWSTRIPE_2PORT	21.9636
PBISTALGO_IDDQROWSTRIPE2_2PORT	21.9636
PBISTALGO_MAPCOLUMN_1PORT	320.5355
PBISTALGO_PRECHARGE_1PORT	208.0545
PBISTALGO_DTXN2_1PORT	840.3545
PBISTALGO_PMOS_OPEN_1PORT	1920.4986
PBISTALGO_FLIP10_1PORT	240.8341
PBISTALGO_IDDQ_1PORT	35.7182
PBISTALGO_RETENTION_1PORT	36.1182
PBISTALGO_IDDQ2_1PORT	35.7182
PBISTALGO_RETENTION2_1PORT	36.0527
PBISTALGO_IDDQROWSTRIPE_1PORT	36.1182
PBISTALGO_IDDQROWSTRIPE2_1PORT	36.1182
	PBISTALGO_IDDQ_2PORT PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MAPCOLUMN_1PORT PBISTALGO_PRECHARGE_1PORT PBISTALGO_DTXN2_1PORT PBISTALGO_PMOS_OPEN_1PORT PBISTALGO_FLIP10_1PORT PBISTALGO_IDDQ_1PORT PBISTALGO_RETENTION_1PORT PBISTALGO_IDDQ2_1PORT PBISTALGO_IDDQ2_1PORT PBISTALGO_RETENTION2_1PORT PBISTALGO_RETENTION2_1PORT PBISTALGO_IDDQROWSTRIPE_1PORT

TEXAS INSTRUMENTS

TI Information

Profiling data for SafeTI™Diagnostic Library API.

Notes: Measurements were made on an RM48 and TMS570LS3137device, with HCLK = 180 MHz.

Some tests do not generate an ESM event, and hence the time to handler is not available. Fault injection timings depend on the handling done by application, and hence is not profiled. Please refer to the API user's guide for details on fault injection.

Timings are indicative, appropriate care should be taken by application when using the data provided here depending on application scenario.

SafeTI Diagnostic Library Test	Timing Measurements (us)
SRAM_ECC_ERROR_FORCING_1BIT	8.66111111
SRAM_ECC_1BIT_FAULT_INJECTION	NA NA
SRAM ECC ERROR FORCING 2BIT	190.1388889
SRAM_ECC_2BIT_FAULT_INJECT	NA
SRAM_PAR_ADDR_CTRL_SELF_TEST	193.222222
SRAM_ECC_ERROR_PROFILING	NA
SRAM ECC ERROR PROFILING FAULT INJECT	NA NA
SRAM RADECODE DIAGNOSTICS	369.422222
SRAM_LIVELOCK_DIAGNOSTICS	NA
ADC_SELFTEST_ALL	39.4611111
ADC_SRAM_PARITY_TEST	3.70555556
FEE_ECC_DATA_CORR_MODE	5.816666667
	4.21111111
FEE_ECC_SYN_REPORT_MODE	4.33333333
FEE_ECC_MALFUNCTION_MODE1	
FEE_ECC_MALFUNCTION_MODE2	5.72222222
FEE_ECC_ADDR_TAG_REG_MODE	NA C 24444444
FEE_ECC_TEST_MODE_1BIT	6.34444444
FEE_ECC_TEST_MODE_1BIT_FAULT_INJECT	NA
FEE_ECC_TEST_MODE_2BIT	6.32777778
FEE_ECC_TEST_MODE_2BIT_FAULT_INJECT	NA
FLASH_ECC_ADDR_TAG_REG_MODE	NA
FLASH_ADDRESS_ECC_SELF_TEST	NA
FLASH_ADDRESS_ECC_FAULT_INJECT	NA
FLASH_ADDRESS_PARITY_SELF_TEST	189.95
FLASH_ADDRESS_PARITY_FAULT_INJECT	NA
FLASH_ECC_TEST_MODE_1BIT	6.01666667
FLASH_ECC_TEST_MODE_2BIT	368.6222222
FLASH_HARD_ERROR_LIVELOCK	NA
FLASH_ECC_TEST_MODE_2BIT_FAULT_INJECT	1.81666667
PBIST_EXECUTE	NA
PBIST_EXECUTE_OVERRIDE	NA
STC_RUN	NA
STC_COMPARE_SELFCHECK	NA
PSCON_SELF_TEST	2.22222222
PSCON_ERROR_FORCING	2.766666667
PSCON_ERROR_FORCING_FAULT_INJECT	5.96111111
PSCON_SELF_TEST_ERROR_FORCING	2.66111111
PSCON_SELF_TEST_ERROR_FORCING_FAULT_INJECT	NA
PSCON_PMA_TEST	NA



TEXAS IT INTO THAT ION Copyright © 2016 Texas Instruments Incorporated. All rights reserved.

EFUSE_SELF_TEST_AUTOLOAD	NA
EFUSE_SELF_TEST_ECC	6.47222222
EFUSE_SELF_TEST_STUCK_AT_ZERO	184.8277778
CCMR4F_SELF_TEST	NA
CCMR4F_ERROR_FORCING_TEST	NA
CCMR4F_ERROR_FORCING_TEST_FAULT_INJECT	NA
CCMR4F_SELF_TEST_ERROR_FORCING	NA
L2INTERCONNECT_RESERVED_ACCESS	4.63888889
L2INTERCONNECT_UNPRIVELEGED_ACCESS	NA
L3INTERCONNECT_RESERVED_ACCESS	4.69444444
L3INTERCONNECT_UNPRIVELEGED_ACCESS	NA
VIM_SRAM_PARITY_TEST	5.15555556
VIM_SOFTWARE_TEST	369.7333333
DMA_SRAM_PARITY_TEST	5.62777778
DMA_SOFTWARE_TEST	40.9444444
HET_SRAM_PARITY_TEST	5.78888889
HET_ANALOG_LOOPBACK_TEST	50.5666667
HTU_SRAM_PARITY_TEST	5.30555556
MIBSPI_SRAM_PARITY_TEST	6.59444444
MIBSPI_ANALOG_LOOPBACK_TEST	24.7666667
SPI_ANALOG_LOOPBACK_TEST	20.4
CAN_SRAM_PARITY_TEST	6.91111111
CAN_ANALOG_LOOPBACK_TEST	NA
GIO_ANALOG_LOOPBACK_TEST	3.03888889
SCI_ANALOG_LOOPBACK_TEST	1091.377778



TEXAS TI Information — INDA NESTRES. Copyright © 2015 Texas Instruments Incorporated. All rights reserved. TI Information — NDA Restrictions

Profiling information for PBIST algorithms.

RAM Group	PBIST Algorithm	Profiling measurement (us)
PBIST_RAMGROUP_01_PBIST_ROM	PBISTALGO_TRIPLE_READ_SLOW_READ	292.7556
PBIST_RAMGROUP_01_PBIST_ROM	PBISTALGO TRIPLE READ FAST READ	110.4778
PBIST RAMGROUP 02 STC ROM	PBISTALGO TRIPLE READ SLOW READ	237.0333
PBIST RAMGROUP 02 STC ROM	PBISTALGO TRIPLE READ FAST READ	92.0556
PBIST RAMGROUP 03 DCAN1	PBISTALGO_MARCH13N_RED_2PORT	372.5222
PBIST RAMGROUP 03 DCAN1	PBISTALGO DOWN1A RED 2PORT	98.8056
PBIST_RAMGROUP_03_DCAN1	PBISTALGO_MAPCOLUMN_2PORT	115.9778
PBIST RAMGROUP 03 DCAN1	PBISTALGO PRECHARGE 2PORT	88.8667
PBIST RAMGROUP 03 DCAN1	PBISTALGO DTXN2 2PORT	130.6444
PBIST RAMGROUP 03 DCAN1	PBISTALGO FLIP10 2PORT	80.8667
PBIST RAMGROUP 03 DCAN1	PBISTALGO IDDQ 2PORT	33.7556
PBIST RAMGROUP 03 DCAN1	PBISTALGO RETENTION 2PORT	34.2000
PBIST RAMGROUP 03 DCAN1	PBISTALGO IDDQ2 2PORT	33.7556
PBIST RAMGROUP 03 DCAN1	PBISTALGO_RETENTION2_2PORT	34.2000
PBIST RAMGROUP 03 DCAN1	PBISTALGO IDDQROWSTRIPE 2PORT	34.2000
PBIST_RAMGROUP_03_DCAN1	PBISTALGO IDDQROWSTRIPE2 2PORT	34.2000
PBIST RAMGROUP 04 DCAN2	PBISTALGO MARCH13N RED 2PORT	370.6500
PBIST_RAMGROUP 04 DCAN2	PBISTALGO_MARCHISN_RED_2FORT	97.3111
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_DOWNIA_RED_2FORT	115.9778
PBIST RAMGROUP 04 DCAN2	PBISTALGO_IMAFCOLOMIN_ZFORT PBISTALGO PRECHARGE 2PORT	88.8667
PBIST RAMGROUP 04 DCAN2	PBISTALGO_FRECHARGE_ZFORT PBISTALGO DTXN2 2PORT	130.6444
PBIST RAMGROUP 04 DCAN2	PBISTALGO_DTXN2_ZFORT PBISTALGO PMOS OPEN 2PORT	301.4222
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_IDDO_2PORT	80.8667 33.7556
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_IDDQ_2PORT	
PBIST_RAMGROUP_04_DCAN2	PBISTALGO IDDO2 3200T	34.2000
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_IDDQ2_2PORT	33.7556 34.2000
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_RETENTION2_2PORT	
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_IDDQROWSTRIPE_2PORT	34.2000
PBIST_RAMGROUP_04_DCAN2	PBISTALGO_IDDQROWSTRIPE2_2PORT	34.2000
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_MARCH13N_RED_2PORT	370.6500
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_DOWN1A_RED_2PORT	97.3111
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_MAPCOLUMN_2PORT	115.9778
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_PRECHARGE_2PORT	88.8667
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_DTXN2_2PORT	130.6444
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_PMOS_OPEN_2PORT	301.4222
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_FLIP10_2PORT	80.8667
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_IDDQ_2PORT	33.7556
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_RETENTION_2PORT	34.2000
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_IDDQ2_2PORT	33.7556
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_RETENTION2_2PORT	34.2000
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_IDDQROWSTRIPE_2PORT	34.2000
PBIST_RAMGROUP_05_DCAN3	PBISTALGO_IDDQROWSTRIPE2_2PORT	34.2000
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_MARCH13N_RED_2PORT	441.7556
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_DOWN1A_RED_2PORT	105.7556
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_MAPCOLUMN_2PORT	131.0889



TEXAS TI Information — INDA RESULCTIONS INSTRUMENTS Copyright © 2015 Texas Instruments Incorporated. All rights reserved. TI Information — NDA Restrictions

PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_PRECHARGE_2PORT	95.0889
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_DTXN2_2PORT	195.9778
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_PMOS_OPEN_2PORT	423.6444
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_FLIP10_2PORT	93.7556
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQ_2PORT	31.0889
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_RETENTION_2PORT	31.5333
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQ2_2PORT	31.0889
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_RETENTION2_2PORT	31.5333
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQROWSTRIPE_2PORT	31.5333
PBIST_RAMGROUP_07_MIBSPI1	PBISTALGO_IDDQROWSTRIPE2_2PORT	31.5333
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_MARCH13N_RED_2PORT	441.7556
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_DOWN1A_RED_2PORT	105.7556
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_MAPCOLUMN_2PORT	131.0889
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_PRECHARGE_2PORT	95.0889
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_DTXN2_2PORT	195.9778
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_PMOS_OPEN_2PORT	423.6500
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_FLIP10_2PORT	93.7556
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQ_2PORT	31.0889
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_RETENTION_2PORT	31.5333
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQ2_2PORT	31.0889
PBIST RAMGROUP 08 MIBSPI3	PBISTALGO RETENTION2 2PORT	31.5333
PBIST_RAMGROUP_08_MIBSPI3	PBISTALGO_IDDQROWSTRIPE_2PORT	31.5333
PBIST RAMGROUP 08 MIBSPI3	PBISTALGO IDDQROWSTRIPE2 2PORT	31.5333
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_MARCH13N_RED_2PORT	441.7556
PBIST RAMGROUP 09 MIBSPI5	PBISTALGO DOWN1A RED 2PORT	105.7556
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_MAPCOLUMN_2PORT	131.0889
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_PRECHARGE_2PORT	95.0889
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_DTXN2_2PORT	195.9778
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_PMOS_OPEN_2PORT	423.6500
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_FLIP10_2PORT	93.7556
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQ_2PORT	31.0889
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_RETENTION_2PORT	31.5333
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQ2_2PORT	31.0889
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_RETENTION2_2PORT	31.5333
PBIST_RAMGROUP_09_MIBSPI5	PBISTALGO_IDDQROWSTRIPE_2PORT	31.5333
PBIST RAMGROUP 09 MIBSPI5	PBISTALGO IDDQROWSTRIPE2 2PORT	31.5333
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_MARCH13N_RED_2PORT	85.7556
PBIST RAMGROUP 11 MIBADC1	PBISTALGO DOWN1A RED 2PORT	32.8667
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO MAPCOLUMN 2PORT	36.4222
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO PRECHARGE 2PORT	31.9778
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_DTXN2_2PORT	44.4222
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_PMOS_OPEN_2PORT	67.2056
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_FLIP10_2PORT	27.9778
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_IDDQ_2PORT	19.9778
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_RETENTION_2PORT	19.9778
PBIST RAMGROUP 11 MIBADC1	PBISTALGO IDDQ2 2PORT	19.9778
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO RETENTION2 2PORT	19.9778
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO IDDQROWSTRIPE 2PORT	19.9778
PBIST_RAMGROUP_11_MIBADC1	PBISTALGO_IDDQROWSTRIPE2_2PORT	19.9778
PBIST_RAMGROUP_12_DMA	PBISTALGO_MARCH13N_RED_2PORT	184.3556



TEXAS TI Information — NDA Nestrictions Instruments Incorporated. All rights reserved. TI Information — NDA Restrictions

PBIST_RAMGROUP_12_DMA	PBISTALGO_DOWN1A_RED_2PORT	59.9111
PBIST_RAMGROUP_12_DMA	PBISTALGO_MAPCOLUMN_2PORT	67.0222
PBIST_RAMGROUP_12_DMA	PBISTALGO_PRECHARGE_2PORT	56.8000
PBIST_RAMGROUP_12_DMA	PBISTALGO_DTXN2_2PORT	72.3556
PBIST_RAMGROUP_12_DMA	PBISTALGO_PMOS_OPEN_SLICE1_2POR	95.8333
PBIST_RAMGROUP_12_DMA	PBISTALGO_PMOS_OPEN_SLICE2_1POR	50.4444
PBIST_RAMGROUP_12_DMA	PBISTALGO_FLIP10_2PORT	46.5778
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQ_2PORT	28.8000
PBIST_RAMGROUP_12_DMA	PBISTALGO_RETENTION_2PORT	28.8000
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQ2_2PORT	28.8000
PBIST_RAMGROUP_12_DMA	PBISTALGO_RETENTION2_2PORT	28.8000
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQROWSTRIPE_2PORT	28.8000
PBIST_RAMGROUP_12_DMA	PBISTALGO_IDDQROWSTRIPE2_2PORT	28.8000
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_MARCH13N_RED_2PORT	504.8667
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_DOWN1A_RED_2PORT	135.9778
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_MAPCOLUMN_2PORT	160.4222
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PRECHARGE_2PORT	125.7556
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_DTXN2_2PORT	176.8667
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PMOS_OPEN_SLICE1_2POR	292.2667
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_PMOS_OPEN_SLICE2_1POR	86.8222
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_FLIP10_2PORT	106.2000
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQ_2PORT	47.0889
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_RETENTION_2PORT	47.5333
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQ2_2PORT	47.0889
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_RETENTION2_2PORT	47.5333
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQROWSTRIPE_2PORT	47.9778
PBIST_RAMGROUP_13_N2HET1	PBISTALGO_IDDQROWSTRIPE2_2PORT	47.9778
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_MARCH13N_RED_2PORT	162.6444
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_DOWN1A_RED_2PORT	58.6444
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_MAPCOLUMN_2PORT	64.8667
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_PRECHARGE_2PORT	56.8667
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_DTXN2_2PORT	66.6444
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_PMOS_OPEN_2PORT	92.0944
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_FLIP10_2PORT	42.6444
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_IDDQ_2PORT	30.6444
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_RETENTION_2PORT	31.0889
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_IDDQ2_2PORT	30.6444
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_RETENTION2_2PORT	31.0889
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_IDDQROWSTRIPE_2PORT	31.0889
PBIST_RAMGROUP_14_HETTU1	PBISTALGO_IDDQROWSTRIPE2_2PORT	31.0889
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_MARCH13N_RED_2PORT	85.7556
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_DOWN1A_RED_2PORT	32.8667
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_MAPCOLUMN_2PORT	36.4222
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_PRECHARGE_2PORT	31.9778
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_DTXN2_2PORT	44.4222
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_PMOS_OPEN_2PORT	67.2056
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_FLIP10_2PORT	27.9778
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQ_2PORT	19.9778
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION_2PORT	19.9778
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQ2_2PORT	19.9778
	· · · · · · · · · · · · · · · · · · ·	



TEXAS TI Information — NDA Nestrictions Instruments Incorporated. All rights reserved. TI Information — NDA Restrictions

PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_RETENTION2_2PORT	19.9778
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQROWSTRIPE_2PORT	19.9778
PBIST_RAMGROUP_18_MIBADC2	PBISTALGO_IDDQROWSTRIPE2_2PORT	19.9778
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_MARCH13N_RED_2PORT	504.8667
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_DOWN1A_RED_2PORT	135.9778
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_MAPCOLUMN_2PORT	160.4222
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PRECHARGE_2PORT	125.7556
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_DTXN2_2PORT	176.8667
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PMOS_OPEN_SLICE1_2POR	292.2667
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_PMOS_OPEN_SLICE2_1POR	86.8222
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_FLIP10_2PORT	106.2000
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQ_2PORT	47.0889
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_RETENTION_2PORT	47.5333
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQ2_2PORT	47.0889
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_RETENTION2_2PORT	47.5333
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQROWSTRIPE_2PORT	47.9778
PBIST_RAMGROUP_19_N2HET2	PBISTALGO_IDDQROWSTRIPE2_2PORT	47.9778
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_MARCH13N_RED_2PORT	162.6444
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_DOWN1A_RED_2PORT	58.6444
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_MAPCOLUMN_2PORT	64.8667
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_PRECHARGE_2PORT	56.8667
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_DTXN2_2PORT	66.6444
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_PMOS_OPEN_2PORT	92.0944
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_FLIP10_2PORT	42.6444
	DDICTALCO IDDO 2000T	30.6444
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_IDDQ_2PORT	30.0444
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2	PBISTALGO_IDDQ_ZPORT PBISTALGO_RETENTION_2PORT	31.0889
		
PBIST_RAMGROUP_20_HETTU2	PBISTALGO_RETENTION_2PORT	31.0889
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT	31.0889 30.6444
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT	31.0889 30.6444 31.0889
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT	31.0889 30.6444 31.0889 31.0889
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT PBISTALGO_IDDQ_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT PBISTALGO_FLIP10_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_RETENTION_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667 24.8667
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667 24.8667
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667 24.8667 24.8667 24.8667
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_RETENTION2_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667 24.8667 24.8667 24.8667 25.3111
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667 24.8667 24.8667 24.8667 25.3111 25.3111
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667 24.8667 24.8667 24.8667 25.3111 25.3111 130.6444
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_24_ETHERNET2 PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQC_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667 24.8667 24.8667 24.8667 25.3111 25.3111 130.6444 45.7556
PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_20_HETTU2 PBIST_RAMGROUP_23_ETHERNET1 PBIST_RAMGROUP_24_ETHERNET2 PBIST_RAMGROUP_24_ETHERNET2 PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_RETENTION_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT PBISTALGO_PRECHARGE_2PORT PBISTALGO_DTXN2_2PORT PBISTALGO_PMOS_OPEN_SLICE1_2POR PBISTALGO_PMOS_OPEN_SLICE2_1POR PBISTALGO_FLIP10_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_IDDQ2_2PORT PBISTALGO_RETENTION2_2PORT PBISTALGO_IDDQROWSTRIPE_2PORT PBISTALGO_IDDQROWSTRIPE2_2PORT PBISTALGO_MARCH13N_RED_2PORT PBISTALGO_DOWN1A_RED_2PORT PBISTALGO_MAPCOLUMN_2PORT	31.0889 30.6444 31.0889 31.0889 31.0889 156.4222 50.6444 57.3111 47.9778 61.7556 88.2556 40.0000 41.3111 24.8667 24.8667 24.8667 24.8667 25.3111 25.3111 130.6444 45.7556 51.9778



TEXAS TI Information — NDA Restrictions Instruments Incorporated. All rights reserved. TI Information — NDA Restrictions

PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_FLIP10_2PORT	36.4222
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQ_2PORT	24.4222
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_RETENTION_2PORT	24.4222
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQ2_2PORT	24.4222
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_RETENTION2_2PORT	24.4222
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQROWSTRIPE_2PORT	24.8667
PBIST_RAMGROUP_24_ETHERNET2	PBISTALGO_IDDQROWSTRIPE2_2PORT	24.8667
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_MAPCOLUMN_1PORT	389.8389
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_PRECHARGE_1PORT	252.5111
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_DTXN2_1PORT	1025.8444
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_PMOS_OPEN_1PORT	2345.4000
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_FLIP10_1PORT	292.5111
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_IDDQ_1PORT	42.2889
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_RETENTION_1PORT	42.2889
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_IDDQ2_1PORT	42.2889
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_RETENTION2_1PORT	42.2889
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_IDDQROWSTRIPE_1PORT	42.2889
PBIST_RAMGROUP_25_ETHERNET3	PBISTALGO_IDDQROWSTRIPE2_1PORT	42.2889