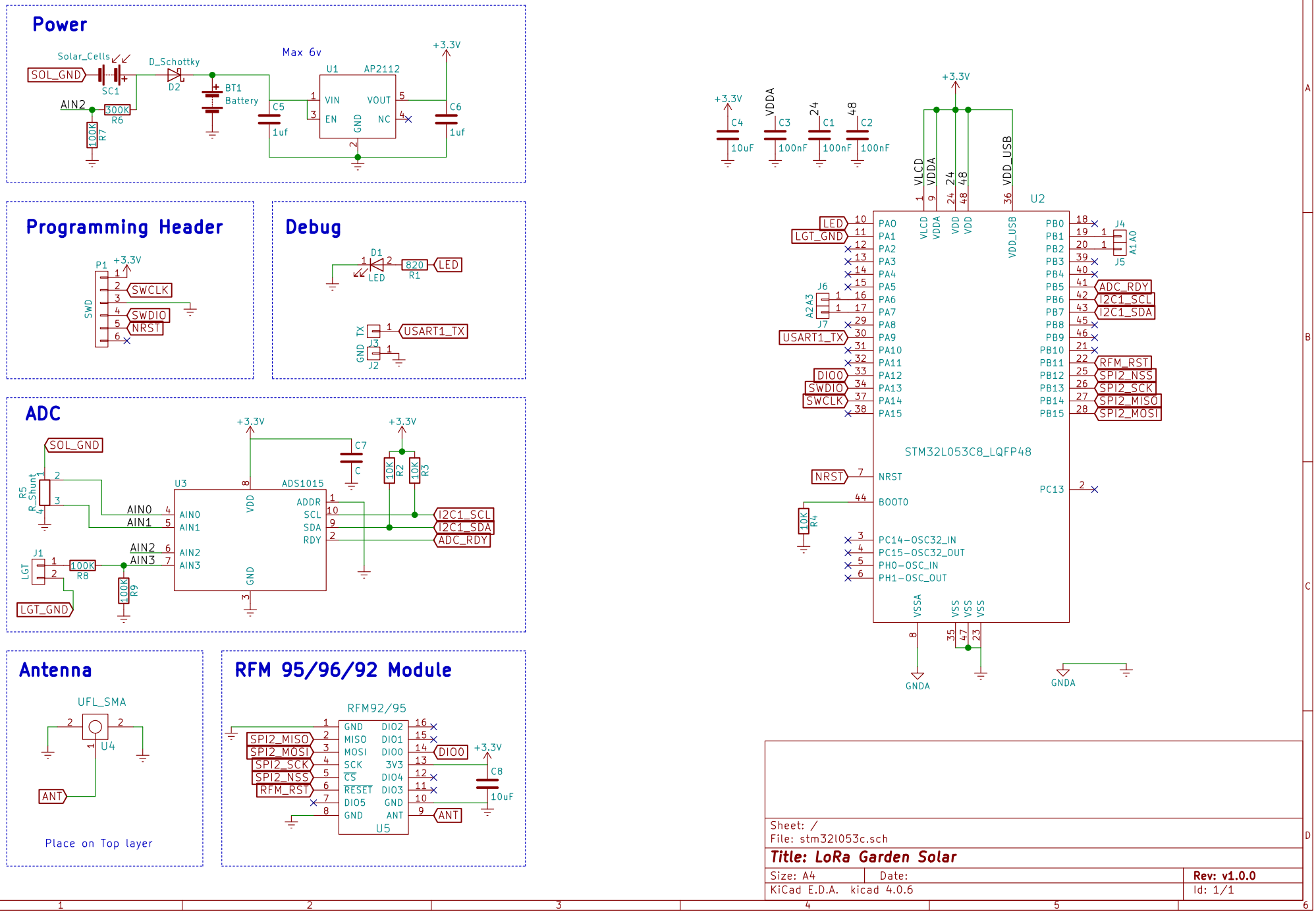
Power
Solar_Cells
SOL_GND
SC1
D_Schottky
D2
BT1
Battery
Max 6v
U1
AP2112
VIN
VOUT
EN
GND
NC
+3.3V
AIN2
300K
R6
100K
R7
C5
1uf
C6
1uf
Programming Header
P1
+3.3V
SWCLK
SWDIO
NRST
SWD
Debug
D1
LED
820
R1
LED
USART1_TX
TX
GND
J3
J2
ADC
SOL_GND
R5
R_Shunt
U3
ADS1015
VDD
GND
ADDR
SCL
SDA
RDY
AIN0
AIN1
AIN2
AIN3
C7
C
10K
R2
10K
R3
I2C1_SCL
I2C1_SDA
ADC_RDY
J1
LGT
100K
R8
100K
R9
LGT_GND
Antenna
UFL_SMA
U4
ANT
Place on Top layer
RFM 95/96/92 Module
RFM92/95
GND
MISO
MOSI
SCK
CS
RESET
DIO5
DIO2
DIO1
DIO0
3V3
DIO4
DIO3
GND
ANT
SPI2_MISO
SPI2_MOSI
SPI2_SCK
SPI2_NSS
RFM_RST
DIO0
+3.3V
C8
10uF
ANT
U5
+3.3V
C4
10uF
VDDA
C3
100nF
24
C1
100nF
48
C2
100nF
VLCD
VDD_USB
U2
LED
LGT_GND
A2A3
J6
J7
USART1_TX
DIO0
SWDIO
SWCLK
PA0
PA1
PA2
PA3
PA4
PA5
PA6
PA7
PA8
PA9
PA10
PA11
PA12
PA13
PA14
PA15
STM32L053C8_LQFP48
NRST
BOOT0
PC14-OSC32_IN
PC15-OSC32_OUT
PH0-OSC_IN
PH1-OSC_OUT
PB0
PB1
PB2
PB3
PB4
PB5
PB6
PB7
PB8
PB9
PB10
PB11
PB12
PB13
PB14
PB15
J4
J5
A1A0
ADC_RDY
I2C1_SCL
I2C1_SDA
RFM_RST
SPI2_NSS
SPI2_SCK
SPI2_MISO
SPI2_MOSI
PC13
10K
R4
VSSA
VSS
GNDA
GNDA
Sheet: /
File: stm32l053c.sch
Title: LoRa Garden Solar
Size: A4
Date:
Rev: v1.0.0
KiCad E.D.A. kicad 4.0.6
Id: 1/1